Figure 1. System Diagram

PCI-6052
DAC
ADC
Counter 0 ➔ Counter 1

PCI-6711
DAC
Counter 1 ➔ Counter 0

Buffer

TTL Out

Neurolog®

Operational Amplifier

LED

Potentiostat

Preparation
Figure 2. Timing Diagram

A. CV Freq (Counter 1 on PCI-6711)

B. Update DAC & ADC Values (Counters 0 & 1 on PCI-6052)

C. Triangle Waveform (DAC on PCI-6052)

D. Trigger for Stimulus generation or TTL (Counter 0 on PCI-6711)

E. Stimulus Waveform (DAC on PCI 6711)