Electronic Supplementary Information

Low Temperature Self-Assembled Synthesis of Hexagonal Plate-Shape Mn₃O₄ 3D Hierarchical Architectures and their Application in Electrochemical Capacitors

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Figure S1. XRD pattern of Mn₃O₄ synthesized at 120°C for 2h (sample S2).

Figure S2. FE-SEM image of Mn₃O₄ hexagonal plates (sample S6) after 4 minutes sonication.
Figure S3. Specific capacitance of Mn$_3$O$_4$ hexagonal plates (sample S6) at different current densities.