Supporting information for

Rapid vapour deposition and in situ melt crystallization for 1-min fabrication of 10-µm-thick crystalline silicon films with a lateral grain size of over 100 µm

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Fig. S1 Temperature measurement of the substrate during the RVD process. Measurement was done with the same time profile of input powers for the tungsten boats and the carbon heater as the standard Si deposition method (Fig. 3) but without putting the Si source material in the boats. Quartz glass substrate with 0.1 μm-thick a-C layer was used (the same substrate structure as Fig. 3), and a Pt-Pt_{0.87}Rh_{0.13} thermocouple was attached to the substrate through a φ=0.3 mm hole made at the centre of the substrate. The substrate temperature \( T_{\text{sub}} \) proved to be 100–150 °C lower than the upper W_{0.95}Re_{0.05}-W_{0.74}Re_{0.26} thermocouple temperature \( T_{\text{TC}} \), and slightly above the melting point of Si \( T_m = 1414 °C \) when starting the RVD process. And \( T_{\text{sub}} \) decreased below \( T_m \) by decreasing the heating power for the upper carbon heater at 5 s.
Thermal linear expansion of Si, SiO₂, and Al₂O₃, calculated using the equations recommended by Touloukian, et al.²⁶ The expansions (%) are:

Si: \[-0.071 + 1.887 \times 10^{-4}(T+273) + 1.934 \times 10^{-7}(T+273)^2 - 4.544 \times 10^{-11}(T+273)^3\]

SiO₂, fused: \[-0.014 + 4.028 \times 10^{-5}(T+273) + 2.733 \times 10^{-8}(T+273)^2 - 1.541 \times 10^{-11}(T+273)^3\]  
\[-0.022 + 8.728 \times 10^{-5}(T+273) - 3.200 \times 10^{-8}(T+273)^2 + 2.347 \times 10^{-12}(T+273)^3\]  
\[-0.015 + 3.968 \times 10^{-5}(T+273) + 4.666 \times 10^{-8}(T+273)^2 - 3.446 \times 10^{-11}(T+273)^3\]

Al₂O₃, polycrystalline: \[-0.180 + 5.494 \times 10^{-4}(T+273) + 2.252 \times 10^{-7}(T+273)^2 - 2.894 \times 10^{-11}(T+273)^3\]

Al₂O₃, a-axis: \[-0.176 + 5.431 \times 10^{-4}(T+273) + 2.150 \times 10^{-7}(T+273)^2 - 2.810 \times 10^{-11}(T+273)^3\]

where \(T\) is temperature (°C).

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