Electronic Supplementary Information for:

Tunable Conduction Type of Solution-Processed Germanium Nanoparticle Based Field Effect Transistors and Their Inverter Integration

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Suppl.Figure 1: Morphological effect of seeding. Same process conditions a) without and b) with seeding (exemplary shown: 700 °C; 0.5 mbar GeH₄ partial pressure in Ar).



Analysis report Ge NP powder

10.000 -10.000

Suppl. Figure 2: Ge NP powder diffractogram (blue) with fit described above (red)

2Theta [°] 

Suppl. Figure 3. The output characteristics of as deposited optimum (1 wt%; 3 deposition steps) layer shows low conductivity levels with minimal gating.



Suppl. Figure 4. Comparison of the FTIR spectra of an as prepared layer and a HP annealed layer (450 °C, 15 min). As the original layers are too thin to deliver spectra with evaluable intensities, very thick layers drop casted from 1 wt% dispersion are utilized. They are sealed under nitrogen in a protective cell equipped with a potassium bromide window. The intensity reduction of the C-O bands at 1042 and 1105 cm⁻¹, the C-O-H band at 1263 cm⁻¹, the δ C-H bands at 1376 and 1456 cm⁻¹ as well as the v C-H bands between 2800 and 3000 cm⁻¹ shows a removal of solvent during hot plate annealing. However, the removal is not complete and cannot be quantified. Adsorbed water is also removed as can be seen at 1653 cm⁻¹ and at the shoulder at ~ 3400 cm⁻¹.

OH-groups (~ 3200 cm⁻¹) are present before and even after annealing and most likely belong to the solvent residues. Most importantly, the surface termination changes from mostly hydrogen-terminated in the as prepared layer (Ge-H at 1991 cm⁻¹) to mostly hydroxyl-terminated in the annealed layer (Ge-OH at ~ 3700 cm⁻¹).^{1–4}



Suppl. Figure 5. Representative SEM images of a) as-prepared and b) HP annealed (450 °C, 15 min) layer on pre-structured substrate.



Suppl. Figure 6. The as-deposited GeNP layers investigated in this study show m values between 3.8 and 5 which determined from the slope of output curves. This proves that device characteristics obey the power-law $I \sim V^m$ with high m values observed for space-charge-limited-currents (SCLC). After annealing m values are well below 2 which indicates less trap density in the layer.



Suppl. Figure 7. The output and transfer characteristics of 5 times deposited (highest PAF value) sample after PMMA encapsulation.



Suppl. Figure 8. The comparison of 3 times and 5 times deposited layers improvements. Although 5 times deposited layer has highest particle coverage on the substrate, best electrical behavior can be seen for 3 times deposited layers (optimum).



Suppl. Figure 9. The transfer characteristics of 5 times deposited sample after ALD.



Suppl. Figure 10. a) Photography and b) schematic of the utilized pre-structured substrates showing source (S), drain (D) and gate (G).

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