

## Supplementary Information

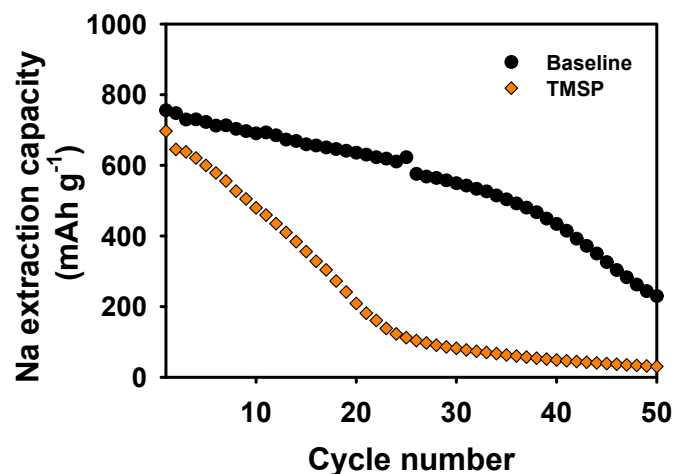
### Interfacial architectures based on binary additive combination for high-performance $\text{Sn}_4\text{P}_3$ anodes in sodium-ion batteries

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**Fig. S1.** Comparison of cycling performance of the  $\text{Sn}_4\text{P}_3$  anodes with and without TMSP additive at a rate of C/10.

**Table S1.** Impedance parameters obtained from the simulation

	$R_e$ ( $\Omega$ )	$R_f$ ( $\Omega$ )	$R_{ct}$ ( $\Omega$ )	Total ( $\Omega$ )
Baseline	0.29	50.5	20	71.79
FEC	0.6	125	49	174.6
FEC+TMSP	0.25	91	14	104.3