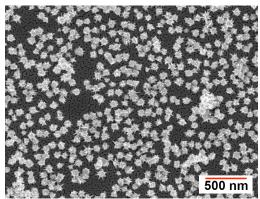
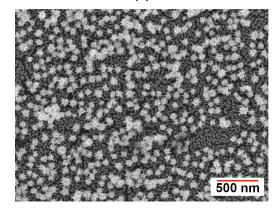
## Towards enhanced optical sensor performance: SEIRA and SERS with plasmonic nanostars

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## **Supporting Information**



(a)



(b)

Figure S1. SEM images of a Si chip with a 5 nm Au layer, the SH-PEG-SH polymer attached on top, and immobilized AuNSts before (a) and after (b) cleaning procedures including washing with acetone, isopropanol, ethanol, and sonication.