

†Electronic Supplementary Information

## **High-Resolution, Electrohydrodynamic Inkjet Printing of Stretchable, Metal Oxide Semiconductor Transistors with High Performances**

S.-Y. Kim,<sup>a</sup> K. Kim,<sup>a</sup> Y.H. Hwang,<sup>b</sup> J. Park,<sup>a</sup> J. Jang,<sup>a</sup> Y. Nam,<sup>c</sup> Y. Kang,<sup>d</sup> M. Kim,<sup>a</sup> H. J. Park,<sup>a</sup> Z. Lee,<sup>a</sup> J. Choi,<sup>d</sup> Y. Kim,<sup>e</sup> S. Jeong,<sup>f</sup> B.-S. Bae<sup>\*c</sup> and J.-U. Park<sup>\*a</sup>

<sup>a</sup> School of Materials Science and Engineering, Ulsan National Institute of Science and Technology (UNIST), Ulsan Metropolitan City, 44919, Republic of Korea.  
Email: jangung@unist.ac.kr

<sup>b</sup> Radiation and Environmental Lab., Central Research Institute, Korea Hydro and Nuclear Power, Daejeon Metropolitan City, 34114, Republic of Korea

<sup>c</sup> Department of Materials Science and Engineering, Korea Advanced Institute of Science and Technology (KAIST), Daejeon Metropolitan City, 34141, Republic of Korea.  
Email: bsbae@kaist.ac.kr

<sup>d</sup> School of Electrical and Computer Engineering, Ulsan National Institute of Science and Technology (UNIST), Ulsan Metropolitan City, 44919, Republic of Korea.

<sup>e</sup> Department of Computer Engineering, Kwangwoon University, Seoul Metropolitan City, 01897, Republic of Korea.

<sup>f</sup> Division of Advanced Materials, Korea Research Institute of Chemical Technology (KRICT), Daejeon Metropolitan City, 34114, Republic of Korea.

## **Experimental Section**

### **Indium oxide (In<sub>2</sub>O<sub>3</sub>) precursor solution synthesis**

An aqueous indium oxide (In<sub>2</sub>O<sub>3</sub>) precursor solution was synthesized by dissolving indium nitrate hydrate (In(NO<sub>3</sub>)<sub>3</sub>·xH<sub>2</sub>O, 99.999% Sigma-Aldrich) in deionized water. The total molar concentration of the In<sub>2</sub>O<sub>3</sub> solutions was 0.2M. The solution was stirred at the ambient atmosphere for 6 hrs.

### **Zr-doped AlO<sub>x</sub> (ZAO) synthesis and fabrication of ZAO dielectric layers**

For the Zr-doped aluminum oxide film (ZAO), aluminum chloride (AlCl<sub>3</sub>, 99.999%, Sigma-Aldrich) and zirconium chloride (ZrCl<sub>4</sub>, 99.99%, Sigma-Aldrich) were dissolved in a mixture of acetonitrile (CH<sub>3</sub>CN, 99.8%, Sigma-Aldrich) and ethylene glycol (HOCH<sub>2</sub>CH<sub>2</sub>OH, 99.8%, Sigma-Aldrich) with a 35/65 volume ratio. The molar concentration of the solutions was 0.4 M. The solution preparation processes were conducted in N<sub>2</sub> condition. As a substrate, a heavily boron-doped silicon wafer (p<sup>+</sup>-type Si) was used after cleaning in an ultrasonic bath. The ZAO precursor solution were spun at 3000 rpm for 25 s and directly annealed at 250 °C in hot plate for 120 min. The spin coating and annealing steps were repeated 4 times to achieve the desired thickness of 155nm. According to our previous reports on *C-f* measurement of the solution-processed high-*k* dielectrics at low-frequency region<sup>[49]</sup>, the dielectric constant of the ZAO layer is measured 21.7 at 100 Hz.

### **Preparation of the 1.8 μm-thick PI substrate**

After poly(methyl methacrylate) (PMMA) which act as a sacrificial layer was spun at 1000 rpm for 30 s on a Si wafer, the PI solution was spun at 3000 rpm for 30 s. Thermal curing process for PI solution was conducted at 250 °C for 4 hrs. After the polyimidization of the PI substrate, the PMMA layer was dissolved by soaking in acetone for 15 min and the remaining PI film was peeled off from the Si wafer.

### **Characterization of the e-jet printed In<sub>2</sub>O<sub>3</sub> film and TFTs**

To analyze surface morphology and thickness profile of the e-jet printed layer, atomic force microscopy (AFM, DI-3100) was performed with a non-contact mode. The chemical composition of the printed layer (at PPM and NPM) and spun layer was examined by X-ray photoelectron spectroscopy (XPS, K-Alpha). Cross-sectional image and diffraction pattern of the In<sub>2</sub>O<sub>3</sub>/ZAO interface was analyzed by transmission electron microscopy (TEM, JEOL-2100F). Transfer and output characteristics and stability of TFTs and electrical properties of the amplifier were measured by a Keithley 4200-SCS semiconductor parametric analyzer.

### **Fabrication of common-source amplifier**

For the fabrication of the common-source amplifier, Cr 3nm/Au 80nm was deposited on SiO<sub>2</sub> 300 nm

wafer and photo-lithographically patterned to form a gate electrode. The preparation of a 155nm thick layer of ZAO dielectrics is described in the ESI Experimental Section. To get access to the gate contact pads, vias were formed by etching solution of  $\text{H}_3\text{PO}_4$  (55-56%), Sodium m-nitrobenzene sulfonate (5-10%) and  $\text{C}_2\text{H}_4\text{O}_2$  (1-5%). After e-jet print  $\text{In}_2\text{O}_3$  (width: 100  $\mu\text{m}$ , length: 50  $\mu\text{m}$ ) and subsequent annealing process, Au electrode (thickness: 100 nm drain) was photolithographically patterned as source/drain electrode. The fabrication was finished by the photolithographical patterning of the 15 nm-thick ITO as resistive load.

**Supplementary Table**

Ref.	Method	material	$\mu$ (cm <sup>2</sup> V <sup>-1</sup> s <sup>1</sup> )	Temp. (°C)	Advanced researches
37	E-jet	IZO	3.7	500	Transparent TFTs on glass
38	E-jet	ZTO	9.8	500	-
25	E-jet	IGZO	1.3	400	-
15	Ink-jet	IGZO	0.03	450	-
5	Ink-jet	In <sub>2</sub> O <sub>3</sub>	6	200	-
27	lk-jet	In <sub>2</sub> O <sub>3</sub>	3.98	200	-
31	Ink-jet	ZnO	1.61	250	1) Inverter 2) Flexible TFTs
33	Flexography	In <sub>2</sub> O <sub>3</sub>	8	300	-
40	Spray pyrolysis	Li-doped ZnO	85	400	
<b>Our results</b>	E-jet	In <sub>2</sub> O <sub>3</sub>	222	250	1) Printed In <sub>2</sub> O <sub>3</sub> TFTs with e-jet printing-assisted high-resolution S/D electrodes 2) Logic circuits 3) E-jet printing of encapsulation layer 4) Flexible/stretchable TFT

**Table S1 Comparison our results with previous reports**

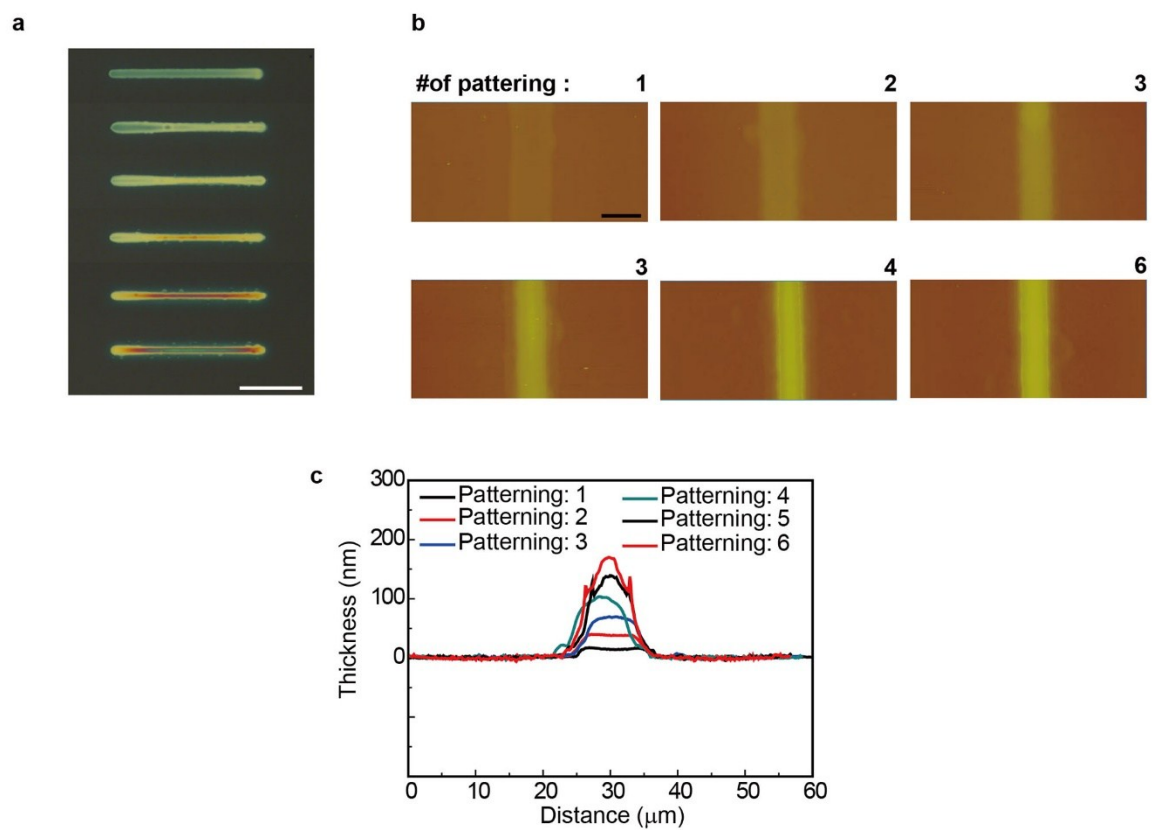
Ref.	Material	Novelty	Disadvantages
37	IZO	<ol style="list-style-type: none"> <li>1) Use of e-jet printing for the fabrication of metal oxide TFTs</li> <li>2) High resolution e-jet printing (line width: 1.5 <math>\mu\text{m}</math>)</li> </ol>	<ol style="list-style-type: none"> <li>1) Low mobility (<math>3.7 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}</math>)</li> <li>2) High-temperature process (<math>500^\circ\text{C}</math>) which limits the use of plastic substrates</li> </ol>
38	ZTO	<ol style="list-style-type: none"> <li>1) Use of e-jet printing for the fabrication of metal oxide TFTs</li> </ol>	<ol style="list-style-type: none"> <li>1) Low mobility (<math>9.8 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}</math>)</li> <li>2) Low resolution inkjet printing (line width: <math>60 \mu\text{m}</math>)</li> <li>3) High-temperature process (<math>500^\circ\text{C}</math>) which limits the use of plastic substrates</li> </ol>
25	IGZO	<ol style="list-style-type: none"> <li>1) IGZO ink incorporating formamide to enhance the mobility</li> <li>2) Use of e-jet printing for the fabrication of metal oxide TFTs</li> </ol>	<ol style="list-style-type: none"> <li>1) Low mobility (<math>1.3 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}</math>) of the e-jet printed TFTs</li> <li>2) High-temperature process (<math>400^\circ\text{C}</math>) which limits the use of plastic substrates</li> </ol>
15	IGZO	<ol style="list-style-type: none"> <li>1) Use of inkjet printing for the fabrication of metal oxide TFTs</li> </ol>	<ol style="list-style-type: none"> <li>1) Low mobility (<math>0.03 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}</math>)</li> <li>2) Low resolution inkjet printing (droplet diameter: <math>40 \mu\text{m}</math>)</li> <li>3) high-temperature process (<math>450^\circ\text{C}</math>) which limits the use of plastic substrates</li> </ol>
35	$\text{In}_2\text{O}_3$	<ol style="list-style-type: none"> <li>1) Use of inkjet printing for the fabrication of metal oxide TFTs</li> <li>2) Low annealing temperature (<math>&lt; 200^\circ\text{C}</math>)</li> </ol>	<ol style="list-style-type: none"> <li>1) Low mobility (<math>3.98 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}</math>)</li> <li>2) Low resolution inkjet printing (droplet diameter: <math>300 \mu\text{m}</math>)</li> <li>3) Post annealing process to develop the bias stability</li> </ol>
27	$\text{In}_2\text{O}_3$	<ol style="list-style-type: none"> <li>1) Low temperature process (<math>&lt; 200^\circ\text{C}</math>)</li> <li>2) Use of inkjet printing for the fabrication of metal oxide TFTs</li> </ol>	<ol style="list-style-type: none"> <li>1) Low resolution inkjet printing (line width: <math>500 \mu\text{m}</math>)</li> </ol>
31	ZnO	<ol style="list-style-type: none"> <li>1) Use of inkjet printing for the fabrication of metal oxide TFTs</li> <li>2) Low voltage operation (<math>&lt; 2 \text{ V}</math>) by using printed ion-gel electrolyte as gate dielectrics</li> <li>3) Fabrication of the inverter</li> <li>4) Fabrication of the flexible metal oxide TFTs</li> </ol>	<ol style="list-style-type: none"> <li>1) Low mobility (<math>1.61 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}</math>)</li> <li>2) Low resolution inkjet printing (line width: <math>50 \mu\text{m}</math>)</li> <li>3) Use of the PEDOT:PSS as gate electrode which is not suitable for practical usage due to the poor long-term reliability</li> </ol>
<b>Our results</b>	$\text{In}_2\text{O}_3$	<ol style="list-style-type: none"> <li>1) Use of e-jet printing for the fabrication of metal oxide TFTs</li> <li>2) High resolution e-jet printing (line width: <math>2 \mu\text{m}</math>)</li> <li>3) Transistors with a high mobility of <math>222 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}</math> by e-jet printing process</li> <li>4) Enhancement of the bias stability by e-jet printing of encapsulation layer</li> <li>5) Fabrication of the logic circuits</li> <li>6) E-jet printed metal oxide TFTs with e-jet assisted high-resolution electrode</li> <li>7) Fabrication of flexible/stretchable metal oxide TFTs by using e-jet printing</li> </ol>	

**Table S2** Comparison our novelty with previous reports

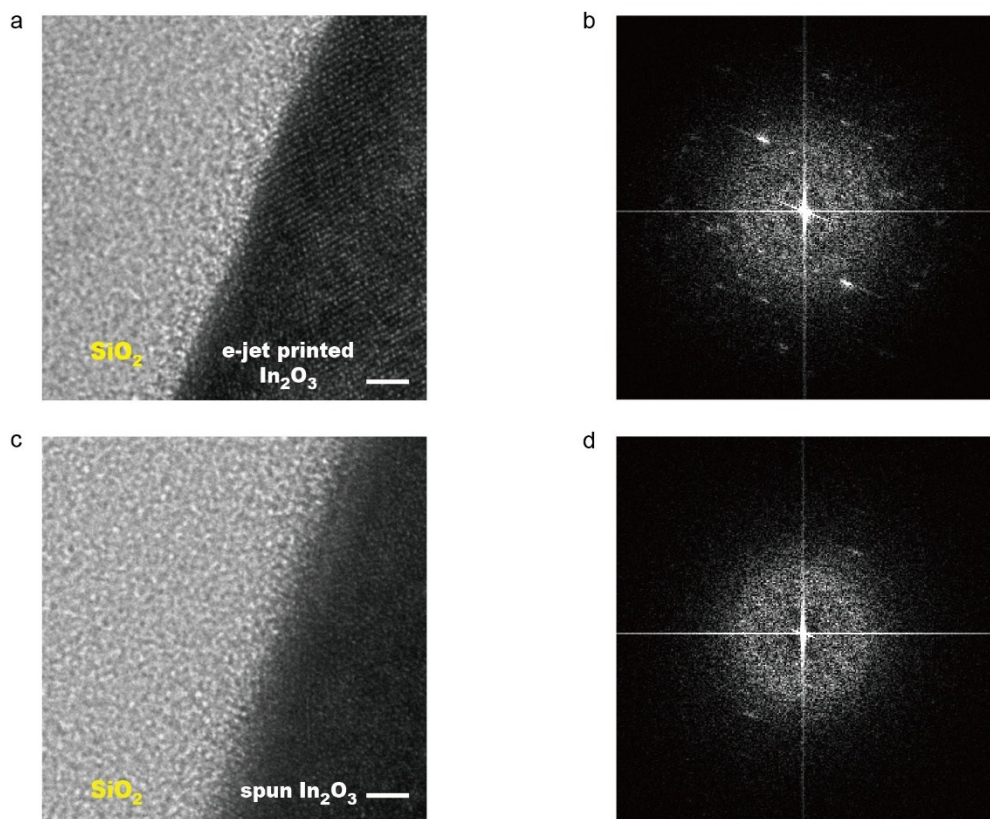
Ref.	Methods	Mobility [cm <sup>2</sup> V <sup>-1</sup> s <sup>-1</sup> ]	Temp. [°C]	application
32	Inkjet printing of IZO on high-k dielectrics (Zr doped AlO <sub>x</sub> )	54.2	350	-
40	Spray pyrolysed TFTs based on Li-doped ZnO semiconductor and ZrO <sub>2</sub> dielectrics	85	400	-
49	Use of a-IGZO incorporating AgNWs as active channel layer	174	400	-
50	Use of Ca/Al-capped a-IGZO as channel layer	160	400	-
<b>Our results</b>	E-jet printing In <sub>2</sub> O <sub>3</sub> on high-k dielectrics (Zr doped AlO <sub>x</sub> )	222	250	Logic circuits

**Table S3** Comparison our results with previous reports which have focused on dramatic enhancement of the mobility value

## Supplementary Figures

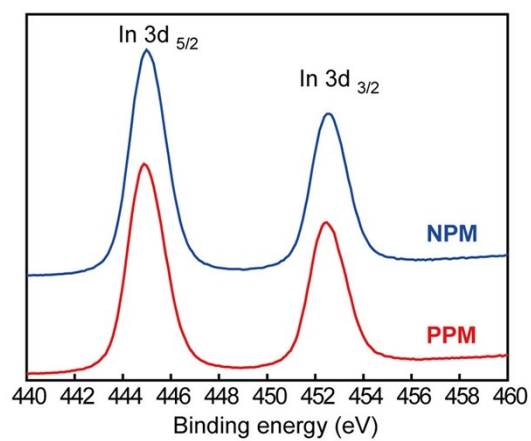


**Fig. S1** Controllable thickness of MOS layers by e-jet printing. (a) Optical micrograph, (b) AFM images and (c) two-dimensional profiles of various thickness of printed layers. White scale bar, 100  $\mu\text{m}$ . Black scale bar, 10  $\mu\text{m}$ .

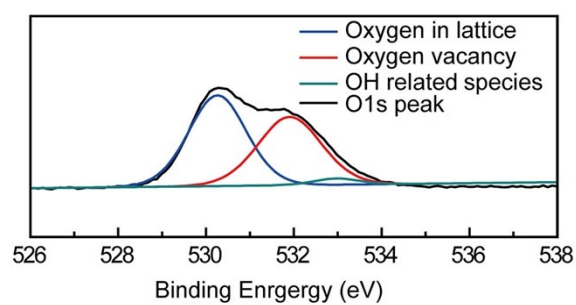


**Fig. S2** Film properties of e-jet printed and spun In<sub>2</sub>O<sub>3</sub> layer on SiO<sub>2</sub> dielectrics. High-resolution transmission electron microscope (TEM) image (a) and diffraction pattern (b) of selective area in e-jet printed layer, showing nano-crystalline phase in the overall film. TEM image (c) and diffraction pattern (d) of selective area in spun layer, showing amorphous phase and clusters in the film. White

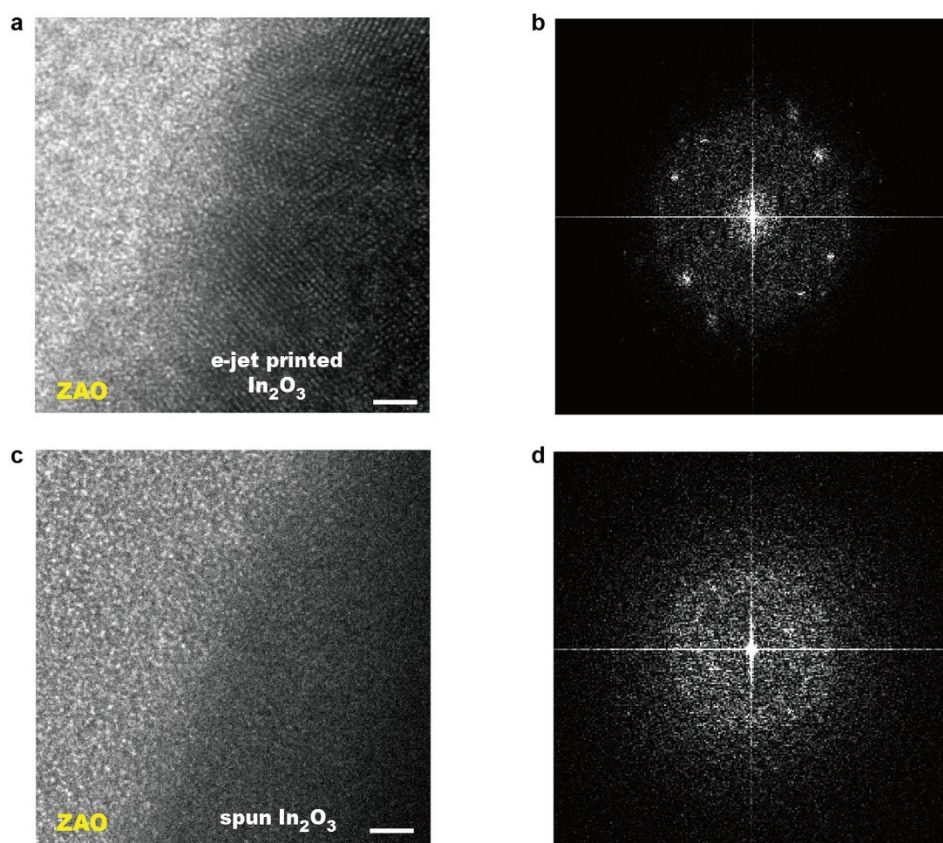




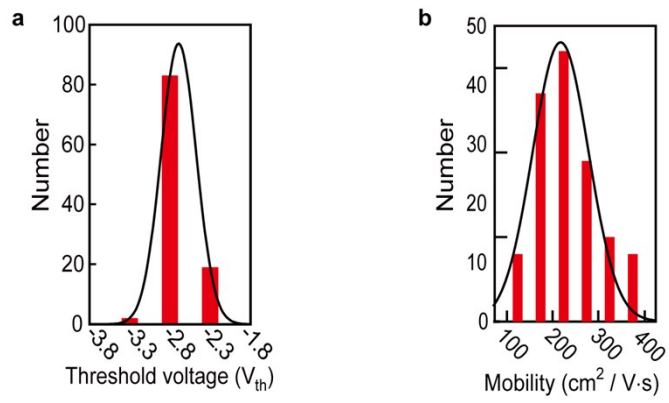
**Fig. S3** XPS analysis of In3d peak in the In<sub>2</sub>O<sub>3</sub> followed by e-jet printing with NPM and PPM. In3d<sub>5/2</sub> XPS spectra of In<sub>2</sub>O<sub>3</sub> films printed by both PPM and NPM indicates the indium species in printed layer are in oxidation states with In-O bonds.



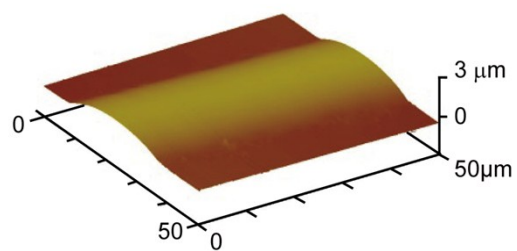
**Fig. S4** XPS analysis of O1s peak in the In<sub>2</sub>O<sub>3</sub> followed by spin coating. O1s XPS spectra of the In<sub>2</sub>O<sub>3</sub> films formed by spincoating, which are deconvoluted by O<sub>OX</sub>, O<sub>V</sub>, and O<sub>OH</sub> sub-curves, exhibiting peaks at 530, 532 and 534 eV, respectively.



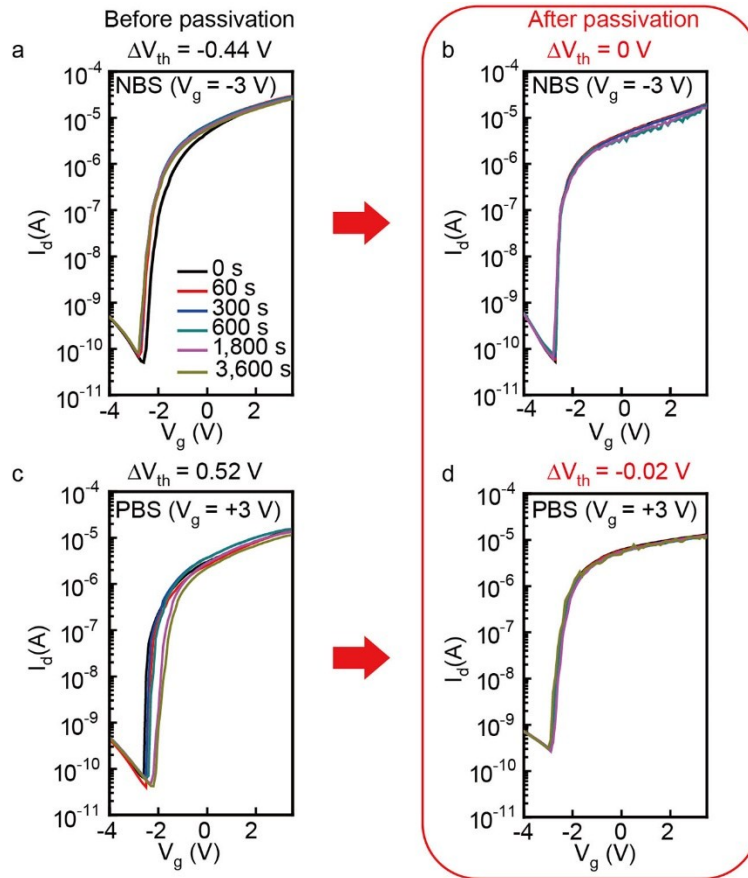
**Fig. S5** Film properties of e-jet printed and spun In<sub>2</sub>O<sub>3</sub> layer on ZAO dielectrics. High-resolution TEM image (a) and diffraction pattern (b) of selective area in e-jet printed layer. The TEM image exhibits nano-crystalline phase in the overall film and gradual transition from ZAO dielectrics to printed In<sub>2</sub>O<sub>3</sub> layer. High-resolution TEM image (c) and diffraction pattern (d) of selective area in spun layer. The TEM image shows amorphous phase and clusters in the film. White scale bar, 2 nm.



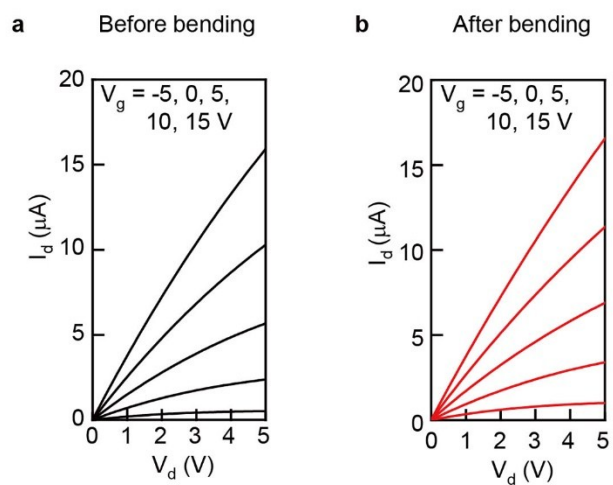
**Fig. S6** Statistical distribution of e-jet printed TFTs employing ZAO dielectrics in linear regime ( $V_d = 1$  V). (a) Mobility and (b) Threshold voltage of the 100 devices.



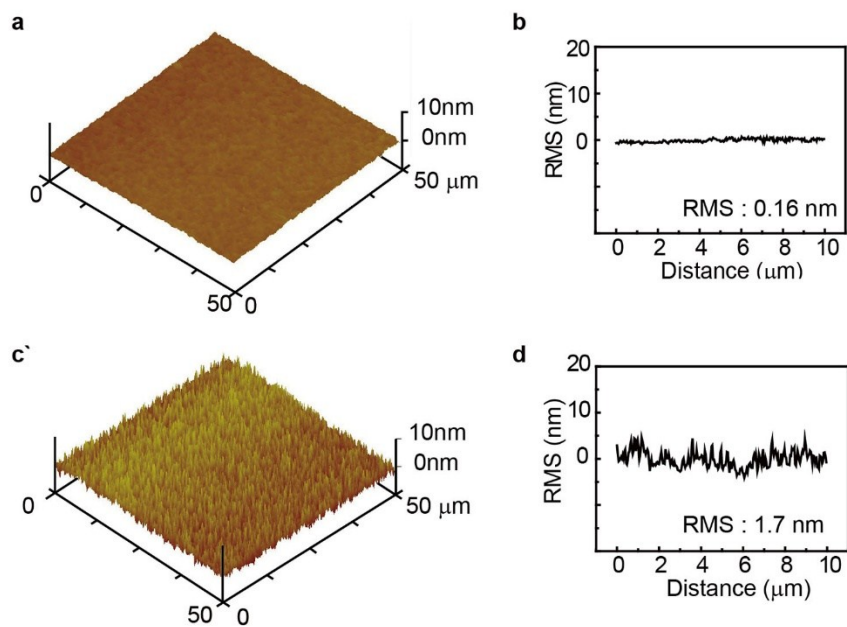
**Fig. S7** AFM image of printed passivation layer. This AFM image shows that the entire printed channel region is encapsulated by e-jet printing of inorganic-organic hybrid precursor based passivation layer.



**Fig. S8** Transfer characteristics of TFTs stressed for 3600 s under negative gate bias (NBS) and positive gate bias (PBS) with/without passivation layer ( $V_g = \pm 3$  V and  $V_d = 1$  V). TFT characteristics under NBS (a) without passivation layer and (b) with passivation layer, showing improved bias stability from  $\Delta V_{th} = -0.44$  V to  $\Delta V_{th} = 0$  V. TFT characteristics under PBS (c) without passivation layer and (d) with passivation layer, showing improved bias stability from  $\Delta V_{th} = 0.52$  V to  $\Delta V_{th} = -0.02$  V.

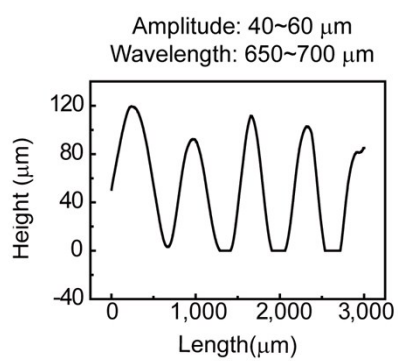


**Fig. S9** Output characteristics of the flexible TFTs before and after transferring to the cylindrical curvature with radius of 3.5 mm. Output characteristics for  $V_g$  ranging from -5 V to 15 V in 5 V steps (a) before and (b) after bending to cylindrical support.

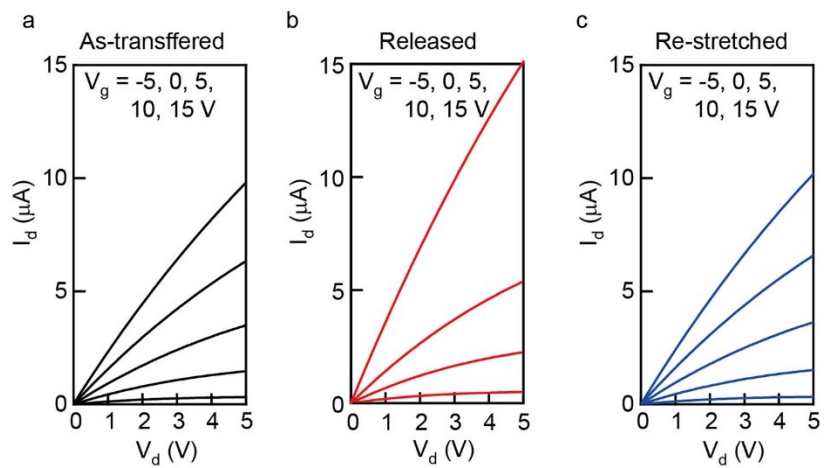


**Fig. S10** Roughness of  $\text{SiO}_2$  substrate and polyimide substrate. (a) AFM image and (b) RMS roughness of the surface of the  $\text{SiO}_2$  substrate. (c) AFM image and (d) RMS roughness of the surface of the polyimide substrate.





**Fig. S11** The wave topology of stretchable, ultra-thin substrate characterized by  $\alpha$ -step. After releasing of the ultra-thin PI substrate from pre-strain PDMS, stretchable PI substrate exhibit the wavelength and amplitude of wave of  $\sim 700 \mu\text{m}$  and  $\sim 50 \mu\text{m}$ , respectively.



**Fig. S12** Output characteristics of the stretchable TFTs corresponding to the states of the stretchable TFTs ( $V_g$  ranging from -5 V to 15 V in 5 V steps). Output characteristics (a) after TFT arrays sheet is as-transferred to the 10% pre-stretched PDMS, (b) after pre-stretched PDMS is relaxed, and (c) after relaxed PDMS is 10% re-stretched.

### Calculation of density of interface defect ( $D_{it}$ )

The interface trap density ( $D_{it}$ ) is one of the main dominants for transistor performance, which results in the large carrier scattering and trapping in the channel layer. According to the conventional metal oxide semiconductor field effect transistor, interface trap density can be calculated from the sub-threshold swing. We summarized the results of calculated density of the interface defect as follows:

Samples	Annealing Temp. ( °C)	Capacitance (F cm <sup>-2</sup> )	$\mu$ (cm <sup>2</sup> V <sup>-1</sup> s <sup>-1</sup> )	S (V per decade)	$D_{it}$ (cm <sup>-2</sup> eV <sup>-1</sup> )
E-jet printed In <sub>2</sub> O <sub>3</sub> on ZAO	250	7.5X10 <sup>-8</sup>	222	0.2	<b>9.0X10<sup>11</sup></b>
E-jet printed In <sub>2</sub> O <sub>3</sub> on SiO <sub>2</sub>	250	3.7 X10 <sup>-8</sup>	7.7	1.2	<b>5.0X10<sup>12</sup></b>

$$S = \left( \frac{d(\log_{10} I_d)}{dV_g} \right)^{-1} \approx \ln 10 \frac{kT}{q} \left[ 1 + \frac{qD_{it}}{C_i} \right],$$

where k: Boltzmann's constant, q: electronic charge, C<sub>i</sub>:

### Component materials and device performance parameters of metal oxide TFTs