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## Supplemental information

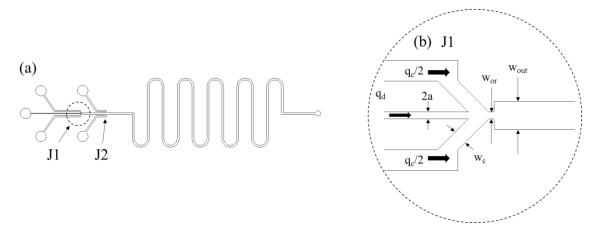


Figure S1. Schematic of (a) the full optimized on-chip particle generation device and (b) the orifice for drop formation at the first junction (J1)

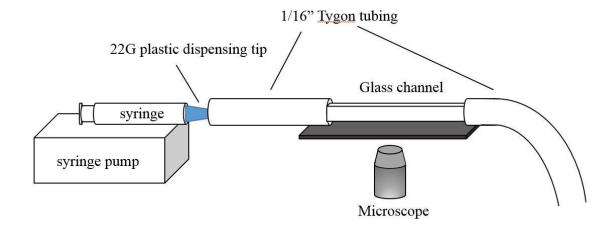


Figure S2. Schematic of experimental set-up for flow experiments