

## Spin transport properties and spin logic gates in manganese phthalocyanine-based molecular combinational circuits

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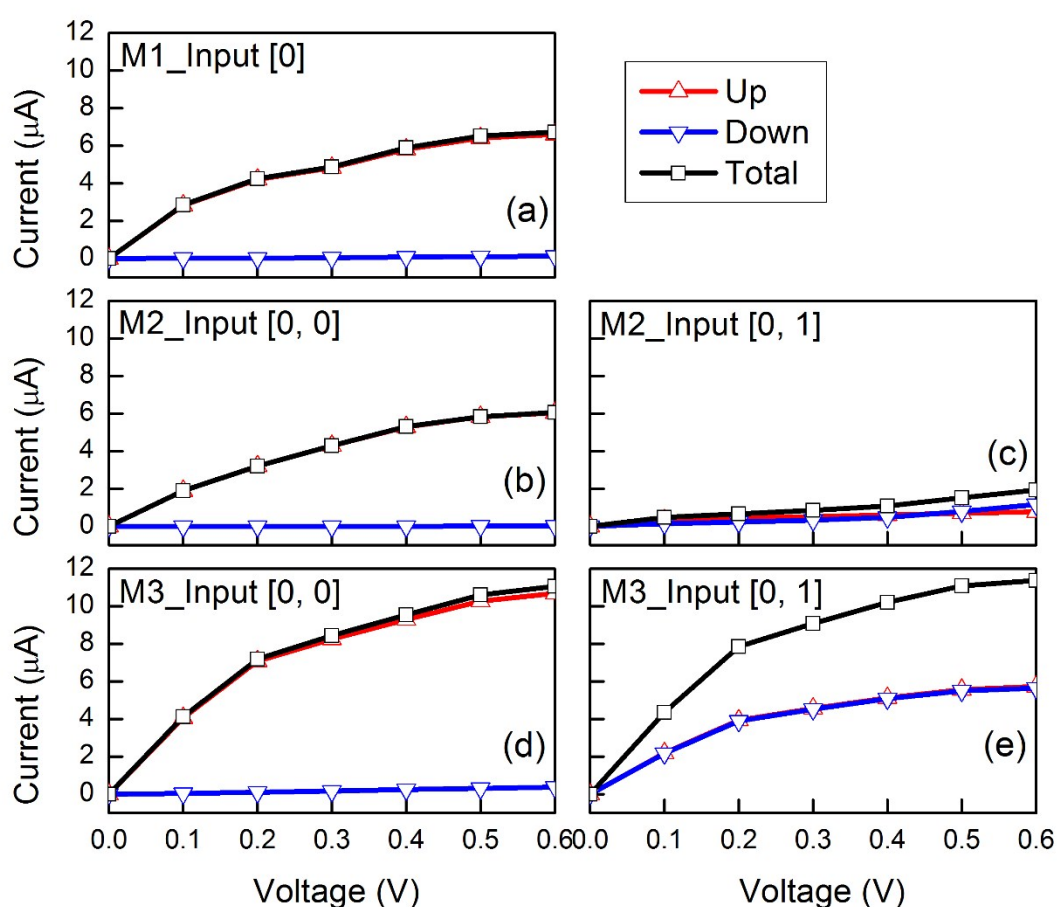


Fig. S1 (a)-(e) Calculated total and spin-resolved  $I$ - $V$  curves for M1-M3. (a) corresponds to the logic input state [0] for M1. (b) and (c) correspond to the logic input states [0, 0] and [0, 1] for M2, respectively. (d) and (e) correspond to the logic input states [0, 0] and [0, 1] for M3, respectively.

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