

Supporting Information

**Highly improved performance in $\text{Zr}_{0.5}\text{Hf}_{0.5}\text{O}_2$ films inserted by
graphene oxide quantum dots layer for resistive switching
nonvolatile memory**

Xiaobing Yan,^{1,4*} Lei Zhang,¹ Yongqiang Yang,² Zhenyu Zhou,¹ Jianhui Zhao,¹ Yuanyuan Zhang,¹ Qi Liu,³
Jingsheng Chen⁴

¹ College of Electron and Information Engineering, Key Laboratory of Digital Medical Engineering of Hebei Province, Key Laboratory of Optoelectronic Information Materials of Hebei Province, Hebei University, Baoding 071002, P. R. China

²Jiangsu Province Special Equipment Safety Supervision and Inspection Institute · Branch of Wuxi, National Quality Supervision and Inspection Center of Graphene Products (Jiangsu), Wuxi2147174, P. R. China

³Key Laboratory of Microelectronic Devices & Integrated Technology, Institute of Microelectronics, Chinese Academy of Sciences, Beijing 100029, P. R. of China

⁴Department of Materials Science and Engineering, National University of Singapore, Singapore 117576, Singapore

*Corresponding author: xiaobing_yan@126.com

I The GOQDs specimens

We can observe that the solution of GOQDs is uniform as shown in Fig. S1, and it shows that GOQDs are evenly distributed inside the solution.



Figure S1. GOQDs solution specimens.

II The XRD of the device.

As shown in Fig. S2, two peaks can be ascribed to Pt (111) and Si (100). Therefore, the $Zr_{0.5}Hf_{0.5}O_2$ films is amorphous through the analysis of the picture of XRD data. From the work of others, we know that amorphous materials can act as a function layer for resistive switching device.¹⁻³

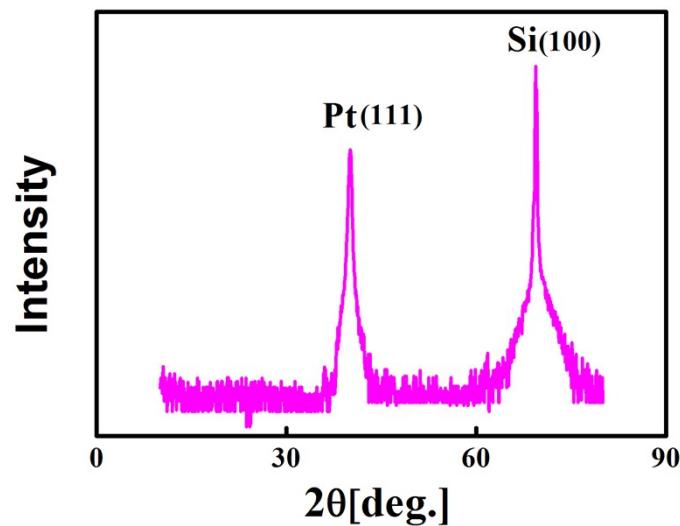


Figure S2. XRD of the device.

III The I-V curves of 200 cycles for the two kinds of devices

In this work, we test I-V of the two kinds of devices with and get the following data as shown in Fig. S3. We can intuitively observe that the I-V circulation of the device with GOQDs is more concentrated than that of the device without GOQDs.

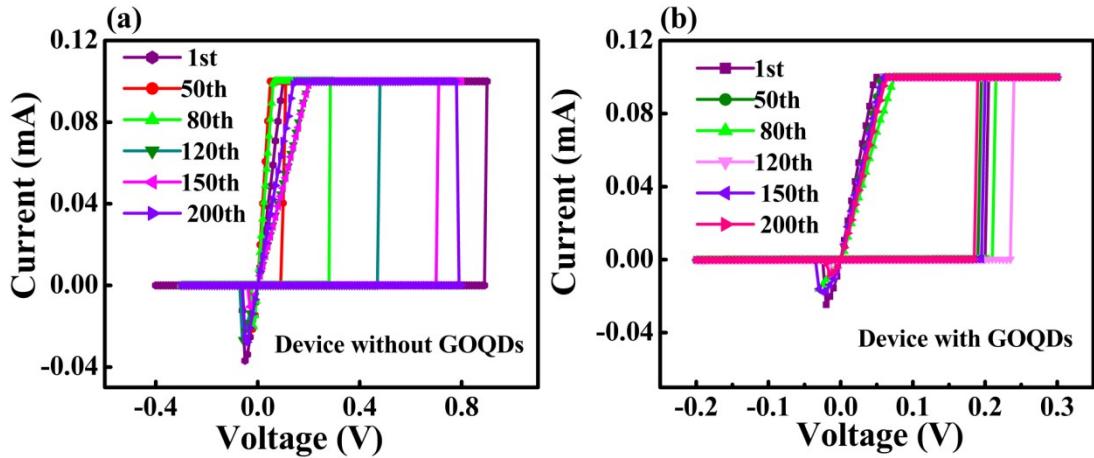


Figure S3. The I-V curves. (a) The I-V curves for 200 continuous RS cycles of the Ag/ZHO/Pt device, (b) The I-V curves for 200 continuous RS cycles of the Ag/ZHO/GOQDs/ZHO/Pt device.

IV The statistic distributions of LRS and HRS

The switching resistances at turn-on and turn-off states for 200 cycles' tests fluctuated in small ranges as described in Fig. S4(a). The statistic distributions of LRS and HRS were relative centralized as shown in Fig. S4(b). The HRS resistance and the LRS resistance of the device is close to $5 \times 10^6 \Omega$ and $1 \times 10^3 \Omega$.

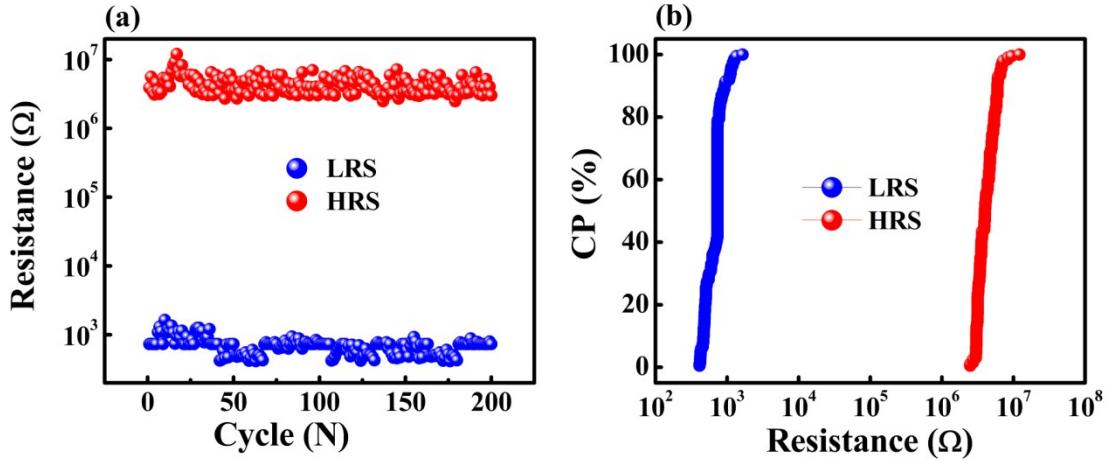


Figure S4. (a) Switch resistance of Ag/ZHO/GOQDs/ZHO/Pt device. (b) Statistic resistance distributions of the 200 I-V cycles sweeping. Cumulative probability is abbreviated as CP.

V Endurance measurement of Ag/ZHO/GOQDs/ZHO/Pt device

The electrical pulse-induced resistive switching characteristic of the GOQDs device is shown in Fig. S5. The pulse of 2V/150ns and -2V/150ns were defined as programming and erasing voltage pulse, and 0.1V/1000ns voltage pulse was applied to read the resistance of the device as shown in Fig. S5(a). It should be noted that a stable resistive switching is observed up to 1×10^6 cycles as shown in Fig. S5(b). After 1×10^6 successive switching cycles, the resistance ratios between HRS and LRS are still has no apparent degradation. It indicates that the GOQDs memory device possesses overwhelming endurance property.

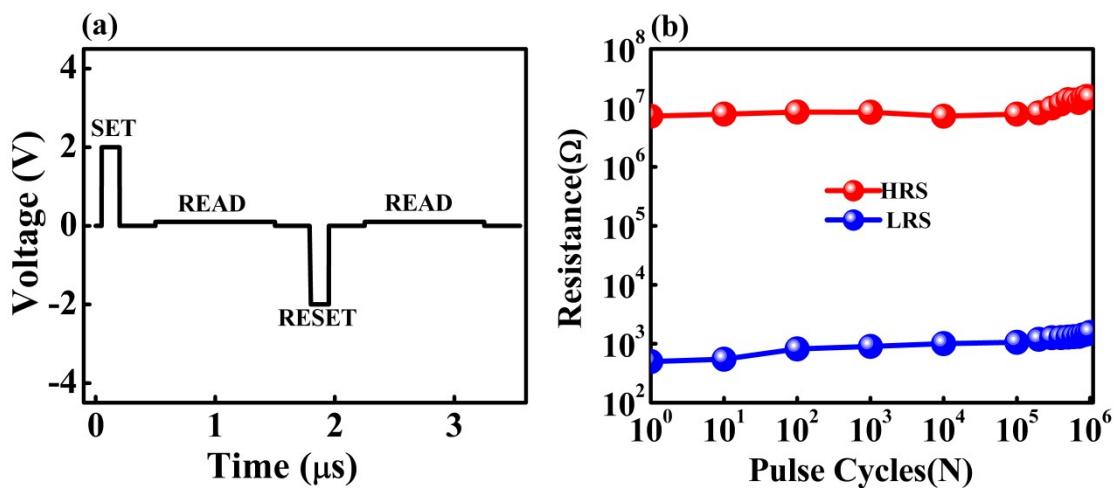


Figure S5. Endurance test of Ag/ZHO/GOQDs/ZHO/Pt device under pulse mode. (a) Pulses with 2V/150ns and -2V/150ns for set and reset, 0.1V/1000ns for reading. (b) Endurance property for 10^6 pulse cycle.

VI The double-logarithmic I-V curve of the GOQDs device

To clarify the conduction mechanisms of oxide-based devices with embedded GOQDs, the positive and negative sweep region about I-V curves of the device with GOQDs in double-logarithmic scale are depicted in Figs. S6(a) and S6(b). Usually the typical I-V characteristics of the trap-controlled space charge limited current (SCLC) consists of three parts: linear relationship between I-V (ohmic contact), the current proportional to the square of the voltage ($I-V^2$), and the dramatically increased I with the increase of V.^{4,5} We can see that at the positive sweep region, the I-V curves in HRS display ohmic behavior with the slope1 is 0.98, Child's law with the square relation when the slope2 is 1.88, and a steep current rise when the slope3 is 3.86 and very large slope at set region. In a word, the fitting results for HRS explain that the charge transport behavior during set process is in good agreement with the classical SCLC mode. At the negative sweep region, the I-V characteristics in HRS also basically follow the trap-controlled SCLC mechanism. In both the LRS at the positive and negative region, both the fitted slopes are close to 1, which indicate that they are ohmic conduction behavior. So it suggests the formation of conductive filaments during the set process.

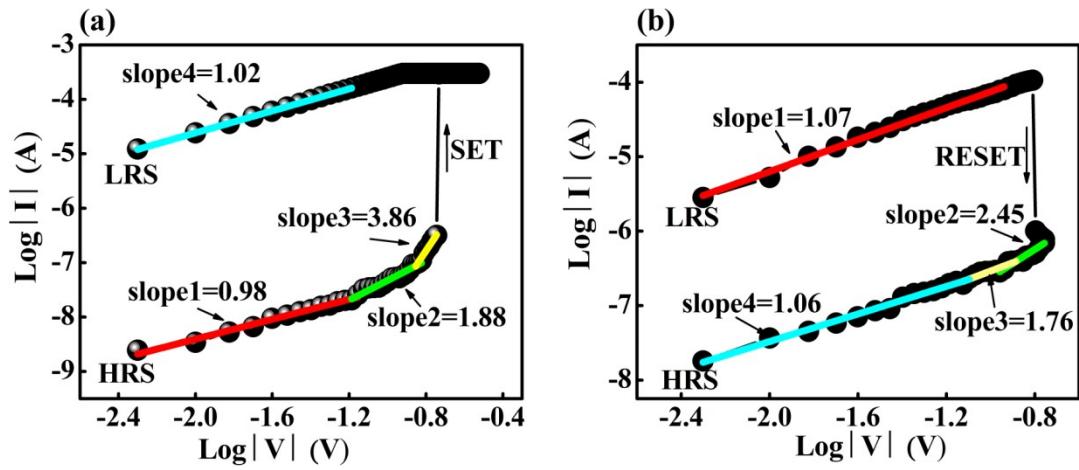


Figure S6. Piecewise linear fitting for the double-logarithmic I-V curve of the GOQDs device. (a) Positive sweep region. (b) Negative sweep region.

VII Comparison on switching voltage and OFF/ON ratio

Device structure	OFF/ON ratio	V_S/V_R	References
Ti/CdSe QDs/Ti-TiO ₂ /CdSe QDs/ITO	$\sim 10^2$	-1V/1.5-2V	6
Al/CdSe QDs/Al/CdSe QDs/ITO	$\sim 10^4$	-0.45V/2V	7
Al/[PMMA:CdSe /ZnS QD]/PMMA/[PMMA:CdSe /ZnS QD]/ITO	$\sim 10^4$	-1.2V/3V	8
Al/In ₂ O ₃ /CdSe QD /ITO	$\sim 10^3$	-1V/2V	9
Ag/(Graphene QDs/PVP)/Ag	~ 14	1.5V/-1.65V	10
Au/PMMA/PMMA:MoS ₂ QDs/PMMA/FTO	$\sim 10^2$	0.5V/-0.9V	11
Ag/ZHO/GOQDs/ZHO/Pt	$\sim 10^4$	0.08-0.3V/-0.14--0.01V	This work

Table S1. Comparison of switching parameters with literatures. From the table , we can see that the RRAM with GOQDs we prepared has a smaller V_S and V_R .

References

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