

Support information for

Epitaxial Growth of Large-scale In₂S₃ Nanoflakes and the Construction of High Performance In₂S₃/Si Photodetector

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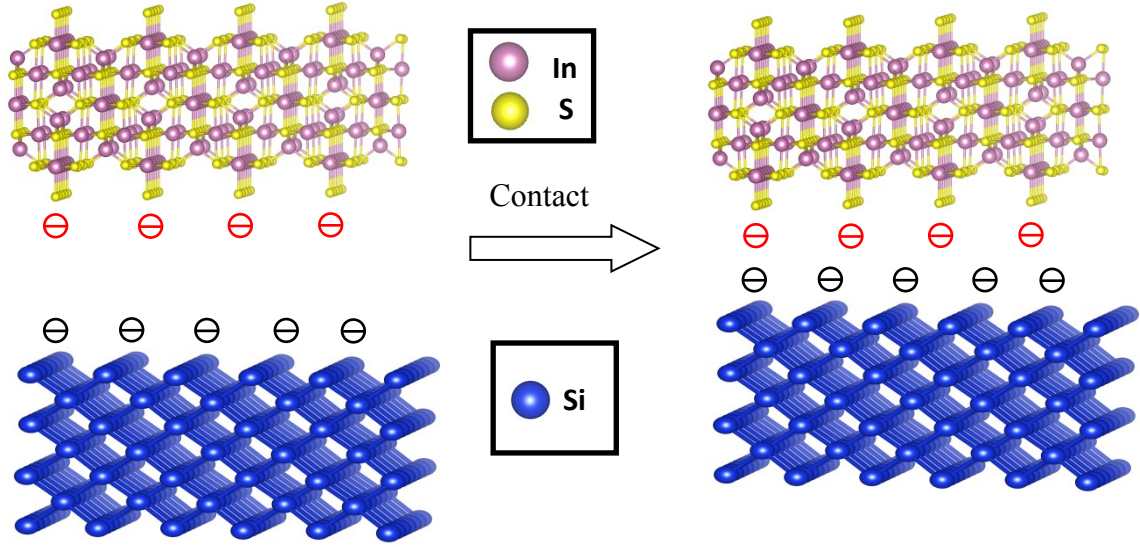


Figure S1. Compatibility between In_2S_3 and Si. Since both In_2S_3 and Si are non-layered structures, there are many unsaturated dangling bonds on the surface of S atoms and Si atoms, as shown on the left. After the transfer of In_2S_3 onto Si, since the electronegativity of the S atom is greater than that of the Si atom, the unsaturated dangling bonds on the surface Si atoms will be attracted to the surface of the S atoms, as shown on the right. Therefore, In_2S_3 and Si will be tightly packed together to form an excellent heterojunction.

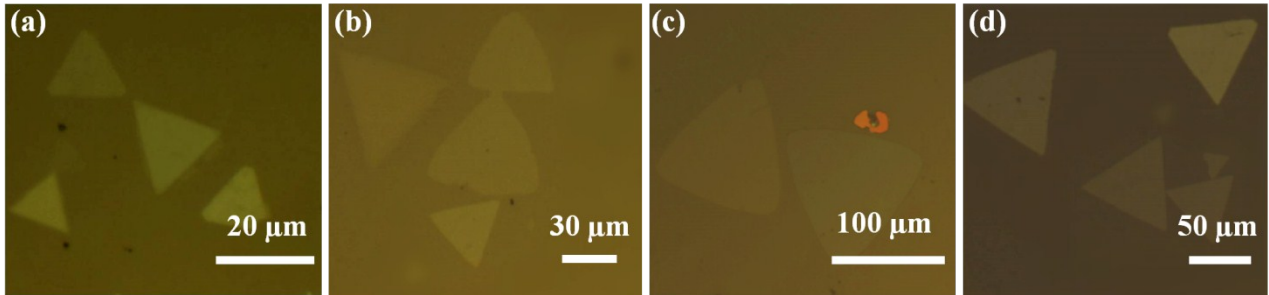


Figure S2. Optical images of the as-grown $\beta\text{-In}_2\text{S}_3$ flakes obtained at different temperatures: (a) 900 °C. (b) 950 °C. (c) 980 °C. (d) 1020 °C. As can be seen, we have prepared In_2S_3 nanoflakes under various temperatures. With the increase of the growth temperature, the size of the In_2S_3 nanoflakes increases. When the growth temperature increases to be more than 980 °C, the size slightly declines. Therefore, 980 °C is chosen as the optimum growth temperature in this work.

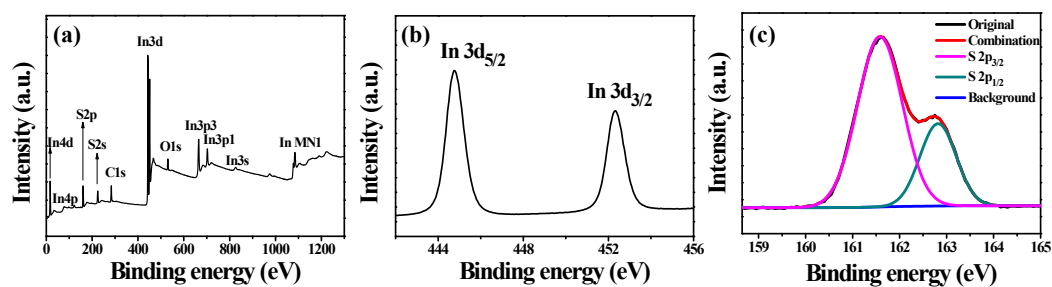


Figure S3. XPS pattern of In_2S_3 . (a) Full spectrum of In_2S_3 . Core spectrum of (b) In_{3d} and (c) S_{2p} .

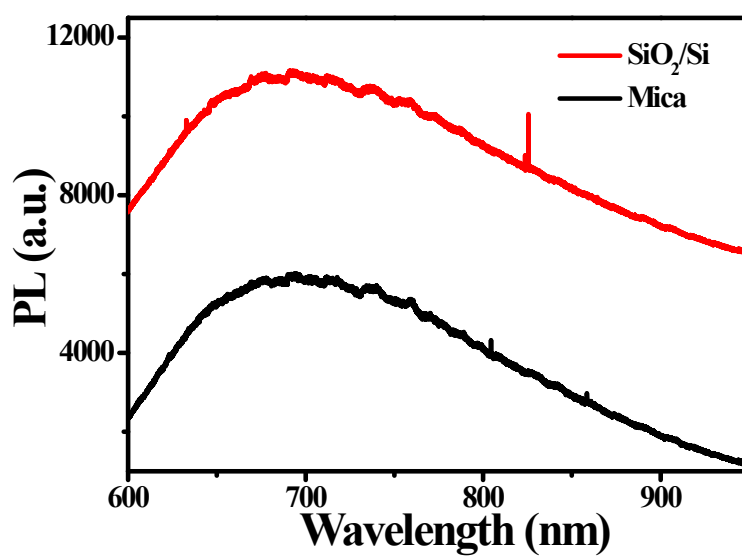


Figure S4. PL spectrum of original In_2S_3 on the mica substrate (before) and transferred In_2S_3 on SiO_2/Si substrate (after).

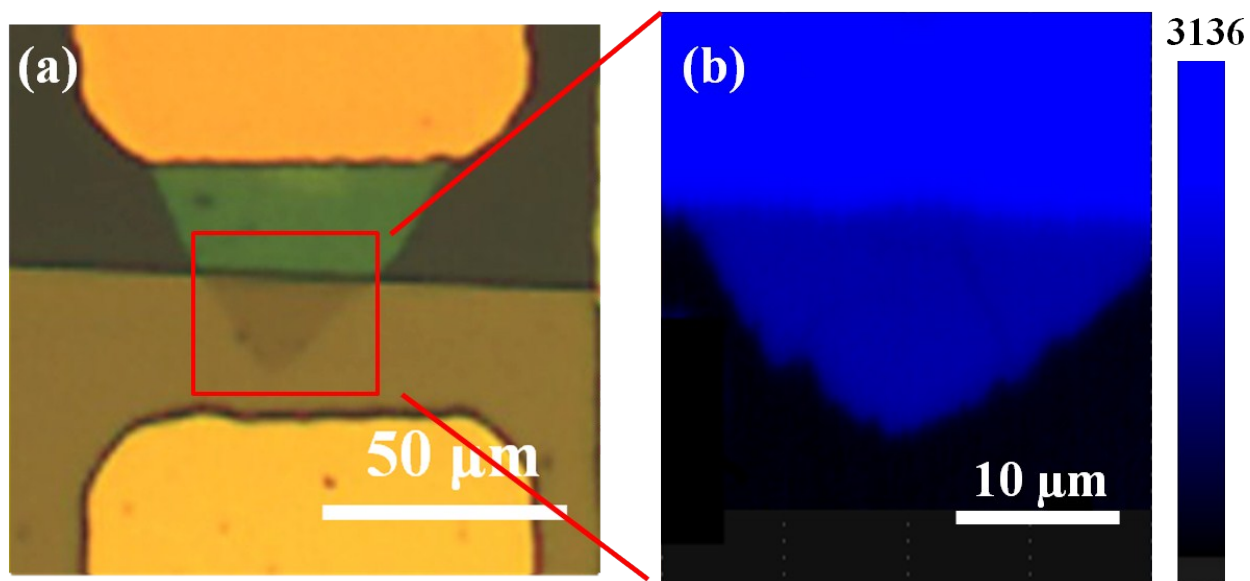


Figure S5. (a) Optical microscope image of the $\text{In}_2\text{S}_3/\text{Si}$ device. (b) The PL mapping of the $\text{In}_2\text{S}_3/\text{Si}$ device, corresponding to the red rectangle in (a). Recognizable PL quenching is observed at the $\text{In}_2\text{S}_3/\text{Si}$ heterojunction.

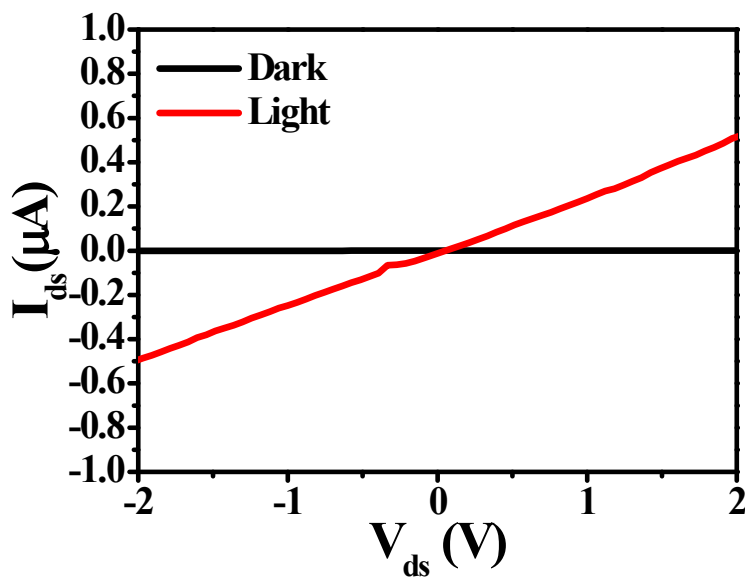


Figure S6. I_{ds} - V_{ds} curves of planar In_2S_3 device under dark and light illumination. The nearly linear I-V curves prove the good Ohmic-contacts for $\text{Ti}/\text{Au}-\text{In}_2\text{S}_3$.

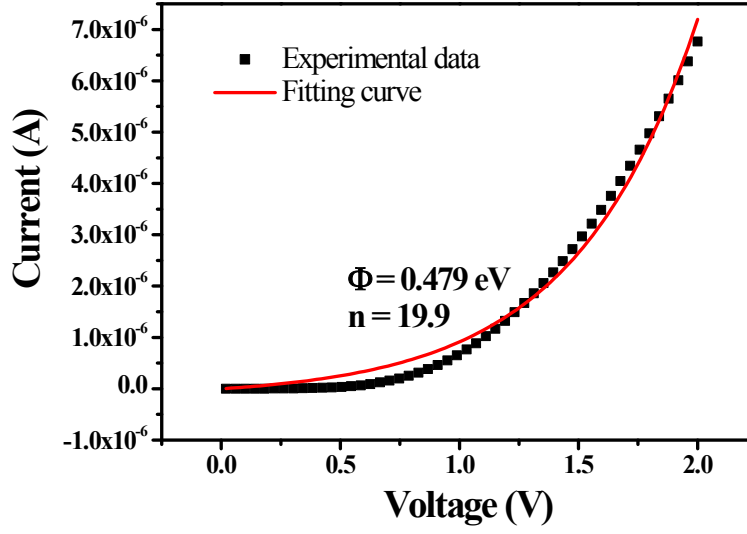


Figure S7. The fitting curve of barrier height of the In₂S₃/Si heterojunction by using the thermionic emission theory-based diode equation.

The current through a Schottky junction can be described by the well-known Richardson–Dushman thermionic emission theory¹⁻²:

$$I = I_s \left[\exp \left(\frac{eV}{nkT} \right) - 1 \right] \quad (1)$$

$$I_s = A_1 A^* T^2 \exp \left(- \frac{e\phi_b}{kT} \right) \quad (2)$$

where I_s is the reverse saturation current, e is the electronic charge, V is the bias voltage, n is the ideality factor, k is the Boltzmann constant, T is the absolute temperature, A_1 is the area of the device, ϕ_b is the Schottky barrier height, and A^* is the effective Richardson constant ($32 \text{ A cm}^{-2} \text{ K}^{-2}$ for p-type Si). On the basis of the above equations, by fitting the dark I–V curves of these two devices, ϕ_b can be deduced to be 0.479 eV for the graphene/Si Schottky junction.

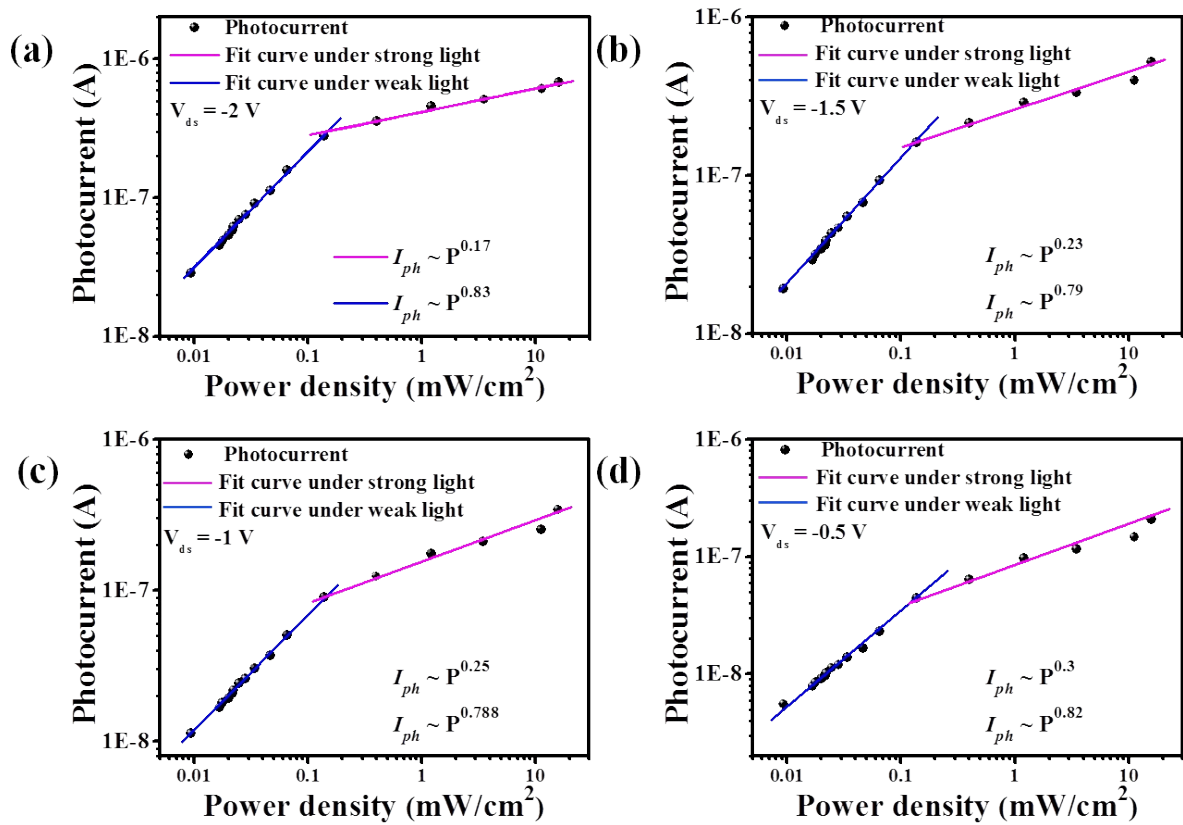


Figure S8. Photocurrent versus illumination power density at the applied voltage of (a) -2 V; (b) -1.5 V (c) -1 V (d) -0.5 V.

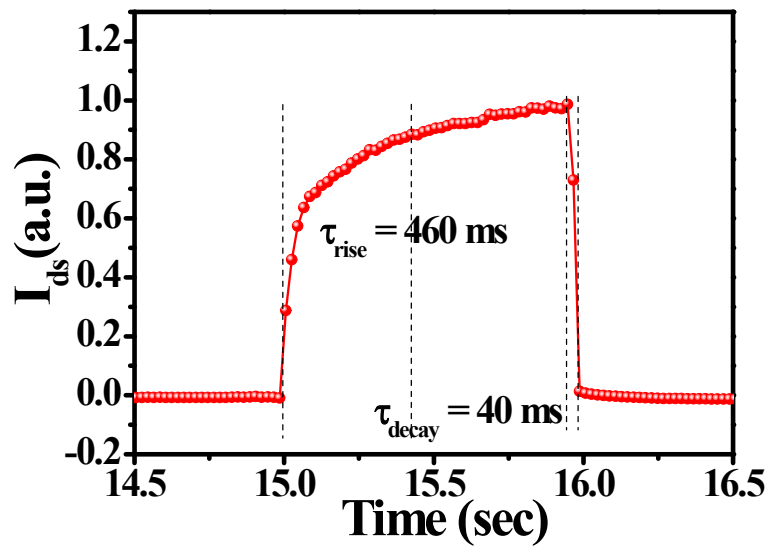


Figure S9. The magnified and normalized plot of one response cycle of pure In₂S₃ device.

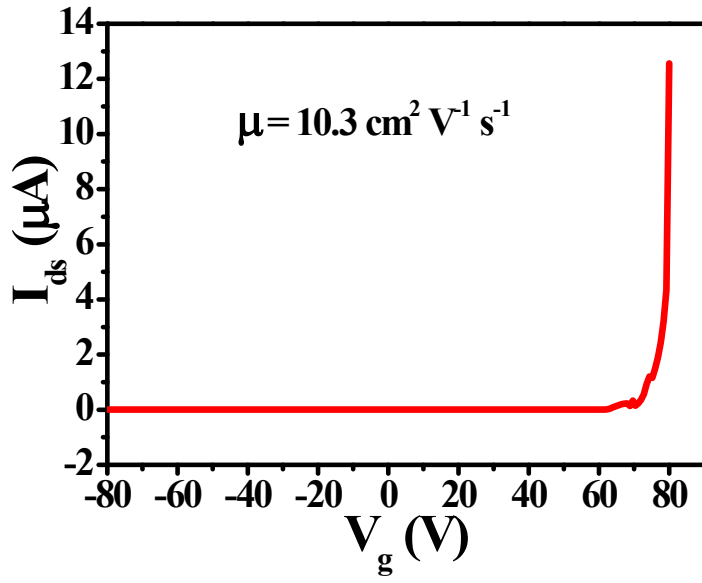


Figure S10. The transfer curve of planar In₂S₃ device.

The electron mobility can be acquired by the following equation³⁻⁴

$$\mu = \frac{\partial I_{ds}}{\partial V_g} \left(\frac{Ld}{W\epsilon_o\epsilon_r V_{ds}} \right)$$

where L and W are the length and width of the channel, $\frac{\partial I_{ds}}{\partial V_g}$ is slope of the transfer curve, μ is mobility, ϵ_r is the relative static permittivity, ϵ_o is the electric constant, and d is the thickness of the SiO₂ layer. Herein, $V_{ds} = 2$ V, $L = 14$ μ m, $W = 22$ μ m, and $d = 300$ nm. Then, $\epsilon_o = 8.85 \times 10^{-12}$ F/m,

$\epsilon_r = 3.9$, and $\frac{\partial I_{ds}}{\partial V_g} = 4.58 \times 10^{-7}$ A/V. Based on the data above, the electron mobility is calculated to be $10.3 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$.

Table S1. Key parameters comparison between our In₂S₃/Si device and other non-layered 2D materials based photodetectors. Commercial Si device is also included.

Devices	Method	Lateral (μm)	R (A/W)	D* (Jones)	On/off ratio	Rise/decay time (ms)	Ref.
In₂S₃/Si	PVE	~ 161	579.6	2.1$\times 10^{11}$	~ 552	9/0.131	Ours
In₂S₃	CVD	~ 10	137	7.74 $\times 10^{10}$	ND	6/8	⁵
In₂S₃	HT	~ 5	ND	ND	16	2000/100	⁶
CdTe	CVD	5-11	0.6 $\times 10^{-3}$	10 ⁹	27	18.4/14.7	⁷
SnTe	PVD	~ 30	71	ND	~ 2	210/730	⁸
PbS	HT	~ 1	0.472	ND	100	ND	⁹
PbS	CVD	2-2.6	1621	10 ¹¹	~ 2	300/300	¹⁰
Pb_{1-x}Sn_xSe	CVD	~ 15	5.95	ND	~ 3	900/740	¹¹
Te	CVD	6-10	160	ND	~3	4400/2800	¹²
Commercial Si	ND	ND	0.5	3 $\times 10^{12}$	ND	ND	¹³

Ref.: reference. CVD: chemical vapor deposition. PVE: physical vapor epitaxy. ME: mechanical exfoliation. HT: Hydrothermal. ND: no data. Gr: graphene.

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