

## Supporting Information

### Amelioration of interfacial combination and suppression of oxygen vacancies for high performance environmentally friendly electrospun SnYO nanofibers field-effect transistors

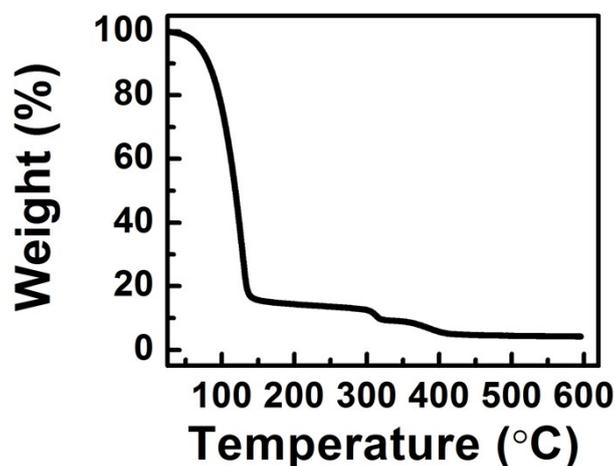
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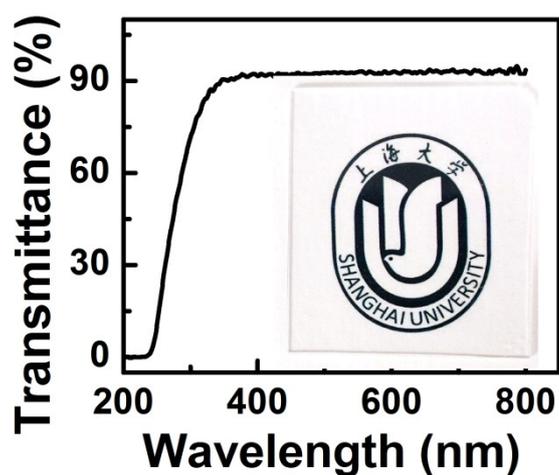
**Fig. S1** Thermal behavior of SnO<sub>2</sub> precursor solution.

Fig. S1 shows the thermal properties of the SnO<sub>2</sub> precursor solutions by means of thermogravimetric (TG) analysis with a heating rate of ~10°C/min. The TG curve of the SnO<sub>2</sub>

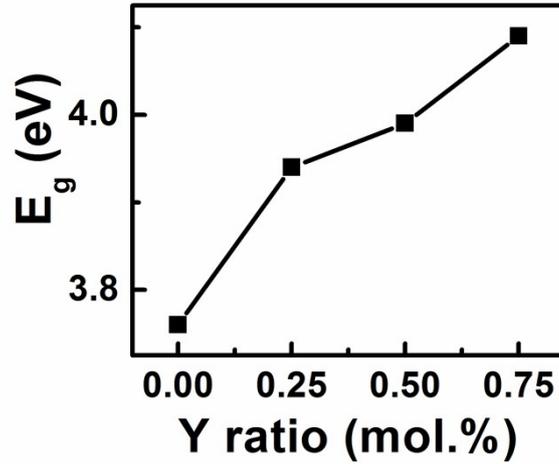
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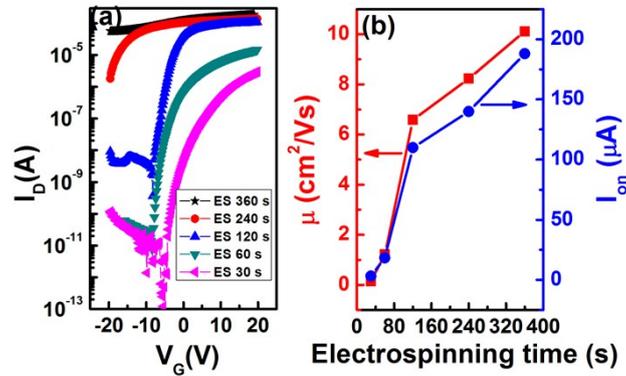
precursor solutions exhibits a three-step reduction in weight. The drastic mass loss in the first step could be due to the evaporation of moisture and residual solvent. In the second step, the weight loss between 280 and 330 °C could arise from the metal-to-oxygen (M-O) formation followed by the below reaction (1) and (2). In the final step, the weight loss at ~450 °C could be attributed to the thermal decomposition of the PVP chains. In this work, the annealing temperature at 500 °C should be chosen because all reactions has been carried out and there is no change in weight loss, indicating the removal of organic PVP and formation of pure SnO<sub>2</sub> nanofibers.



**Fig. S2** Optical transmission spectra of SnO<sub>2</sub> nanofibers .



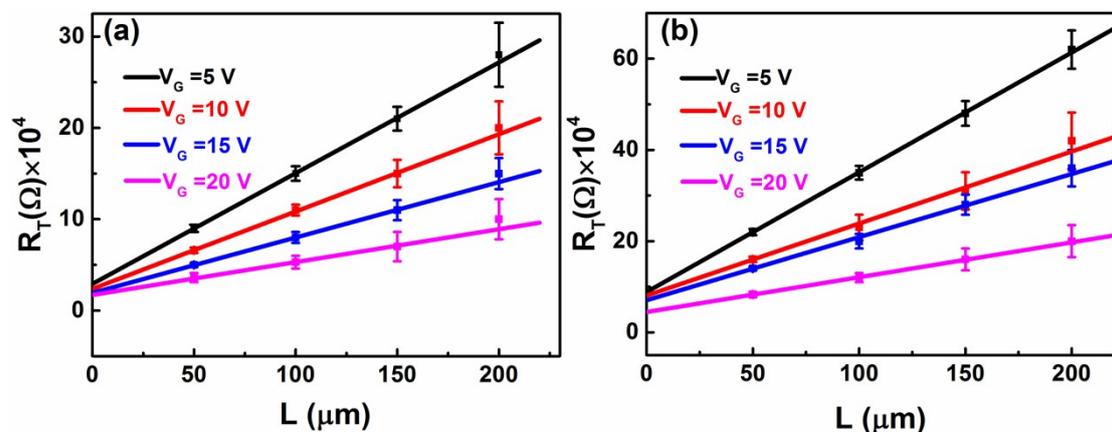
**Fig. S3** The calculated  $E_g$  for SnYO nanofibers with different concentrations.



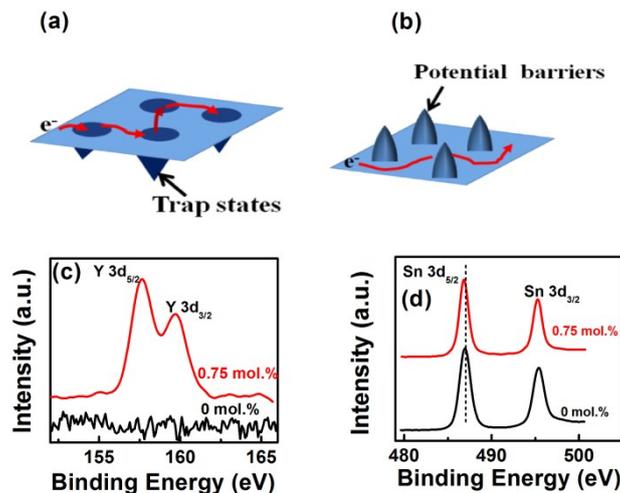
**Fig. S4** (a) Transfer curves of SnO<sub>2</sub> nanofibers FETs with different electrospinning time. (b) Electrical parameters of  $\mu$  and  $I_{\text{on}}$  as a function of electrospinning time for SnO<sub>2</sub> nanofibers FETs.

The nanofibers network channel coverage controlled by the electrospinning time also plays a key role in the nanofibers FETs electrical performance. Fig. S4(a) shows the transfer curves of SnO<sub>2</sub> nanofibers FETs with different electrospinning time. The performance parameters of the FETs are summarized in Fig. S4(b). The results imply that the FETs performance is strongly depend on the nanofibers network channel coverage. When the electrospinning time is 30 s, the device shows a poor electrical parameter, including a low  $I_{\text{on}}$  of 3.12  $\mu\text{A}$  and  $\mu$  of 0.15  $\text{cm}^2/\text{V}\cdot\text{s}$ . With the increase in the electrospinning time,  $I_{\text{on}}$  and  $\mu$  are gradually improved due to the production of the more conducting path for carrier electrons. Although the device shows the high

$\mu$  of 10.12 cm<sup>2</sup>/V·s with the electrospinning time of 360 s, the poor switching characteristics are clearly observed resulting from the overlapping of the nanofibers, which is not suitable for practical applications. The nanofibers FETs with the optimized electrospinning time of 2 min exhibit a better electrical performance, including a better  $I_{on}$  of 104  $\mu$ A and a  $\mu$  of 6.21 cm<sup>2</sup>/V·s. Therefore, it is plausible that the formation of sufficient nanofibers network and adequate electrospinning time are of great importance to the nanofibers FETs electrical properties.



**Fig. S5** The contact resistance with different channel length for (a) SnO<sub>2</sub> nanofibers FETs and (b) SnY<sub>0.5%</sub>O nanofibers FETs.



**Fig. S6** Schematic Representation (a) TLC and (b) PB Conduction Mechanism. (c) Y 3d and (d) Sn 3d XPS spectrum with various Y doping ratios.

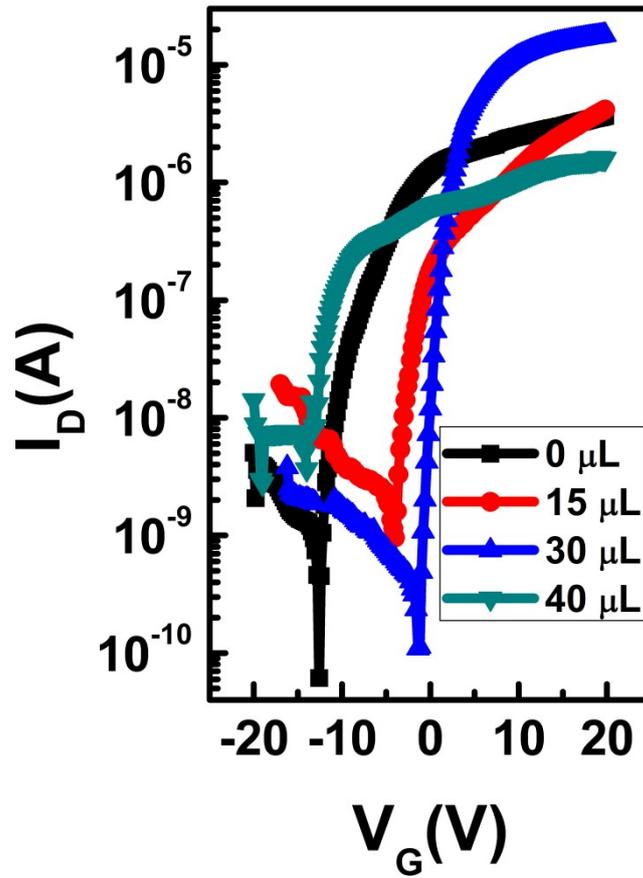


Fig. S7 Transfer curves of SnY<sub>0.5%</sub>O nanofibers FETs with different EA volumes.

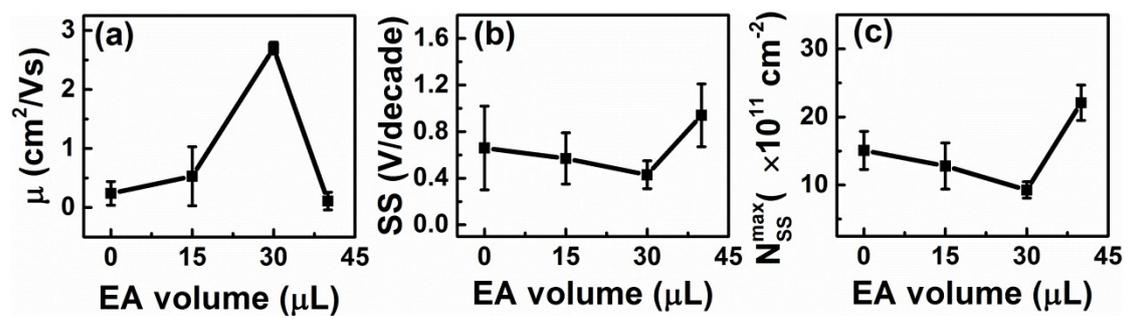
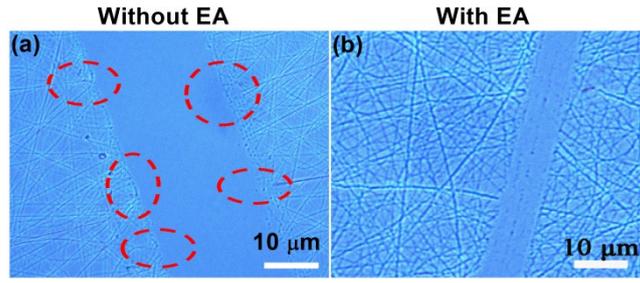
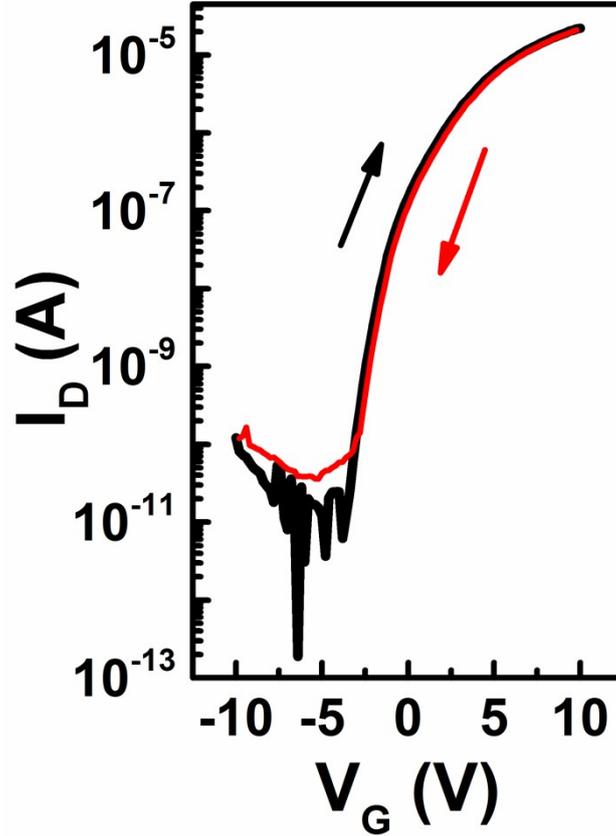


Fig. S8 The electrical parameters of SnY<sub>0.5%</sub>O nanofibers FETs with various EA contents.



**Fig. S9** Scraping tests of nanofibers (a) without and (b) with EA addition.



**Fig. S10** Transfer curves of SnYO nanofibers/ZrAlO<sub>x</sub> FETs ( $V_D=10$  V) measured with forward and reverse gate voltage sweeps

The mobility ( $\mu$ ) can be calculated by the following expression:

$$I_D = \frac{WC_i}{2L} \mu (V_G - V_T)^2 \quad (3)$$

where  $C_i$  (24 nF/cm<sup>2</sup>) is the areal capacitance of the SiO<sub>2</sub> dielectric,  $W$  and  $L$  are the channel width and length, respectively.  $V_G$  and  $V_T$  are the gate voltage and threshold voltage, respectively. The

subthreshold voltage ( $SS$ ) can be calculated by the following equation:

$$SS = \frac{dV_G}{d\log(I_D)} \quad (4)$$

The maximum trap density ( $N_{SS}^{max}$ ) can be obtained using,

$$N_{SS}^{max} = \left[ \frac{SS \log(e)}{kT/q} - 1 \right] \frac{C_i}{q} \quad (5)$$

where  $k$ ,  $T$  and  $q$  represent Boltzmann's constant, absolute temperature and charge quantity, respectively.