## Correlation between tetragonality (c/a) and direct current (dc) bias characteristics of BaTiO<sub>3</sub>-based multi-layer ceramic capacitors (MLCC)

## **Supplementary Information**

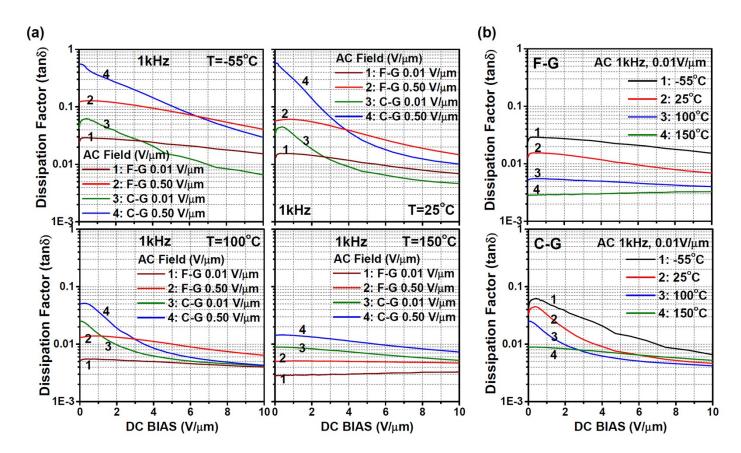


FIG. S1. (a) Dielectric loss versus dc-bias field of F-G and C-G specimens at the temperature of -55°C, 25°C, 100°C, and 150°C, and (b) the replotted data of FIG. S1. (a) for F-G and C-G specimen separately.