

Electronic Supporting Information

High-performance vertical field-effect transistors based on all-inorganic perovskite microplatelets

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1. Experimental details

1.1 Synthesis of CsPbBr₃, MoS₂ and Graphene:

All the reagents were purchased from Aladdin without further purification. CsPbBr₃ microplatelets were synthesized in a single-zone tube furnace with a 1-inch quartz tube. The substrate was placed inside the downstream of the quartz tube. To prepare the precursors, the PbBr₂ powder and CsBr powder with a molar ratio of 1:1 are mixed in a ceramic boat placed in the center of the furnace. The whole system was kept at the pressure of 10 Pa with a continuous 35 sccm flow of high purity Ar (99.99%). The furnace temperature was raised up to 575 °C at a ramping rate of 25 °C/min. After the temperature was held for 15 min, the furnace was then naturally cooled to room temperature.

To synthesize MoS₂, the CVD method was performed in a two-temperature-zone tube furnace with a 2-inch quartz tube. The substrates were placed inside the downstream of the quartz tube. MoO₃ powder (30 mg) was placed in a ceramic boat and inserted the heating zone near the substrate, and pure Sulfur powder (1 g) was placed in another ceramic boat at the upper stream side maintained at 130 °C. Afterward, a mixed gas of Ar and H₂ (volume ratio of Ar and H₂ was 95:5) was passing through the tube at a low pressure of 50 Pa. The reaction temperature was increased from room temperature to 785 °C and then held for 15 minutes.

As for graphene, a single-zone tube furnace with a 2-inch quartz tube was used. The copper foil was first placed in a dilute hydrochloric acid solution and was then sonicated for 15 minutes with an ultrasonic cleaner to remove the surface oxide layer. Afterward, it was placed in the center of the furnace. A mixed gas of CH₄ and H₂ (volume ratio of CH₄ and H₂ was 2:1) was passing through the tube and the whole system was kept at the pressure of 100 Pa. The reaction temperature was increased from room temperature to 800 °C and then held for 1 h.

1.2 Fabrication of CsPbBr₃ VFETs

Firstly, the graphene was transferred from Cu foils to Si/SiO₂ substrates by a well-developed wet transfer method¹. The CsPbBr₃ microplatelets were grown on the graphene substrate by CVD above-mentioned. Then utilizing a height difference between the CsPbBr₃ single-crystal and the substrate, 100 nm Au electrons were prepared by direct vacuum evaporation to achieve the vertical structure of the transistors.

1.3 Fabrication of Inverters

Firstly, the graphene, MoS₂ and graphene were transferred to SiO₂/Si substrates by a dry transfer method¹, respectively. Then CsPbBr₃ single-crystals were prepared with the graphene/MoS₂/graphene/SiO₂/Si substrate by van der Waals epitaxy in the single-zone tube furnace. Afterward, the Au (100 nm) electrodes were evaporated to obtain the inverters.

1.4 Characteristics of CsPbBr₃ Microplatelets

Optical images were captured by Olympus BX51. SEM and AFM images were taken by FEI Quanta 250FEG and a Bruker Multimode 8 AFM system, respectively. The optical spectra were characterized by PL spectroscopy (HORIBA, HRi 320, 442 nm laser wavelength), and UV–VIS (Shimadzu, UV3600). XRD patterns were recorded using a Bruker D8 multipurpose system.

1.5 Device Measurement

The electrical properties of the samples were measured by a Keysight B1500A Semiconductor Device Analyzer at room temperature under ambient conditions.

2. Supplemental Figures

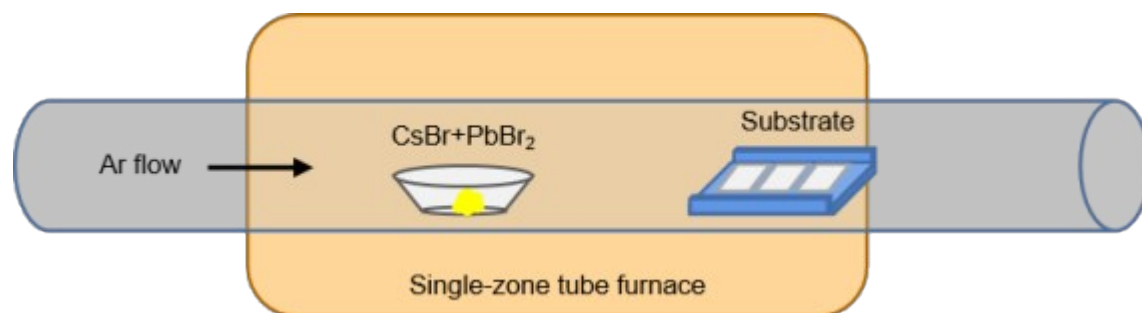


Figure S1. Schematic diagram of preparation of CsPbBr₃ microplatelets.

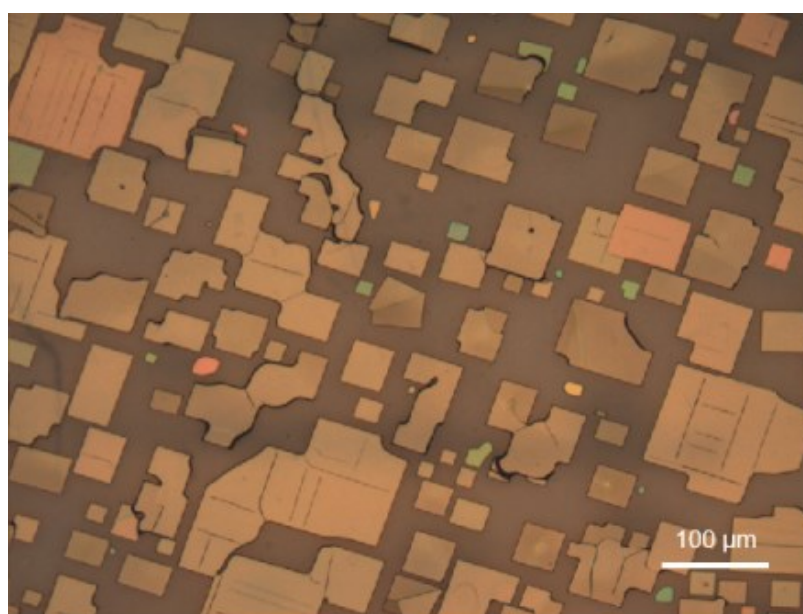


Figure S2. OM image of the CsPbBr₃ microplatelets grown by CVD.

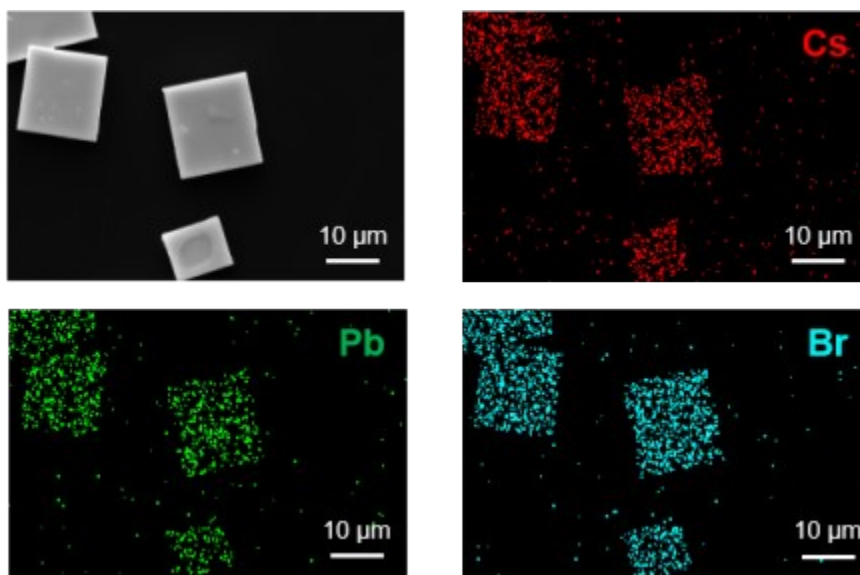


Figure S3. SEM-EDS mapping of CsPbBr₃ single-crystals.

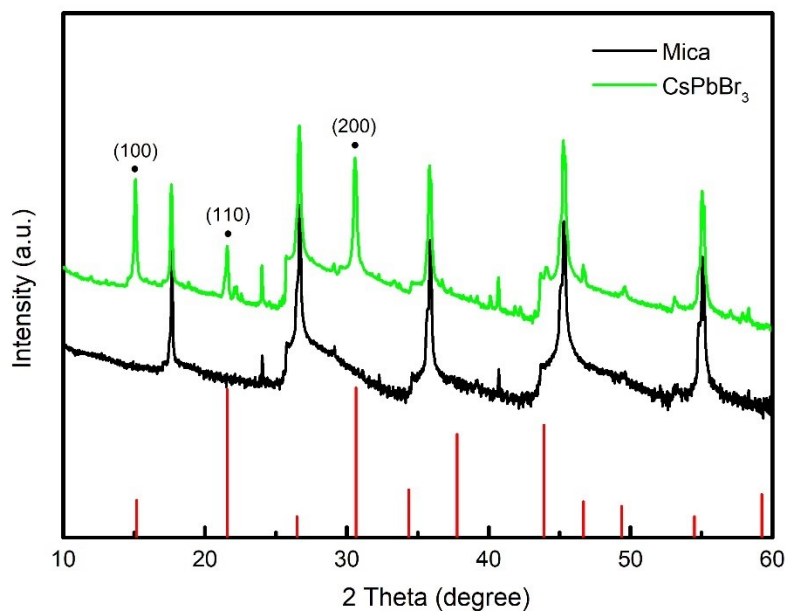


Figure S4. XRD spectra of CsPbBr₃ microplatelets (green line) on the Mica and Mica (black line). Red line represents the PDF standard card of CsPbBr₃ cubic phase.

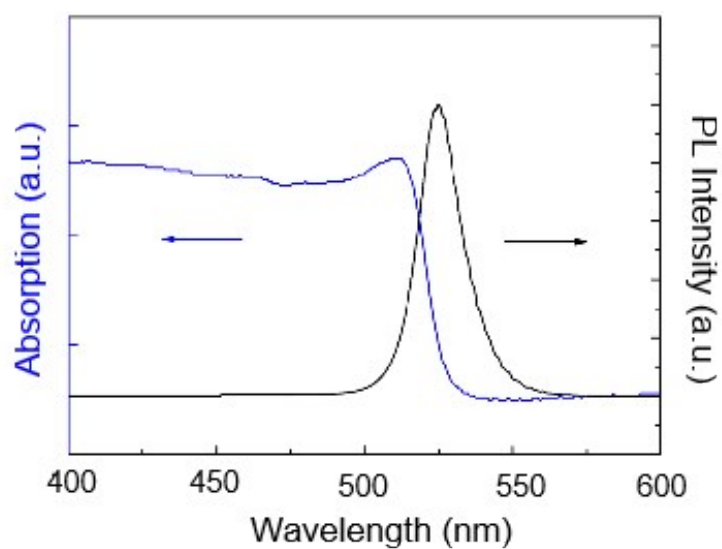


Figure S5. Absorption and photoluminescence spectra of the CsPbBr₃ single-crystal.

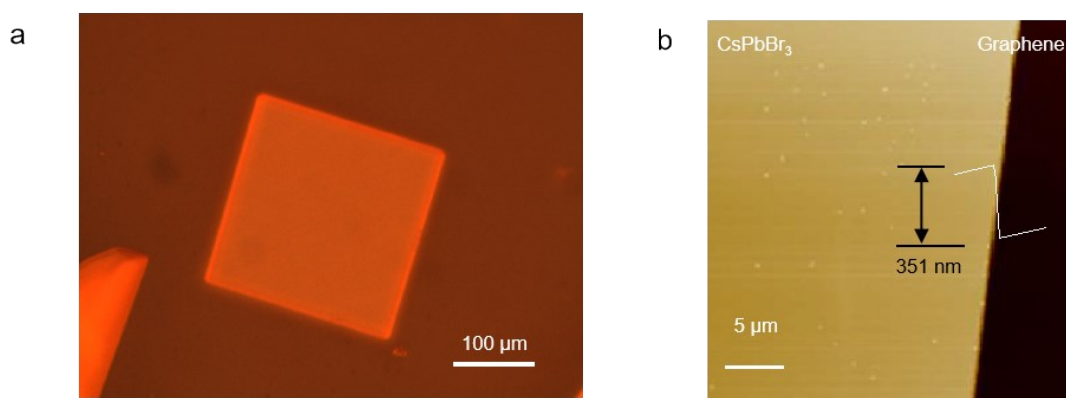


Figure S6. (a) OM image of the CsPbBr₃ microplatelet after evaporation. The lateral size of the single-crystal is above 200 μm. (b) AFM image of the CsPbBr₃ microplatelet. The thickness of the CsPbBr₃ microplatelet is about 351 nm.

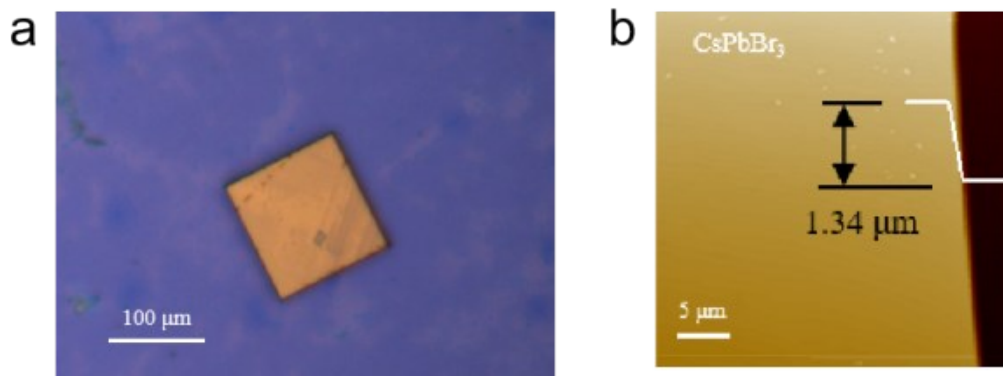


Figure S7. a) OM image of the CsPbBr₃ microplatelet on Graphene substrate. b) AFM image of the CsPbBr₃ microplatelet. The thickness of the CsPbBr₃ microplatelet is about 1.34 μm.

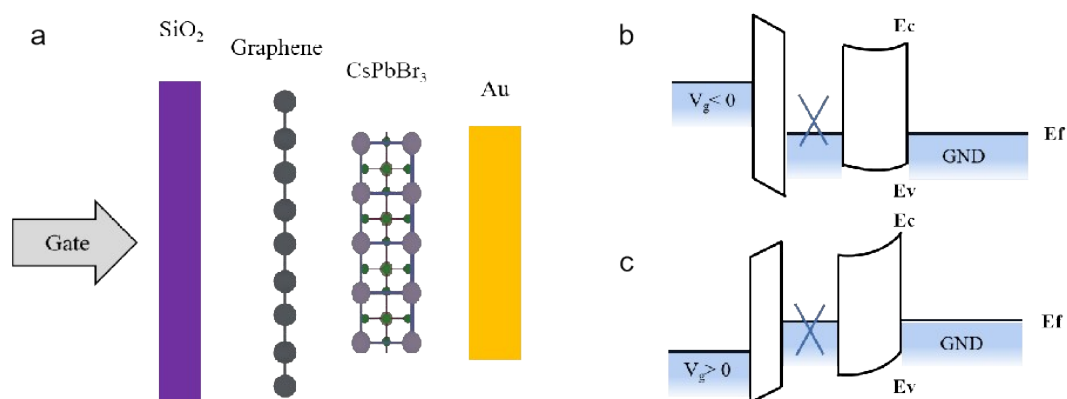


Figure S8. (a) Schematic structure of the CsPbBr₃-based VFET. Gate electric field is applied from silicon back gate. (b) The band structure under negative gate voltage (b, $V_g < 0$) and positive gate voltage (c, $V_g > 0$) at zero source-drain bias with the top Au electrode connected to ground.

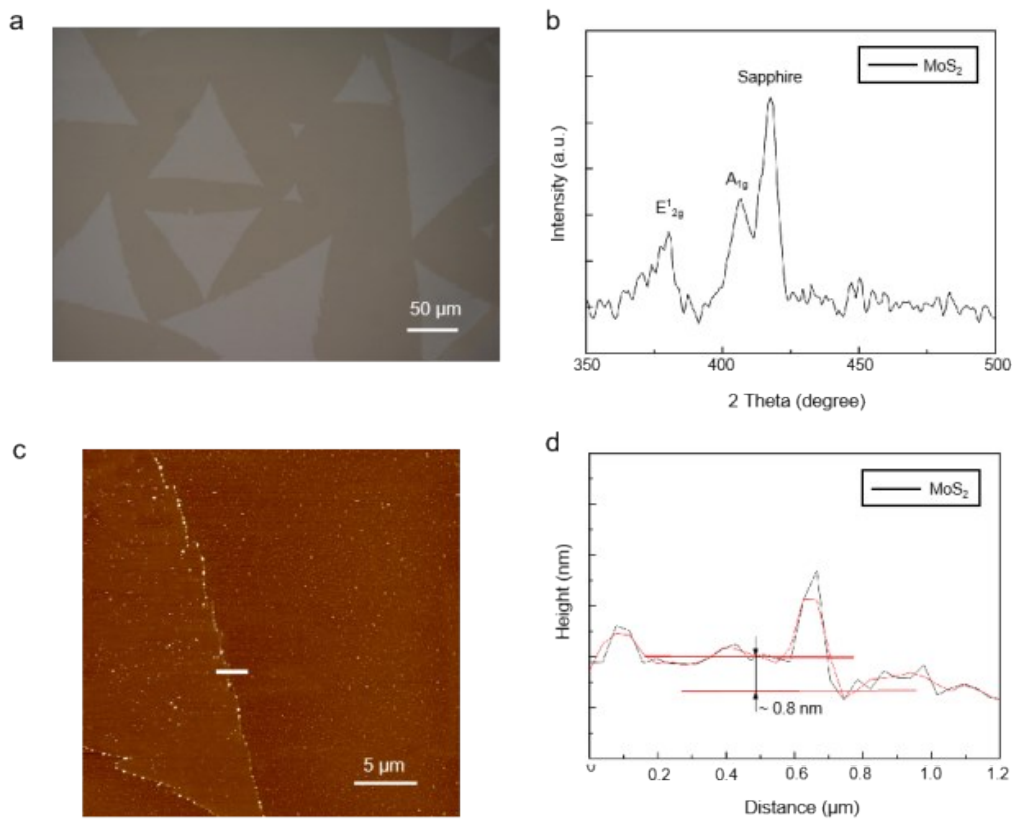


Figure S9. (a) OM image of the MoS₂ simples prepared by CVD. (b) Raman spectrum of the MoS₂ shown in (a). (c) AFM image of the MoS₂ simple. (d) AFM height profile of the MoS₂ simple shown in (c).

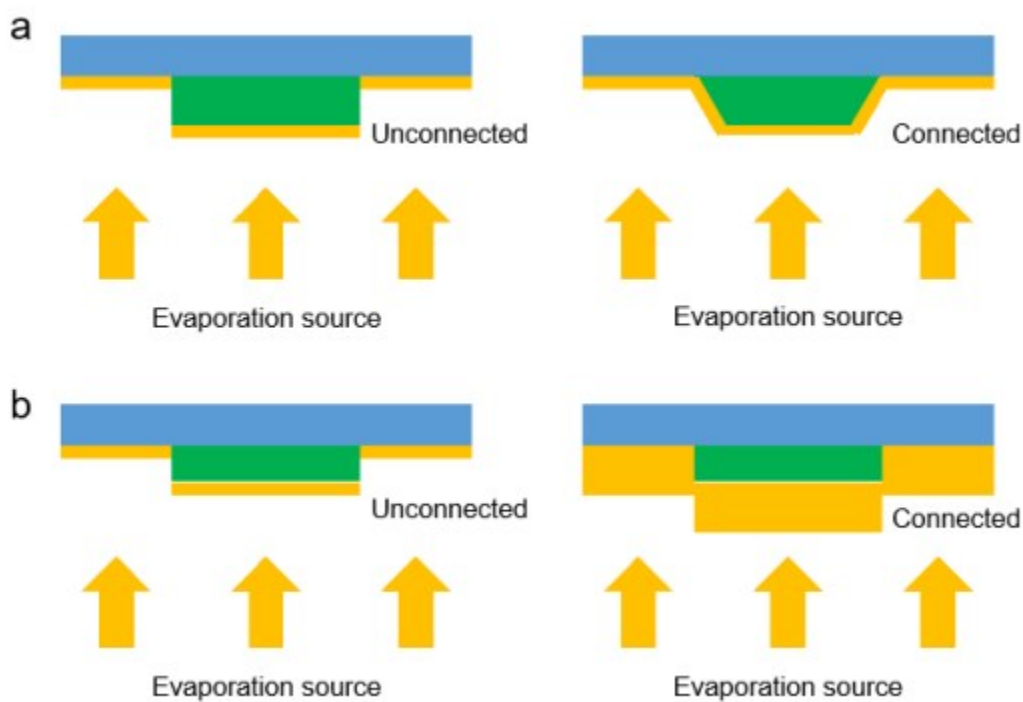


Figure S10. Schematic diagram of the metal deposition process. (a) The schematic diagram of vapor deposition when the quality of the CsPbBr₃ single-crystal is poor, (b) when the deposited metal is too thick.

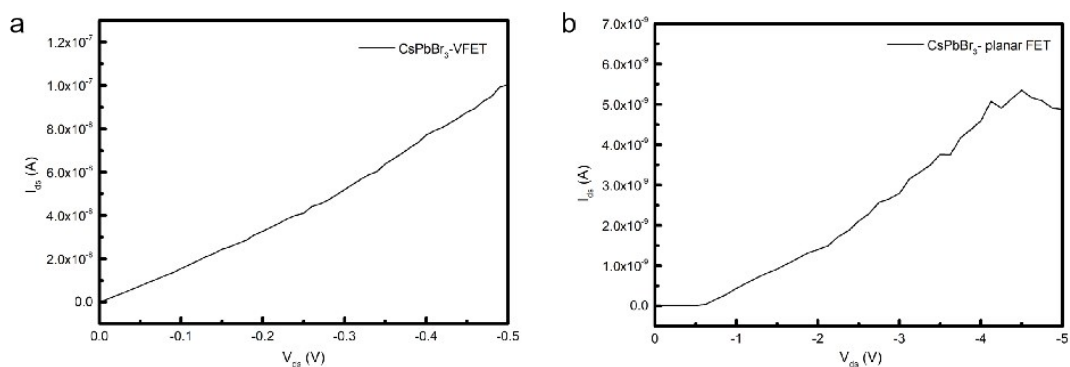


Figure S11. I_{ds} - V_{ds} curves of the CsPbBr₃-based VFET (a) and the CsPbBr₃-based planar FET (b). The back gate voltage is 0 V. The contact resistance of CsPbBr₃ VFET and planar FET is about 1.2×10^{17} and $4 \times 10^{17} \Omega/\text{m}^2$.

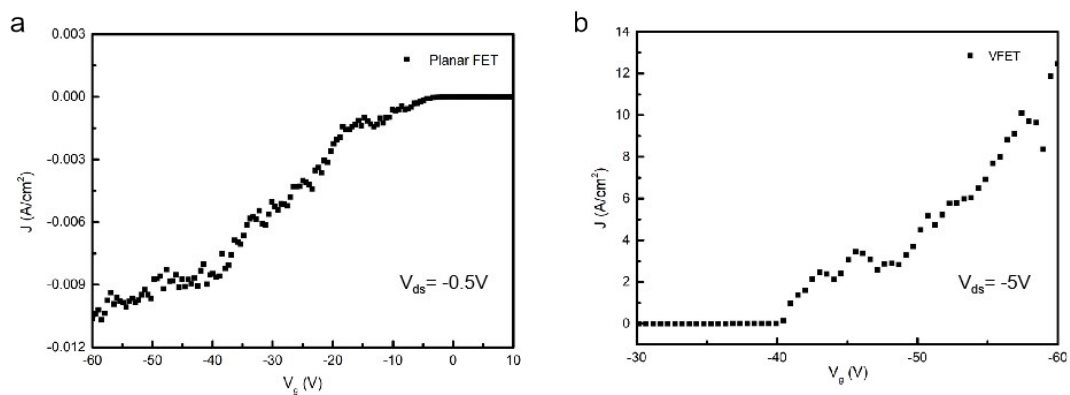


Figure S12. Current density - Gate voltage (J - V_g) transfer curves of the CsPbBr₃-based VFET (a) and the CsPbBr₃-based planar FET (b).

Table S1. Key parameters comparing different channel materials based VFET.

Materials	Thickness (nm)	on/off ratio	Current density	Reference
n-IGZO/p-Ge	10-20	10^6	1.72 $\mu\text{A}/\mu\text{m}^2$	Ref.2
IGZO	<100	10^5	6 $\mu\text{A}/\mu\text{m}^2$	Ref.3
P3HT	<200	$>10^3$	3.4 A/cm^2	Ref.4
p-Si	~ 1	10^2	20 A/cm^2	Ref.5
bis(1,2,5-thiadiazolo)-p-quinobis(1,3-dithiole) (BTQBT)	100	10^5	1 A/cm^2	Ref.6
InAs	~ 45	11	45000 A/cm^2	Ref.7
CsPbBr ₃ NCs	160	-	0.25 A/cm^2	Ref.8
MoS ₂	36	10^3	5000 A/cm^2	Ref.9
CsPbBr ₃ microplatelts	350	10^6	12.3 A/cm^2	Our work

Table S2. Key parameters comparing planar FET and VFET.

Type	Channel length	on/off ratio	Current density
Planar FET	20 μm	5000	0.01 A/cm^2
VFET	350 nm	10^6	12.3 A/cm^2

Reference

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