

Electronic Supplementary Information

High performance near infrared phototransistor via enhanced electron trapping effect

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1. Experimental section

Film fabrication and characterization: The heavily doped p-type silicon wafer with a 300 nm thermal oxide SiO₂ layer was used as the substrate. OTS modification was processed to via vacuum deposition approach at 100 °C for 60 mins. Dielectric capacitance is about 11.5 nF/cm² for bare and 11 nF/cm² for OTS modified SiO₂/Si substrate. DPPDTT and DCV3T were purchased from Lumtec company and used directly. The 6 mg/ml DPPDTT solution with chlorobenzene as solvent was stirred for 60 mins at 60 °C and then spin-coating at a speed of 3000 rpm for 30s to obtain the pure DPPDTT film. DCV3T solution with the same concentration and solvent was fabricated as well. For the blended films, the DPPDTT solution and DCV3T solution were mixed with different ratio by volume and stirred for 30 mins. Then the mixed solution was spin-coated using the same condition. All films were annealed at 90 °C for 30 mins. The AFM images were recorded using the Bruker dimensional Icon system in tapping mode. UV-vis absorption spectrum was conducted via the Lambda 950. The 2D-Grazing incident X-ray diffraction was conducted at Shanghai Synchrotron Radiation Facility.

Device fabrication and measurement: 40 nm thick gold (Au) was thermally deposited as source and drain electrodes on the top of semiconducting film through a shadow mask to form the top-contact bottom gate thin film transistors (W= 1 mm, L= 50 μm). LED source of 740 nm, 850 nm and 940 nm was controlled by the system. The transistor performance was measured using the Keithley 4200SCS integrated with a lakeshore vacuum probe station. During measurement, the device is in ambient condition. Phototransistor parameter calculation was performed according to the following equations.

$$R = (I_{ill} - I_{dark})(P_i A)^{-1} \quad (1)$$

$$D^* = \frac{1}{RA^2} \frac{1}{(2qI_{dark})^2} \quad (2)$$

$$P = \frac{I_{ph}}{I_{dark}} = \frac{I_{ill} - I_{dark}}{I_{dark}} \quad (3)$$

of which the I_{ill} and I_{dark} represent the I_D current under illumination and in dark, P_i is the light power intensity, A is the channel area, q is elementary charge of electron. D^* is using the one that only put shot noise in consideration.

2. Additional data

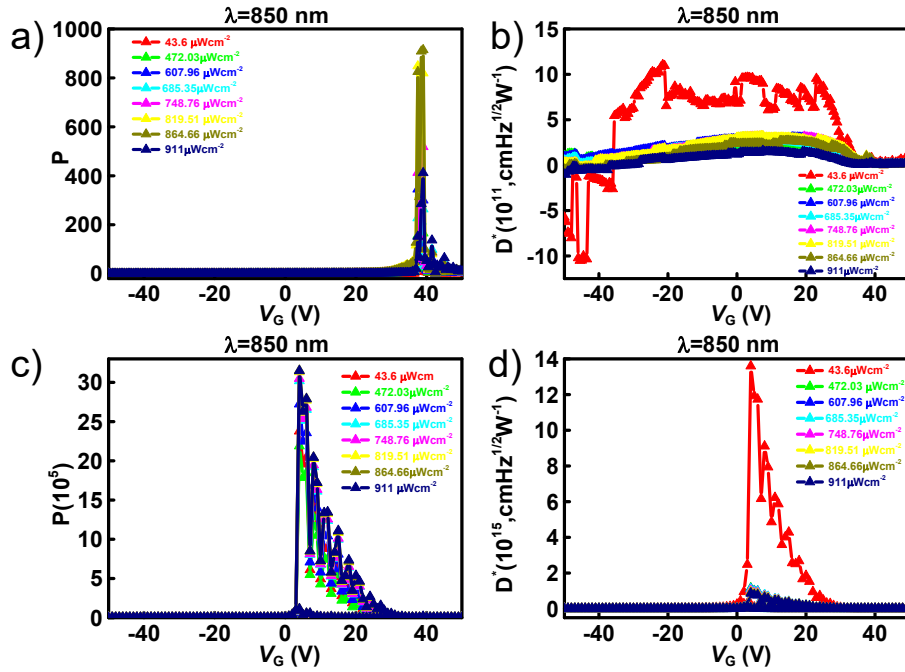


Figure S1. The photosensitivity and detectivity plot with gate voltage for pure DPPDTT (a-b) and DPPDTT:DCV3T(9:1)

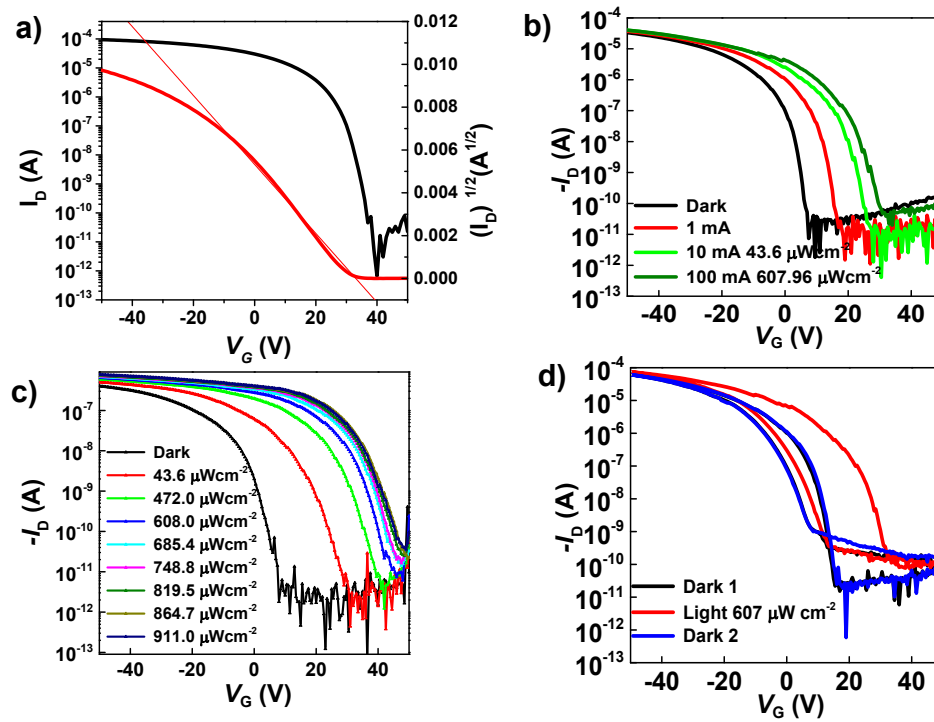


Figure S2. a) the transfer and $I_D^{1/2}$ vs gate voltage plots of DPPDTT:DCV3T (9:1) transistors, the plots indicates that at high negative positive voltage, the device charge mobility is reduced. b) the transfer curve evolution upon illumination by the light with operation current from 1 mA to 100 mA of DPPDTT:DCV3T (9:1) on OTS modified substrate. c) The photoresponse of DPPDTT:DCV3T (9:1) on bare Si/SiO₂ substrate, a clearly gradual shift of the V_T and current increment was observed upon illumination by 850 nm light with different power intensity. d) transfer curve of a testing cycle from the initial dark, the first illumination and then the second dark state. After illumination, the transfer of dark condition recovers to the level of the initial dark condition.

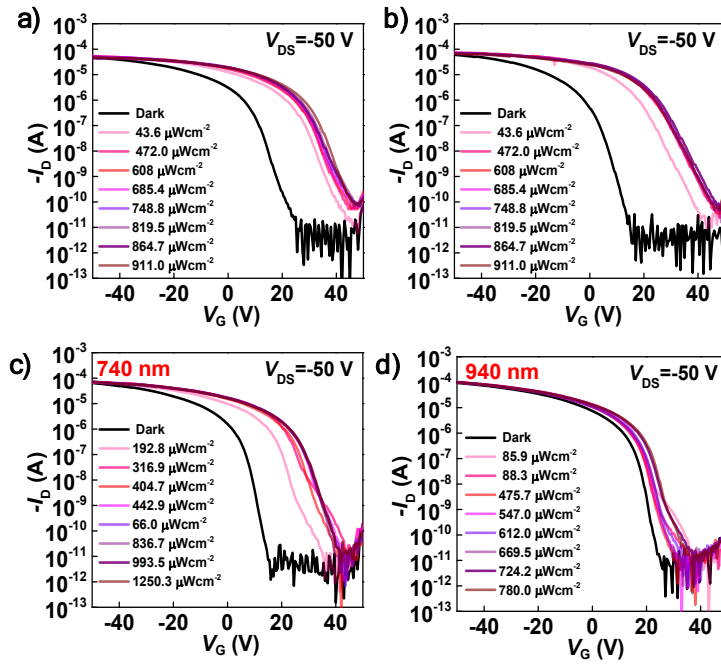


Figure S3. Transfer curve evolution of a) DPPDTT:DCV3T (3:1) and b) DPPDTT:DCV3T (1:1) film upon illumination by 850 nm; Transfer curve evolution of DPPDTT:DCV3T(9:1) upon illumination by c) 740 nm and d) 940 nm.

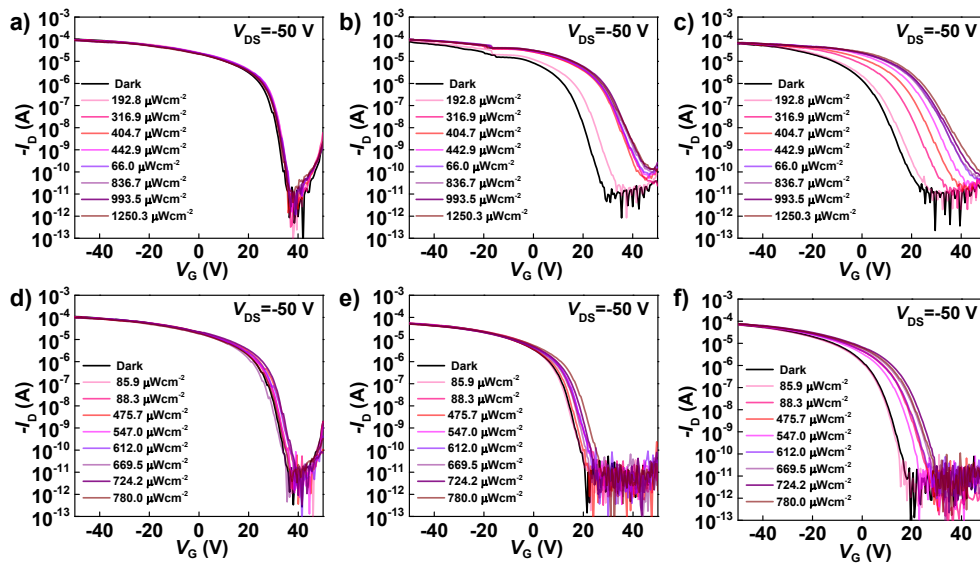


Figure S4. transfer curve evolution upon light irradiation at 740 nm (a-c) and 940 nm (d-f) of a, d) pure DPPDTT, (b, e) DPPDTT:DCV3T(3:1); (c, f) DPPDTT:DCV3T(1:1).

Table S1. Performance comparison of our work and typical organic NIR phototransistors reported previously.

Materials	Morp h- ology	Light/power	R (AW ⁻¹)	P _{max}	D* (Jones)	Ref.
TFT-CN	2D single crystal	808 nm / (2.5 μWcm ⁻²)	9*10 ⁴	5*10 ⁵	6*10 ¹⁴	Adv. Mater. 2018, 30, 1706260
PDPPBTT: PCBM	Films	820 nm / (0.031 mWcm ⁻²)	5.26*10 ³	10 ⁶	8.21*10 ¹⁶	Adv. Opt. Mater. 2021, 9, 2002031
BODIPY	Films	850 nm / (0.5 mWcm ⁻²)	1.14*10 ⁴	1.04*10 ⁴	-----	Adv Electron Mater. 2017, 3, 1600430
DPPDPTT: PCBM	Films	808 nm / (67 nW cm ⁻²)	8*10 ⁵	3*10 ⁴	3.5*10 ¹²	Nanoscale, 2013, 5, 11850
CuPc/PbPc :PTCDA	Films	808 nm / (1 mW cm ⁻²)	0.322	9.4*10 ²	-----	Org. Electron. 2013, 14, 1045
PDIBDF- TT	Nano wire	808 nm / (47 mWcm ⁻²)	0.44	3.3*10 ⁴	-----	Nanoscale, 2016, 8, 7738
PDVT: PCBM	Films	760 nm / (20 μWcm ⁻²)	5.7*10 ⁵	5*10 ⁷	1.15*10 ¹⁸	ACS Mater. Interface, 2021, 13, 1035
PQT-12/ F ₄ -TCNQ	Films	2000 nm / (90 μWcm ⁻²)	2.75 *10 ⁶	6.8*10 ⁵	3.12*10 ¹⁴	Adv. Funct. Mater. 2021, 2103787
DPPDPTT: DCV3T (9:1)	Film	850 nm / (43 μWcm ⁻²)	2*10 ³	3*10 ⁶	1.4*10 ¹⁶	This work

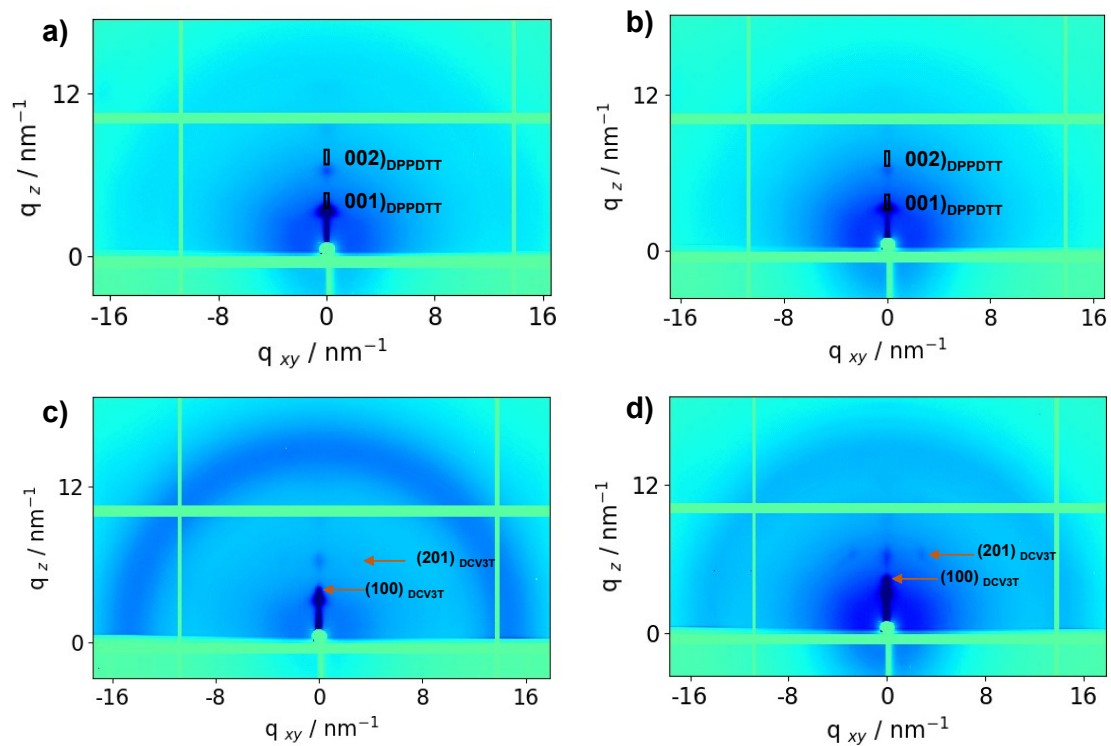


Figure S5. 2D-GIWAXS of pure DPPDTT and the DPPDTT:DCV3T blending films. a) pure DPPDTT, b) DPPDTT:DCV3T (9:1), c) DPPDTT:DCV3T (3:1), d) DPPDTT:DCV3T (1:1).