

## Electronic Supplementary Information (ESI)

### One-dimensional van der Waals stacked p-type crystal Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> for nanoscale electronics

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## TABLE OF CONTENTS

### Calculation of inter-ribbon binding energy

### Supporting Figures

**Fig. S1.** Reaction temperature-dependent synthesis of Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals

**Fig. S2.** XRD analysis of vapor-grown bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals

**Fig. S3.** EDS analysis of bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals

**Fig. S4.** Thermal stability of bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals

**Fig. S5.** Calculation of the electronic band structure of bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub>.

**Fig. S6.** 7 nm-thick Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> FET

**Fig. S7.** Ti contact analysis

**Fig. S8.** Device stability analysis

**Fig. S9.** Modeling of Schottky barrier height

**Fig. S10.** P-N homogenous junction fabrication schematic

**Fig. S11.** P-N homogenous junction by partial encapsulation of Al<sub>2</sub>O<sub>3</sub> layer

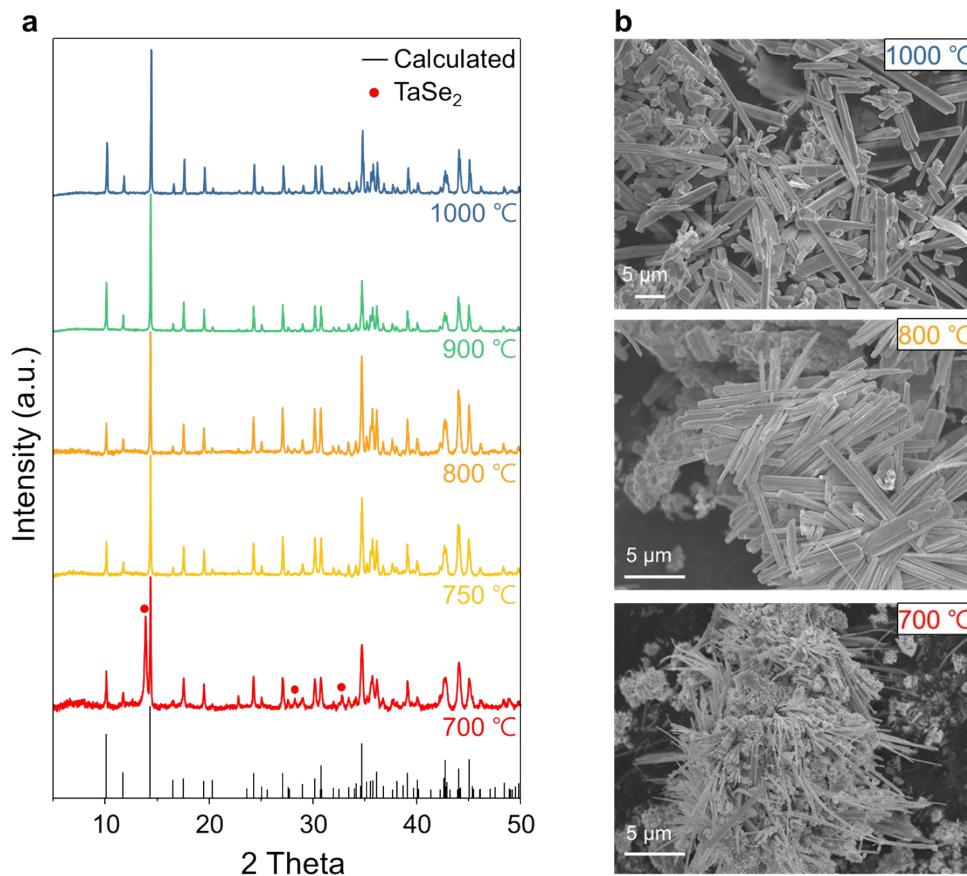
### Calculation of inter-ribbon binding energy

To understand the structural dissociative property, we calculated the inter-ribbon bonding energy using the following formula:

$$E_b = - [E_{tot}(bulk) - \{2 \times E_{tot}(1D \text{ single ribbon})\}] / 4$$

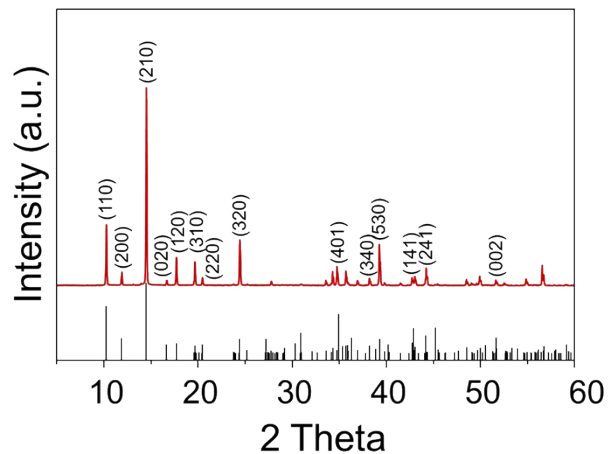
The molecular ribbons in bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> have inter-ribbon bonding energy of 0.37 eV/atom, which is comparable to the binding energy of 0.24 eV/atom of hydrogen bond-type vdW forces between water molecules, as presented in Table S1. Therefore, we can expect that the weak vdW bonding between molecular ribbons provides the possibility of cleavage of bulk crystals into thinner nanoribbons by mechanical exfoliation. The following table shows the inter-ribbon bonding energy of bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub>.

## Reaction temperature-dependent synthesis of $\text{Ta}_2\text{Pt}_3\text{Se}_8$ crystals



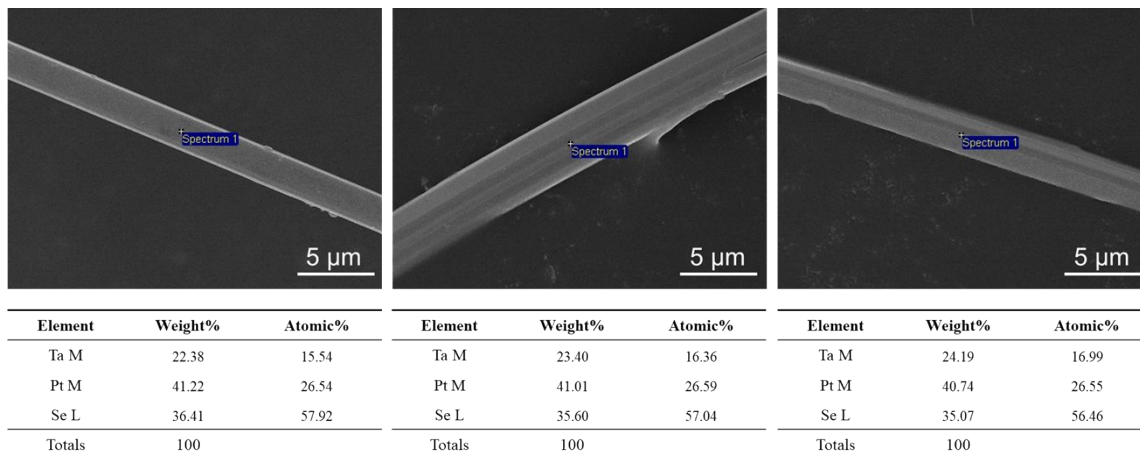
**Fig. S1** Reaction temperature-dependent experiment at a stoichiometric composition (Ta: Pt: Se = 2: 3: 8). (a) XRD results of samples synthesized with an increase in temperature from 700 to 1000 °C. (b) SEM images of the synthesized samples with different reaction temperatures.

## XRD analysis of vapor-grown bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals



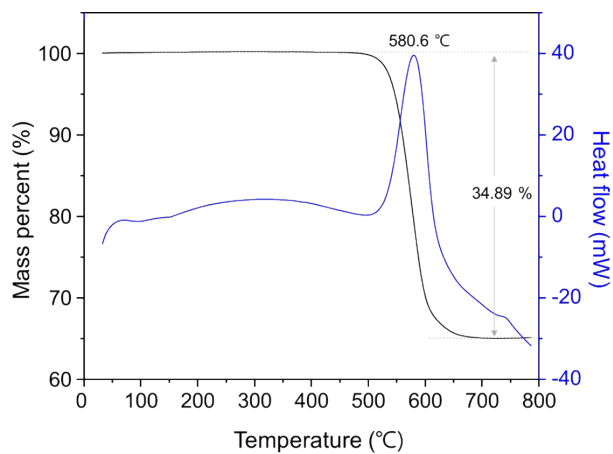
**Fig. S2** XRD result of vapor-grown large Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals synthesized at 1000 °C with Se excess condition (Ta: Pt: Se = 2: 3: 10). The samples obtained from this condition were used in further characterization and device fabrication.

## EDS analysis of bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals



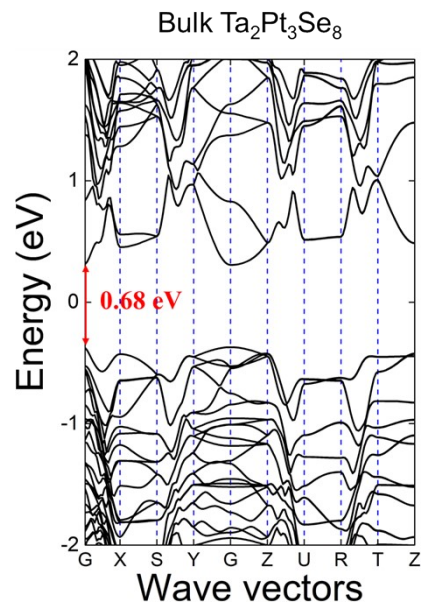
**Fig. S3** EDS data of the bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals on multiple positions at different locations of the sample, representing atomic percent close to Ta: Pt: Se = 2: 3: 8.

## Thermal stability of bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals



**Fig. S4** TG-DSC measurement of bulk Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> crystals

## Calculation of the electronic band structure of bulk $\text{Ta}_2\text{Pt}_3\text{Se}_8$

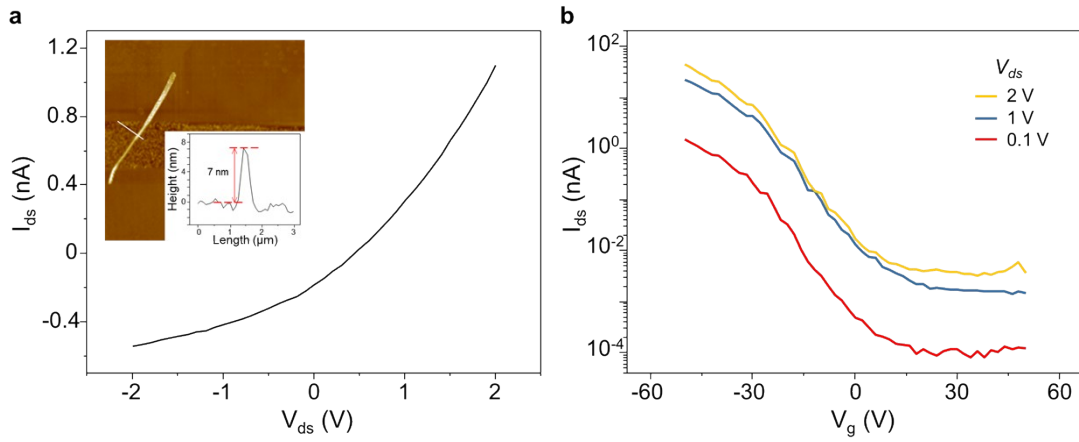


**Fig. S5** Calculated electronic band structure of bulk  $\text{Ta}_2\text{Pt}_3\text{Se}_8$ .



## 7 nm-thick Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> FET

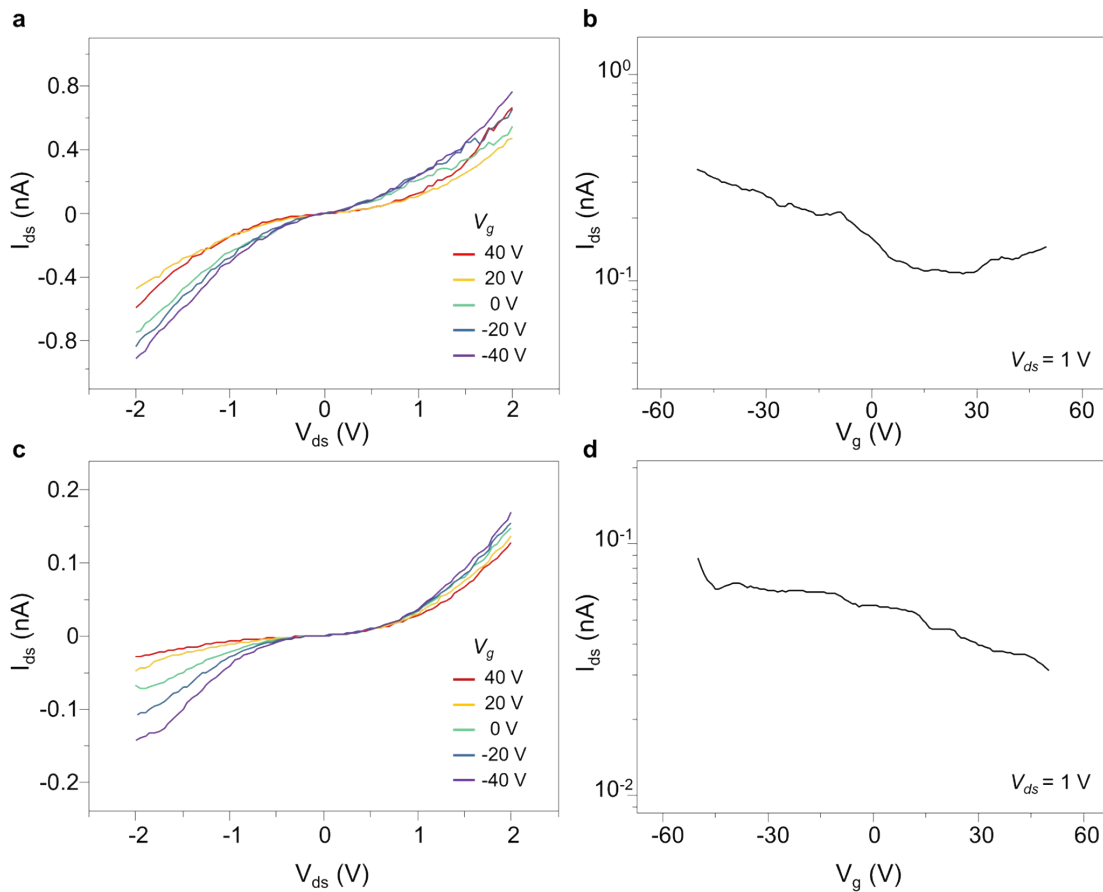
The work function of Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> gradually decreased as the thickness of Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> decreased. In the field-effect measurement of 7 nm thick Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> nanoribbon, we observed a slight Schottky behavior in the I-V curve and a typical p-type characteristic in the transfer curve. The extracted mobility was recorded as 1.6 cm<sup>2</sup> V<sup>-1</sup>s<sup>-1</sup> at V<sub>ds</sub> of 1 V, and the I<sub>on</sub>/I<sub>off</sub> ratio exceeded 10<sup>4</sup>.



**Fig. S6** 7 nm-thick Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> FET electrical characteristics. (a) I-V curve of 7 nm-thick Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub>. Inset shows AFM image with its height profile. (b) P-type transfer characteristic of 7 nm-thick Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> at RT.

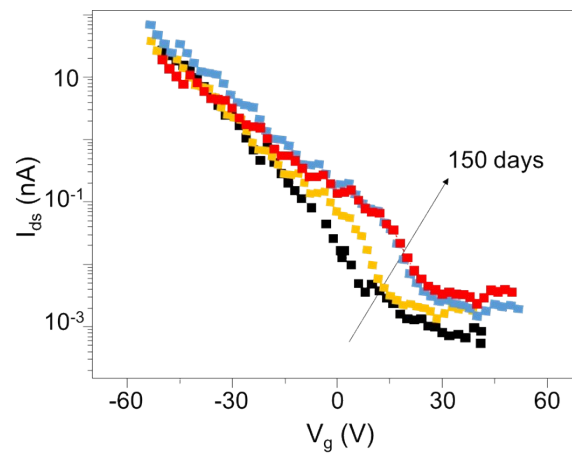
## Ti contact analysis

We tested the field-effect characteristics of several multilayer  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  FETs using Ti/Au (20 nm / 60 nm) as electrodes. The output curve of  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  showed a Schottky contact at all gate biases. In addition, the transfer curves of each device indicated that the  $I_{\text{on}}/I_{\text{off}}$  ratio and current level were significantly degraded compared to the previous results, that is, transfer curves using Ni/Au electrodes. Therefore, it is plausible that the Ti/Au electrode creates a high Schottky barrier with  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  owing to its low work function.



**Fig. S7** Ti contact  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  FET characteristic. (a) 28 nm-thick  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  output characteristic at different gate biases and (b) transfer characteristic at RT. (c) 16 nm-thick  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  output characteristic at different gate biases and (d) transfer characteristic at RT.

## Device stability analysis



**Fig. S8** Stable p-type transfer characteristic of Ta<sub>2</sub>Pt<sub>3</sub>Se<sub>8</sub> FET

## Modeling of Schottky barrier heights

Generally, to extract the formula for the Schottky barrier height, a thermionic emission current model was used to explain the charge barrier overcome between the metal and the semiconductor through a quantum-mechanical process. When  $V_{ds}$  is applied to the device, the potential barrier to be overcome by electrons is  $\Phi_{SB} - V_{ds}$  ( $\Phi_{SB}$  is a notation of Schottky barrier height). A generalization for n-dimensional materials considering an (n-1) dimension surface in k-space, the current density is given by

$$J_n = R_n T^{(n+1)/2} \exp\left(\frac{-q(\Phi_B - V_{ds})}{k_B T}\right) \quad (1)$$

where n is n-dimension.<sup>1</sup> The Richardson constant is given by:

$$R_n^* = 2^{(n+1)/2} \pi^{(n-1)/2} m^{(n-1)/2} k_B^{(n+1/2)} h^{-n} q \quad (2)$$

Therefore, in 2D, the temperature term in the prefactor changes to  $T^{3/2}$  and in 1D as T. Through equation 2, we can derive the 2D and 1D thermionic emission equation:

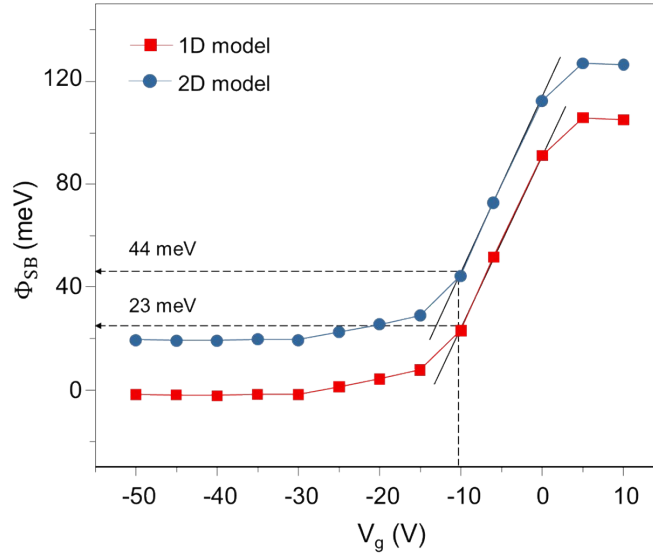
2D thermionic emission:

$$I_{2D} = A_c R_{2D}^* T^{3/2} \exp\left[\frac{-q(\Phi_{SB})}{k_B T}\right] \left[\exp\left(\frac{qV_{ds}}{k_B T}\right)\right], \quad R_{2D}^* = \frac{q(8\pi m^* k_B^3)^{1/2}}{h^2} \quad (3)$$

1D thermionic emission:

$$I_{1D} = A_c R_{1D}^* T \exp \left[ \frac{-q(\Phi_{SB})}{k_B T} \right] \left[ \exp \left( \frac{qV_{ds}}{k_B T} \right) \right], \quad R_{1D}^* = \frac{2qk_B}{h} \quad (4)$$

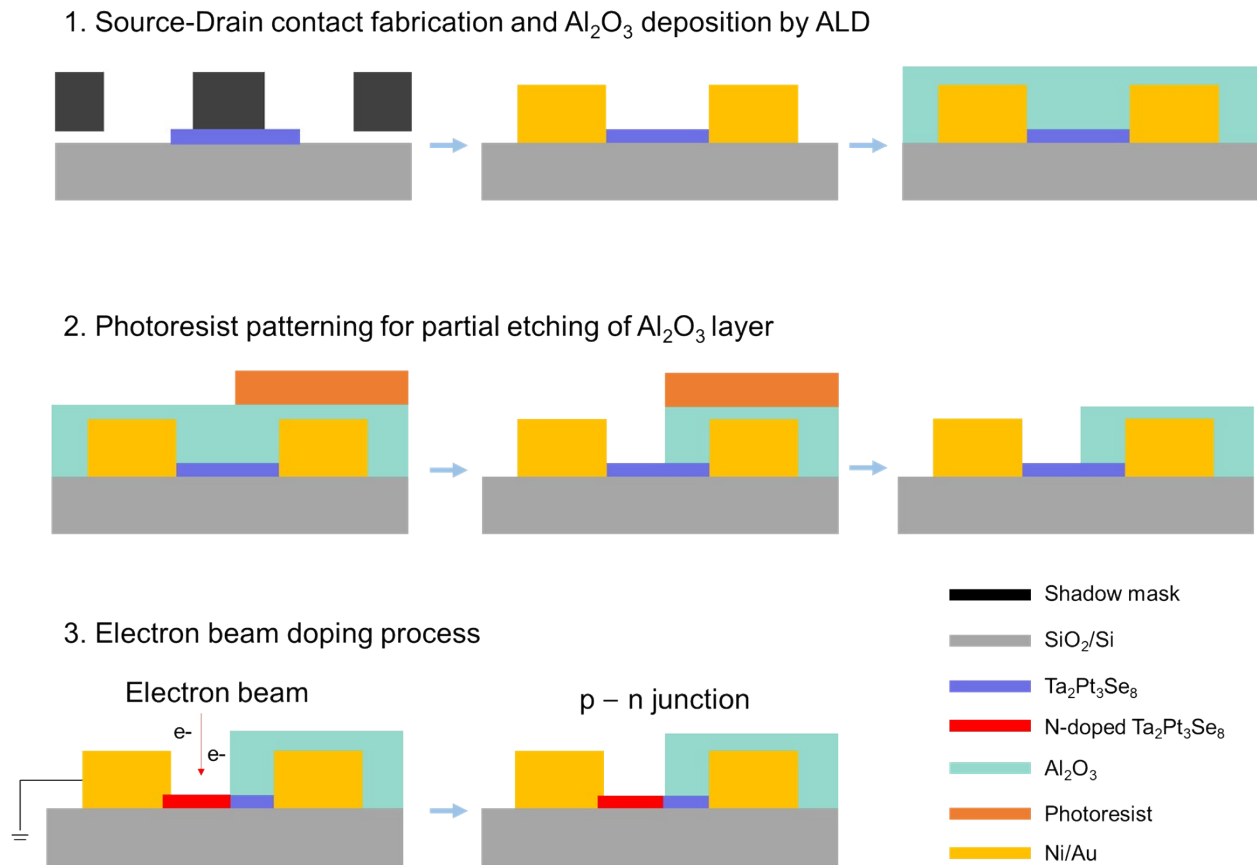
where  $A_c$  is the contact area,  $R^*$  is the Richardson constant,  $k_B$  is the Boltzmann constant, and if  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  is considered as a 1-dimension material, the 2D model could be adjusted to explain its transport mechanism such that the SBH was extracted using  $\log(I/T^{3/2})$  as a function of the  $1/T$  plot. As shown in Fig. S8 each SBH of the hole extracted from the two models shows almost the same value as the difference of  $\text{SBH}_{\text{hole}}$  21 meV.



**Fig. S9.** Modeling of Schottky barrier heights.

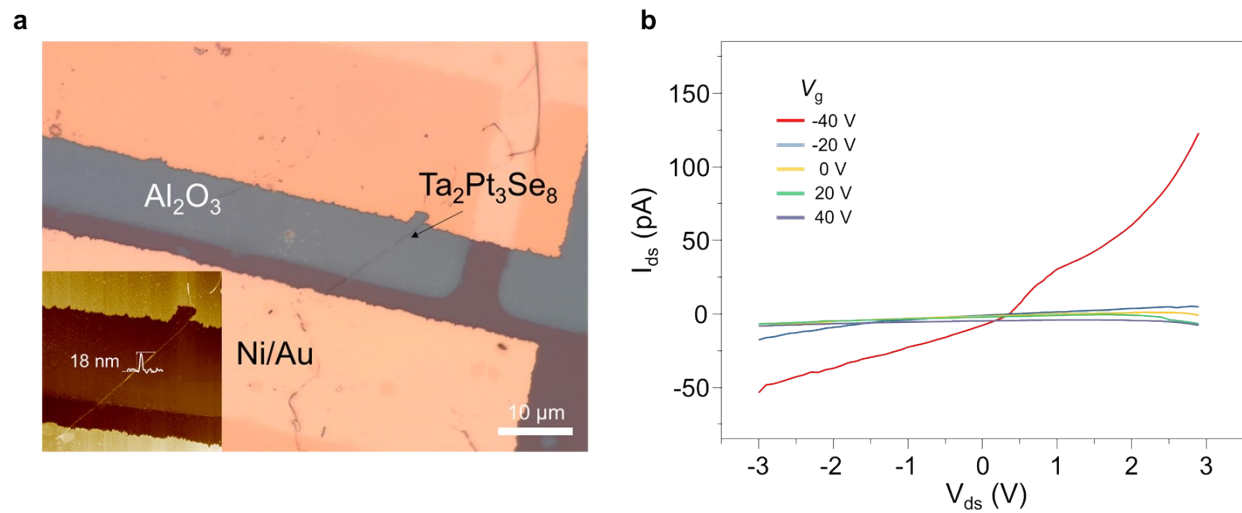
## P-N homogenous junction fabrication schematic

Before the formation of a partially encapsulated device structure to show the P-N diode characteristics, we made source-drain contact to  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  using a TEM grid. Subsequently, the atomic layer deposition process of the  $\text{Al}_2\text{O}_3$  layer was performed. Next, using a typical photolithography method, a photoresist pattern was placed on the  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  nanoribbon with only half of the wire covered. To form a partially encapsulated device, the air-exposed  $\text{Al}_2\text{O}_3$  layer was etched by a 6:1 buffered oxide etchant bath for 5 s. Finally, the electron beam was irradiated onto the device with one electrode grounded to the metal holder during the e-beam doping process.



**Fig. S10**  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  homogenous p-n junction device fabrication schematic using the electron beam doping method.

## P-N homogenous junction by partial encapsulation of $\text{Al}_2\text{O}_3$ layer



**Fig. S11** Device structure and diode performance. (a) Optical microscopy images of partial  $\text{Al}_2\text{O}_3$  layer encapsulation structure for p-n homogenous junction  $\text{Ta}_2\text{Pt}_3\text{Se}_8$ . The inset shows the AFM image of device and  $\text{Ta}_2\text{Pt}_3\text{Se}_8$  thickness of 18-nm. (b) Diode performance of  $\text{Ta}_2\text{Pt}_3\text{Se}_8$ .

## REFERENCES

- (1) M. F. O'Dwyer, R. A. Lewis, C. J. Zhang, *Microelectronics J.* 2008, **39**, 597-600.