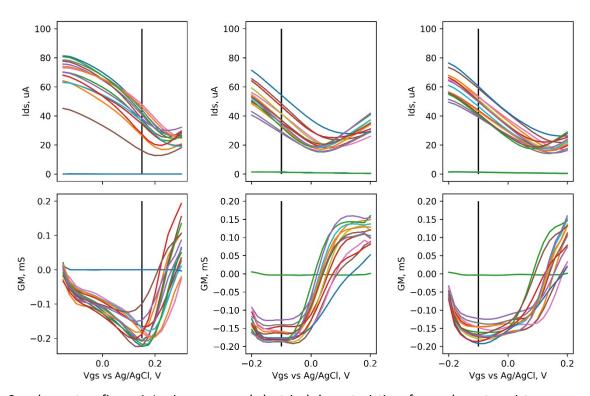
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Supplementary figure 1. In vivo measured electrical characteristics of a graphene transistors array with 16 channels measured at 50 mV drain-to-souce voltage for three different experiments. Top: Current (lds)-voltage characteristics for each of the gFETs in the array in each of the three experiments. Bottom: Calculated transconductange (GM: dlds/dVgs) for each of the gFETs in the array in each of the three experiments. Black vertical lines denotes the used Vgs bias point for that experiment.