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Electronic Supplementary Information

Highly Parallel Stateful Boolean Logic Gates based on Aluminum-Doped Self-Rectifying Memristors in Vertical Crossbar Array Structure

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Supplementary Section I. Self-rectifying memristors in the vertical array structure

The energy dispersive spectrometer (EDS) analysis in Figure S1(a) represents that Al is doped in the HfO_2 region. The resistive switching can occur in the lateral direction between Pt and TiN electrodes.



Figure S1. The vertical self-rectifying memristor. The energy dispersive spectrometer composition mapping of the D_{2t2} memristor.

Supplementary Section II. Parallel logic operations in ASM-two-2AND gates

When the bias of $V_{a,AND}$ is applied to BL1 connecting BL2 to the ground in Fig. 5(a) configuration, four FAS logic gates representing ASM-two-2AND gates can be realized. For each input case, the experimental demonstration is shown in Figure S2.



Figure S2. Parallel ASM-two-2AND gates in the two-layered 2 × 2 vertical crossbar array structure.

Supplementary Section III. Analysis of the sneak current

The sneak current path is formed by the three memristors in parallel to a target memristor in the reading process, as shown in Figure S3. After the logic operation in the ASM-two-2AND gate between D_{1t1} and D_{1t2} , the non-selected memristors (D_{2t1} and D_{2t2}) are switched to R_{on} state while D1t2 is assigned for the second input of the logic gate to maximize the sneak current path. Since the equivalent circuit for the sneak path in Figure S3(b) has the reversed memristor, the high forward-reversed rectification ratio can suppress the sneak current in the 2 × 2 vertical CBA.



Figure S3. The sneak current paths formed in the 2 \times 2 vertical CBA structure using floating scheme. (a) The schematic diagram of the vertical CBA when bias is applied to D_{1t1}. The sneak current flow to the reversed memristor D2t2 is effectively suppressed, indicated by the "x" symbol. (b) The equivalent circuit of the current paths. (c) Experimental demonstration of the ASM-two-2AND gate between D_{1t1} and D_{2t1} when the non-selected memristors, D2t1 and D2t2, are initialized to R_{on} .

Supplementary Section IV. Energy band diagram model

In order to construct the energy band diagram model, the simulation model is constructed based on the conduction mechanism of the self-rectifying memristors. The trap level is found to be 1.69 eV measured from the Poole-Frenkel fitting at the high positive voltage in R + off state, as shown in Figure S4-1.



Figure S4-1. Poole-Frenkel fitting of the self-rectifying memristor.



Figure S4-2. Simulation fitting model of the self-rectifying memristors.

The simulation model based on the experimental results can be constructed to calculate the applied bias to the memristor in the two anti-serial memristors using Verilog-A model, as shown in Figure S4-2. Accordingly, the voltage drops over each memristor for ASM-two-2AND and ASM-two-2OR gates can be simulated, as shown in Table S4-1. Then, the electric field applied to HfO_2 and Ta_2O_5 can be found from the boundary equations.

Table S4-1. The voltage drops over each memristor when the conditional bias for the logic gates is applied.

ASM-two- 2AND	V _{D1t1} [V]	V _{D1t2} [V]	ASM-two- 2OR	V _{D1t1} [V]	V _{D2t1} [V]
Input case I	3.503	5.497	Input case I	6.351	5.650
Input case II	5.394	3.606	Input case II	7.852	4.148
Input case III	2.705	6.295	Input case III	5.459	6.541
Input case IV	3.103	5.897	Input case IV	8.430	3.570

1) Considering the work functions of Pt and TiN, the sum of the voltage across HfO₂ and Ta₂O₅ equals to applied voltage.

$$V_a = V_{HfO_2} + V_{Ta_2O_5} = d_{HfO_2}E_{HfO_2} + d_{Ta_2O_5}E_{Ta_2O_5} + \phi_{Pt} - \phi_{TiN}$$
(S4-1)

2) Electric flux is assumed to be constant that there is no surface charge at the interface between HfO_2 and Ta_2O_5 .

$$D_{HfO_2} = D_{Ta_2O_5}, E_{HfO_2} \varepsilon_{HfO_2} = E_{Ta_2O_5} \varepsilon_{Ta_2O_5}$$
(S4-2)

Solving the two equations yield the electric field applied to HfO2 and Ta2O5.

$$d_{Hf0_2}E_{Hf0_2} + d_{Ta_20_5}E_{Ta_20_5} = V_a - (\phi_{Pt} - \phi_{TiN})$$
(S4-3)

$$-\varepsilon_{HfO_2}E_{HfO_2} + \varepsilon_{Ta_2O_5}E_{Ta_2O_5} = 0$$
(S4-4)

$$\begin{bmatrix} d_{HfO_2} & d_{Ta_2O_5} \\ -\varepsilon_{HfO_2} & \varepsilon_{Ta_2O_5} \end{bmatrix} \begin{bmatrix} E_{HfO_2} \\ E_{Ta_2O_5} \end{bmatrix} = \begin{bmatrix} V_a - (\phi_{Pt} - \phi_{TiN}) \end{bmatrix} \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$
(S4-5)

$$\begin{bmatrix} E_{HfO_2} \\ E_{Ta_2O_5} \end{bmatrix} = \frac{V_a - (\phi_{Pt} - \phi_{TiN})}{\varepsilon_{Ta_2O_5} d_{HfO_2} + \varepsilon_{HfO_2} d_{Ta_2O_5}} \begin{bmatrix} \varepsilon_{Ta_2O_5} & -d_{Ta_2O_5} \\ \varepsilon_{HfO_2} & d_{HfO_2} \end{bmatrix} \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$
$$= \frac{\frac{V_a - (\phi_{Pt} - \phi_{TiN})}{\varepsilon_{Ta_2O_5} \varepsilon_{HfO_2}}}{\frac{d_{Ta_2O_5}}{\varepsilon_{Ta_2O_5}} + \frac{d_{HfO_2}}{\varepsilon_{HfO_2}}} \begin{bmatrix} \varepsilon_{Ta_2O_5} & -d_{Ta_2O_5} \\ \varepsilon_{HfO_2} & d_{HfO_2} \end{bmatrix} \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$
(S4-6)
$$V_a - (\phi_{Pt} - \phi_{TiN}) \qquad V_a - (\phi_{Pt} - \phi_{TiN})$$

$$E_{HfO_2} = \frac{\varepsilon_{HfO_2}}{\frac{d_{Ta_2O_5}}{\varepsilon_{Ta_2O_5}} + \frac{d_{HfO_2}}{\varepsilon_{HfO_2}}}, \ E_{Ta_2O_5} = \frac{\varepsilon_{Ta_2O_5}}{\frac{d_{Ta_2O_5}}{\varepsilon_{Ta_2O_5}} + \frac{d_{HfO_2}}{\varepsilon_{HfO_2}}}$$
(S4-7)

As a result, the band diagram can be drawn using the electric field and material parameters from the reference in Table S4-2.

Table S4-2. Summarized material parameters for Ta₂O₅, HfO₂, Pt, and TiN used in the energy band diagram model.

Material Parameters			
<i>Х</i> та₂0₅	3.2 eV		
$\chi_{\rm HfO_2}$	2.2 eV		
$\phi_{ m TiN}$	4.52 eV		
$\phi_{ m Pt}$	5.65 eV		
$d_{\mathrm{Ta}_{2}\mathrm{O}_{5}}$	5 nm		
d_{HfO_2}	10 nm		
Eg,Ta ₂ O ₅	4.4 eV		
Eg,HfO ₂	5.7 eV		