

Supplementary Information

Synthesis of large area ReS₂ thin film by CVD for in-depth investigation of resistive switching: effect of metal electrode, channel width and noise behaviour

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Section S1

XPS Fitting

X-ray photoelectron spectroscopy (XPS) was performed using the PHI Versa probe III system with a monochromatic X-ray source of Al K α ($h\nu = 1486.6$ eV). XPS data was obtained over a $500 \times 500 \mu\text{m}^2$ area and with an analyser pass energy of 20 eV. All the XPS spectra were fitted in CasaXPS software after subtraction of a Shirley background. The spectra have been calibrated for the charging effect with respect to C 1s peak at 284.5 eV, obtained from small amount adventitious carbon present over the film. Gaussian-Lorentzian peak shapes with a 70/30 mix were used for all peaks. Spin-orbit splitting for the Re 4f core level is set to 2.43 eV and for the S 2p core level to 1.18 eV. An area constraint of 4:3 is used for all the peaks corresponding to Re-4f_{7/2}:Re-4f_{5/2}. Atomic ratio, Re:S is calculated from the Re-4f and S-2p peaks corresponding to ReS₂ in the high resolution scan of Re-4f and S-2p peaks.

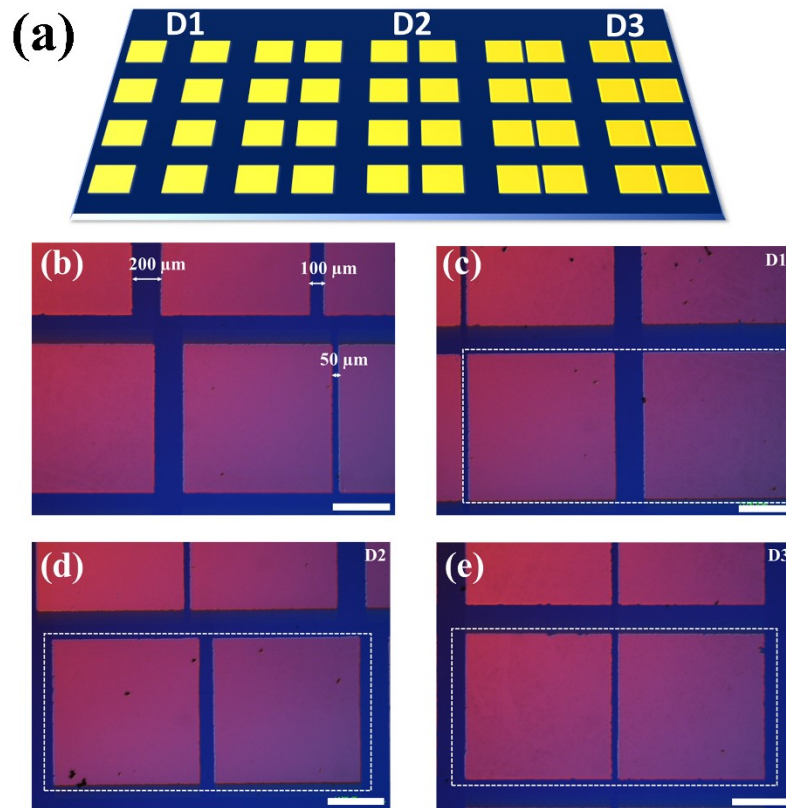


Figure S1

Fig. S1 (a) Schematic of the device architecture. (b) Optical microscopic image depicting overview of the device. It shows presence of devices with 200, 100, and 50 μm channel width all together. Optical microscopic image of (c) D1, (d) D2, and (e) D3. The scale provided in each figure is 400 μm .

Figure S2

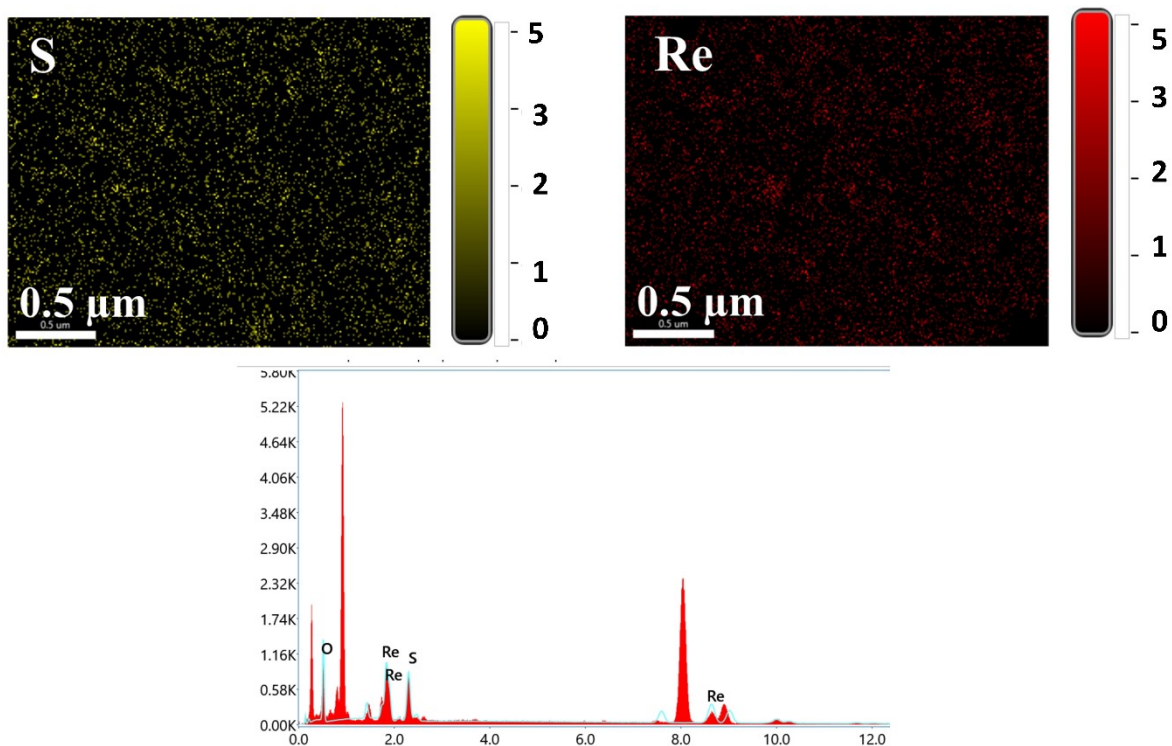


Fig. S2 EDAX spectra of the as-grown ReS₂ film showing peak signals from Re and S and depicting the atomic ratio, Re:S equal to 1:1.9, thereby indicating the formation of stoichiometric film.

Figure S3

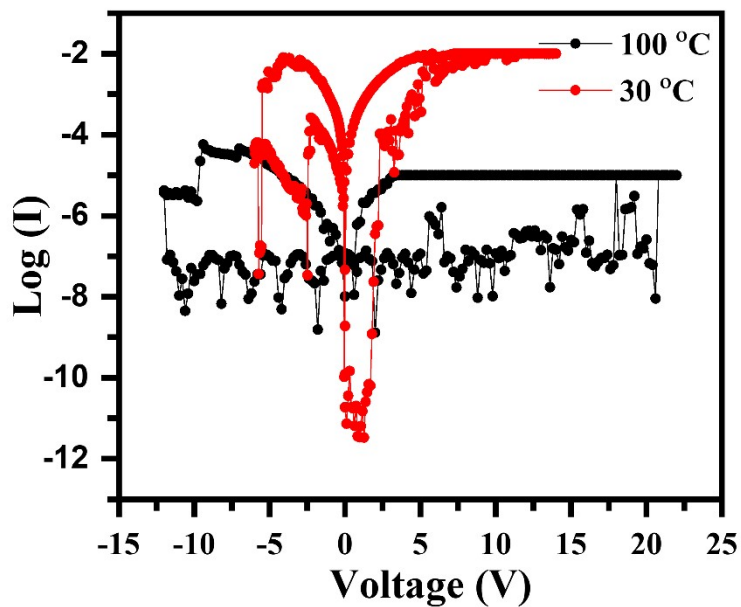


Fig. S3 Resistive switching cycle for device D2 at 30 and 100 °C. A change in HRS/LRS resistance ratio from 10^8 to 10^3 is observed which has been attributed to the weakening of Ag filament followed by which the current is dominated by defect mediated charge transport.

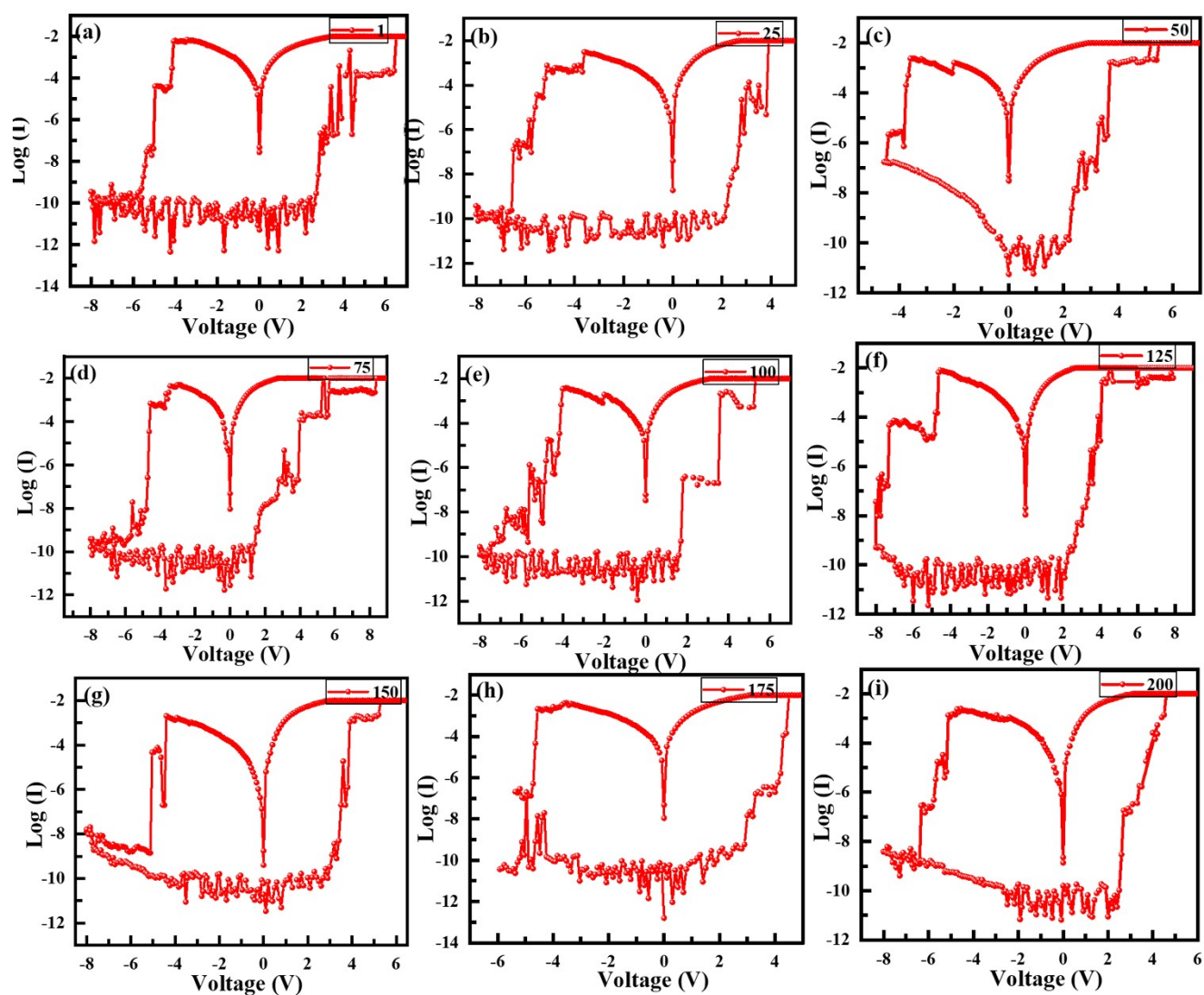


Figure S4

Fig. S4 RS cycles for D1 at the interval of 25 cycles. (a) 1st cycle, (b) 25th cycle, (c) 50th cycle, (d) 75th cycle, (e) 100th cycle, (f) 125th cycle, (g) 150th cycle, (h) 175th cycle, and (i) 200th cycle.

Figure S5

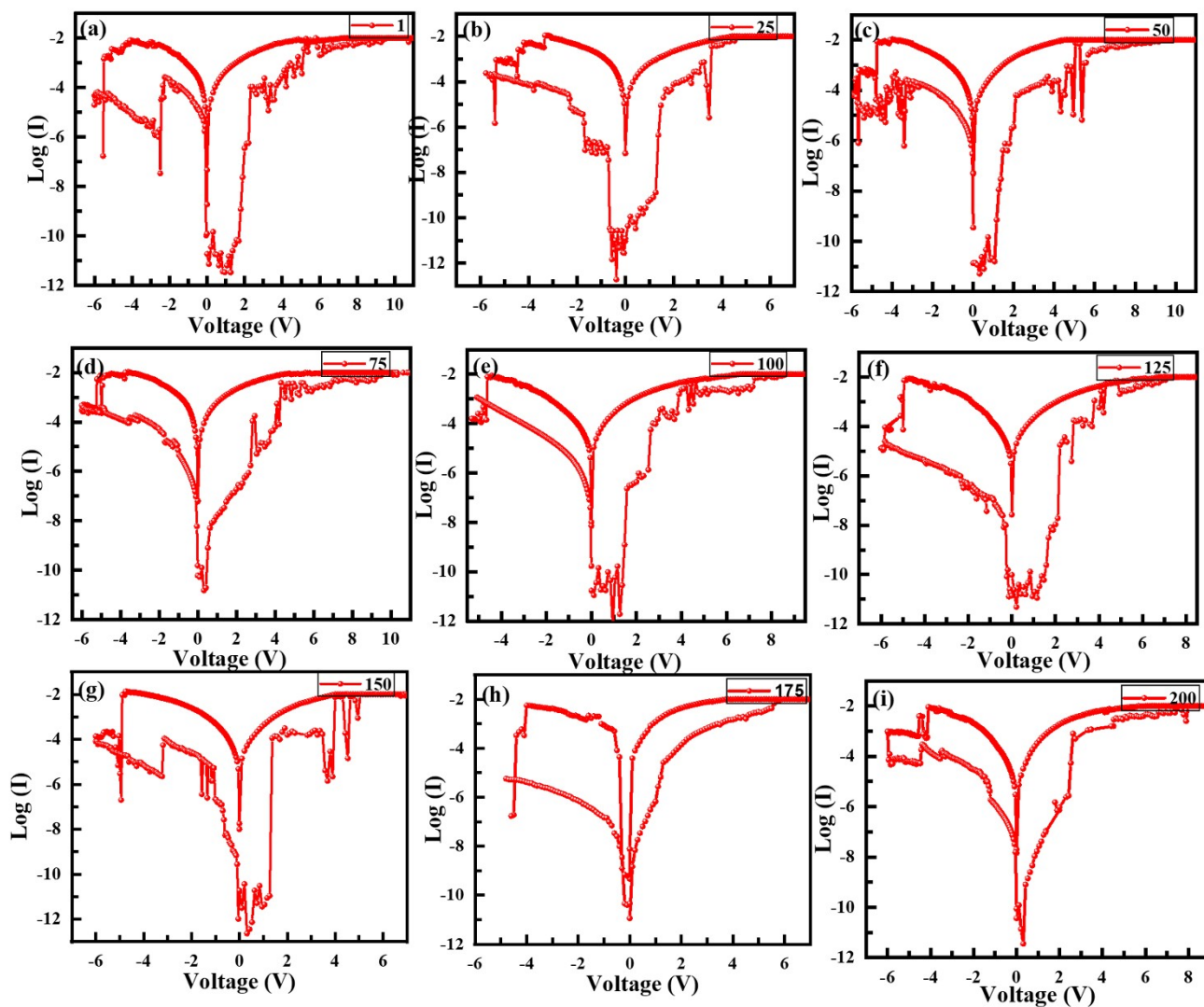


Fig. S5 RS cycles for D2 at the interval of 25 cycles. (a) 1st cycle, (b) 25th cycle, (c) 50th cycle, (d) 75th cycle, (e) 100th cycle, (f) 125th cycle, (g) 150th cycle, (h) 175th cycle, and (i) 200th cycle.

Figure S6

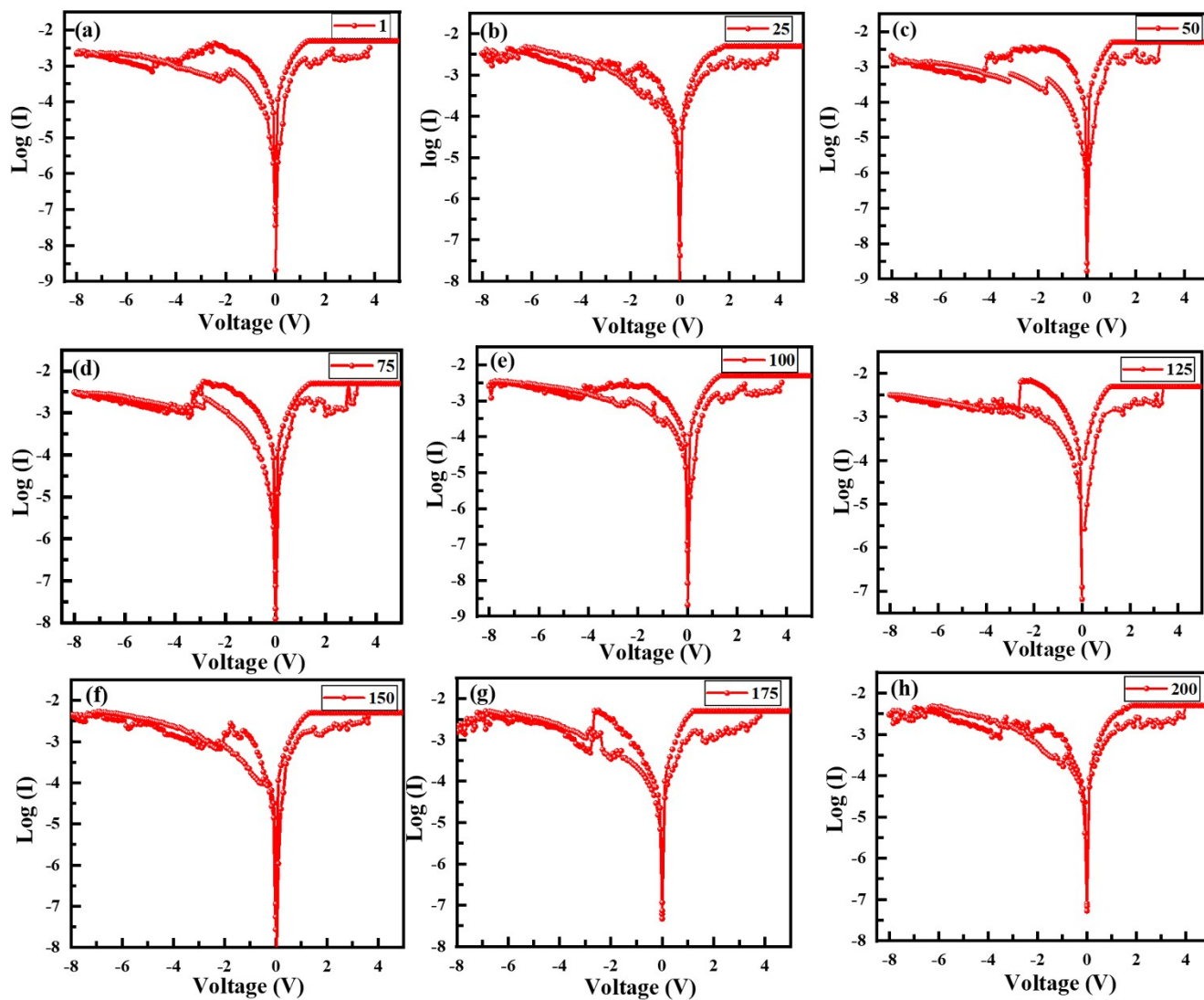


Fig. S6 RS cycles for D3 at the interval of 25 cycles. (a) 1st cycle, (b) 25th cycle, (c) 50th cycle, (d) 75th cycle, (e) 100th cycle, (f) 125th cycle, (g) 150th cycle, (h) 175th cycle, and (i) 200th cycle.

Figure S7

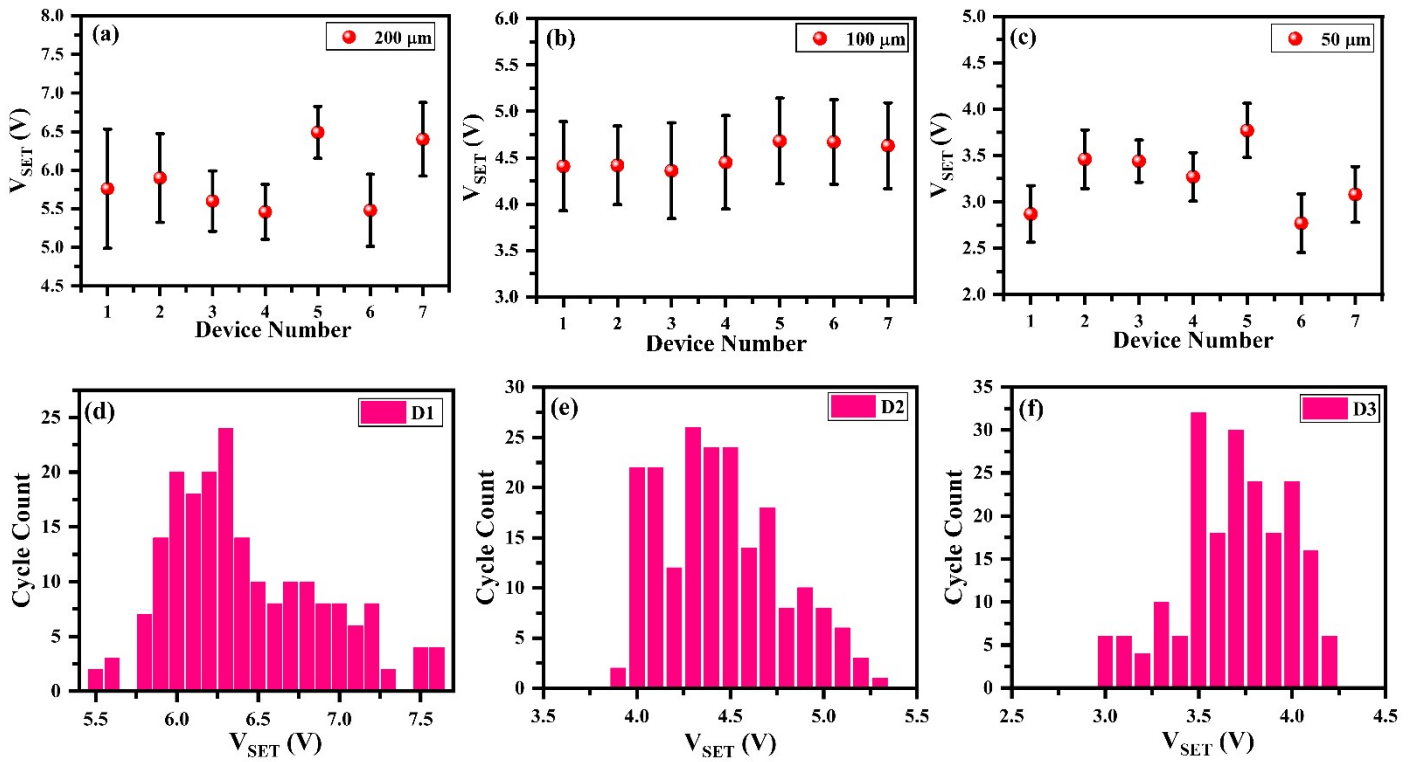


Fig. S7 Device statistics depicting average of the SET voltage, V_{SET} , for the tested seven devices with channel length of (a) 200 μm , (b) 100 μm , and (c) 50 μm . Plots clearly indicate increase in V_{SET} with channel width, which matches with the results reported in the manuscript. Histogram plot representing distribution of V_{SET} over 200 cycles for (d) D1, (e) D2, and (f) D3. V_{SET} is 6.55 ± 0.63 V for D1, 4.6 ± 0.5 V for D2, and 3.6 ± 0.3 V. This small value of standard deviation represents device repeatability and stability over multiple cycles.

Table T1

Table T1. Comparison table containing reported R_{HRS}/R_{LRS} resistance ratio of memristors fabricated on 2DMs.

S.No	Contact Metal	Material	Geometry	R_{HRS}/R_{LRS}	Ref
1	Ti/Au	ReS ₂	Lateral	50	1
2	Ti/Au	ReS ₂	Vertical	10 ⁶	2
3	Au	ReS ₂	Lateral	10 ⁵	3
4	Au Ag/Au	MoS ₂	Vertical	- 10 ²	4
5	Cu/ReSe ₂ /graphene	ReS ₂	Lateral	10 ⁵	5
6	Ag, Cu	WS ₂	Vertical	4.2 × 10 ⁴	6
7	Al	MoS ₂ -MoO _x	Vertical	10 ⁸	7
8	Au, Cr/Au	hBN	Vertical	10 ⁷	8
9	Ag/Au Pt/Au	ReS ₂	Lateral	10 ⁸ 10 ²	This Work

Reference

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