

Supplementary information

High-Performance van der Waals stacked transistors based on ultrathin GaPS₄ dielectrics

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1. XRD spectra of GaPS_4 powders.

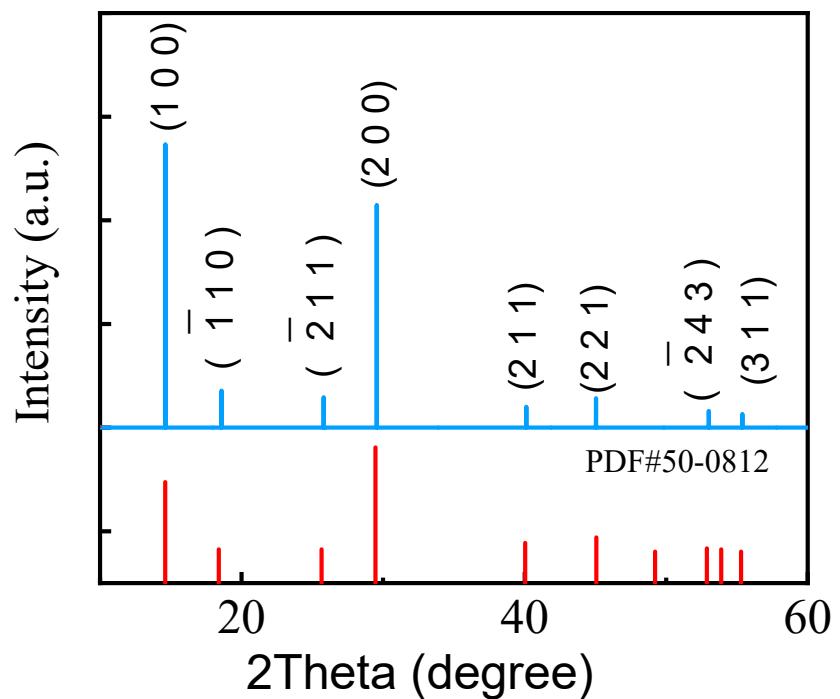


Fig. S1. XRD spectra for GaPS_4 powders in the blue line, and in comparison with the standard PDF card in the red line. The card number is (PDF#50-8012)

2. We use EDS to analyze the quality of GaPS₄ single crystal grown by CVT. As shown in the figure, we can see that the proportion of Ga, P, and S elements is close to 1:1:4, indicating that the quality of our singer crystal is very well.

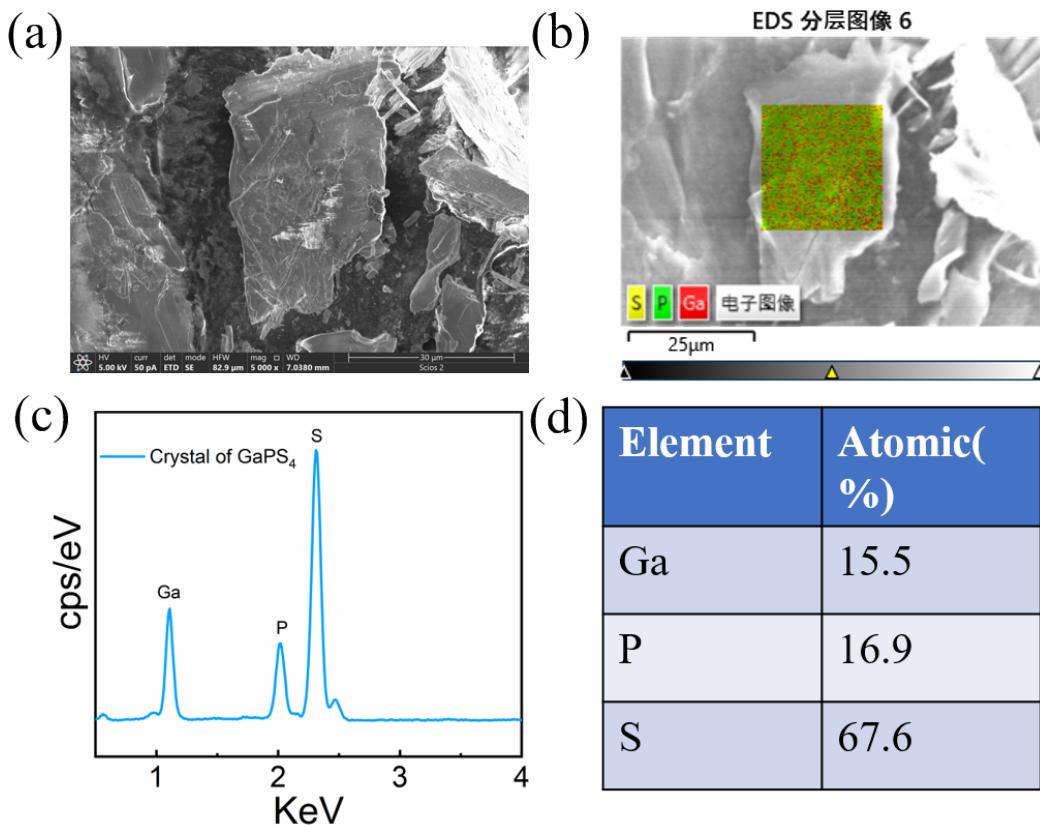


Fig. S2. EDS spectrum of GaPS₄

3. Ultraviolet absorption Spectra of GaPS₄ thin Films with different thicknesses.

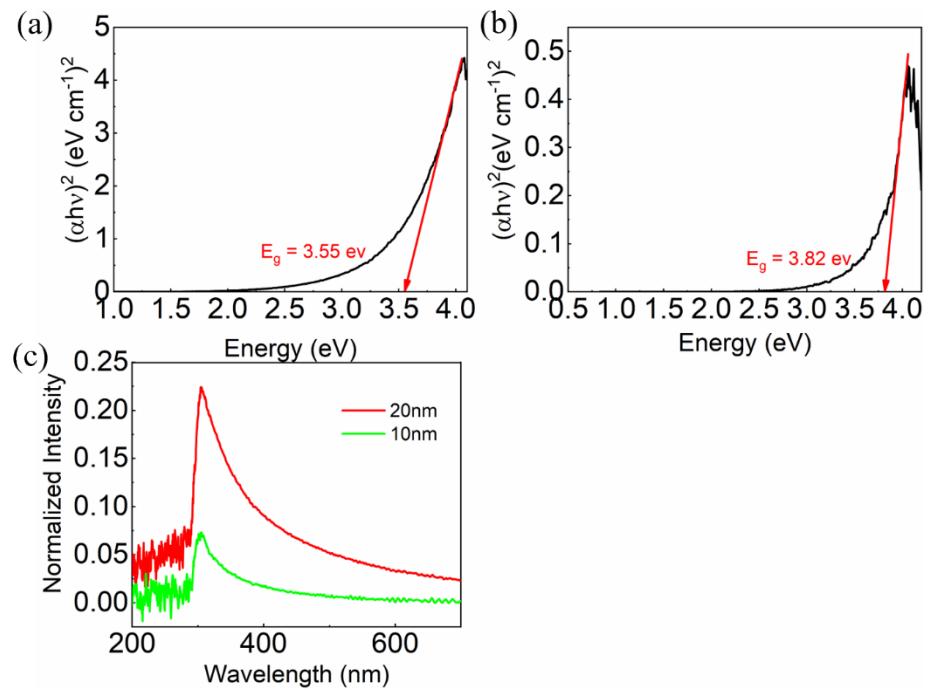


Fig. S3. (a) and (b) The fittings to these spectra to extract the optical band gap. (c) Absorption spectra of GaPS₄ flakes with different thicknesses.

4. UPS measurement of GaPS₄ and band misalignment between GaPS₄ and MoS₂.

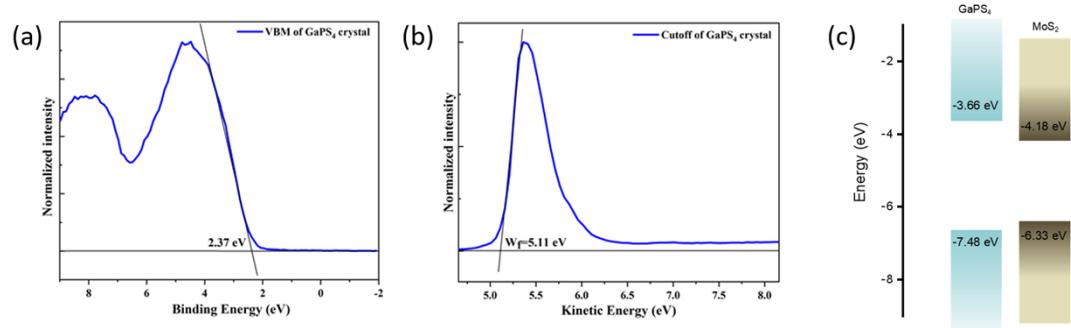


Fig. S4 (a) VBM of GaPS₄ crystal. (b) Work function of GaPS₄ crystal. (c) Band alignment between GaPS₄ and few-layer MoS₂.

5. The performance of different GaPS₄ gated MoS₂ FETs.

To investigate the thickness-dependent effect, we fabricated four MoS₂/GaPS₄ transistors with different thicknesses. Then, we display the device with better performance in Figure 3, and the characterization of other additional devices is shown in Figure S4 of the supplementary information. The comparison of the extracted parameters is shown in Table S1. The results we obtained show that FETs assembled from MoS₂/GaPS₄ with different thicknesses have similar gate responses, and the mobility and threshold voltage of different devices are different. This may be due to the influence of electrode contacts such as metal (Ti/Ni) or the quality of MoS₂ channels. Therefore, the thickness effect is not an important factor affecting device performance.

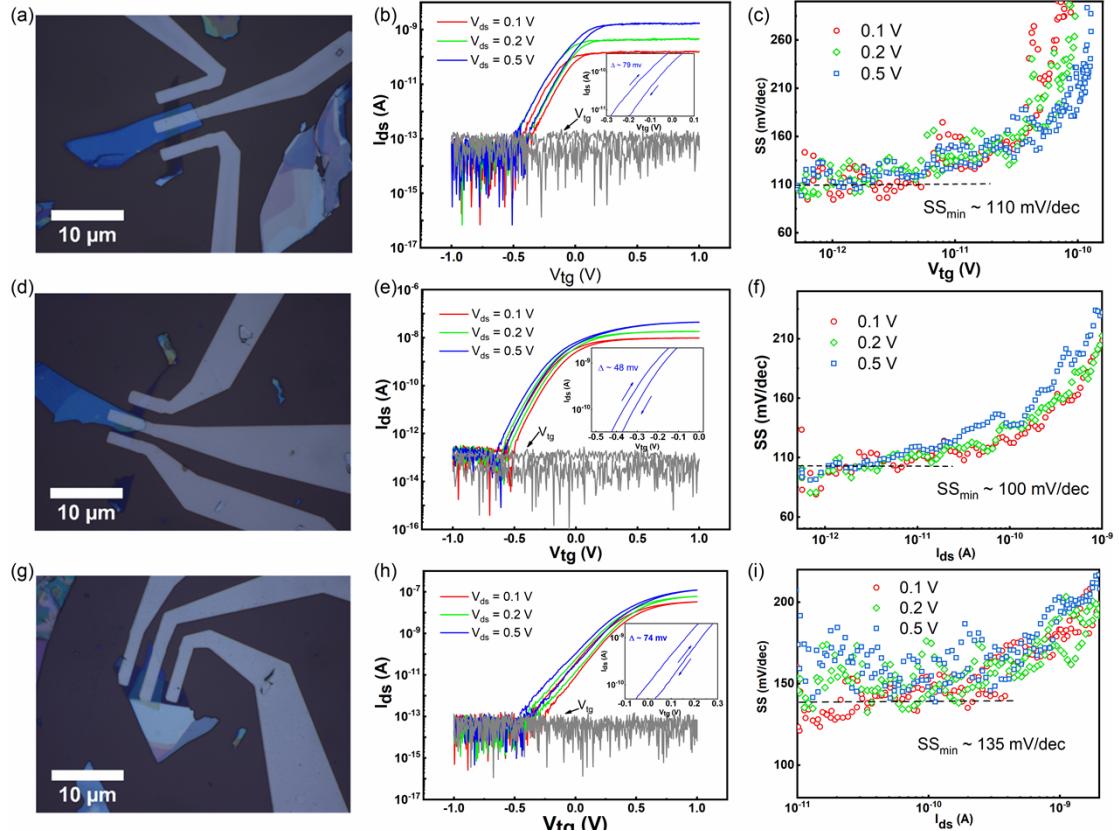


Fig. S5 Optical images, gate transfer curves, and SS responses for an additional three GaPS₄ gated MoS₂ FET transistors. (a)-(c) Optical image, gate transfer curves, and SS responses for device 2. (d)-(f) Optical image, gate transfer curves, and SS responses for device 3. (g)-(i) Optical image, gate transfer curves and SS responses for device 4.

6. The performance of GaPS₄ gated MoS₂ FETs.

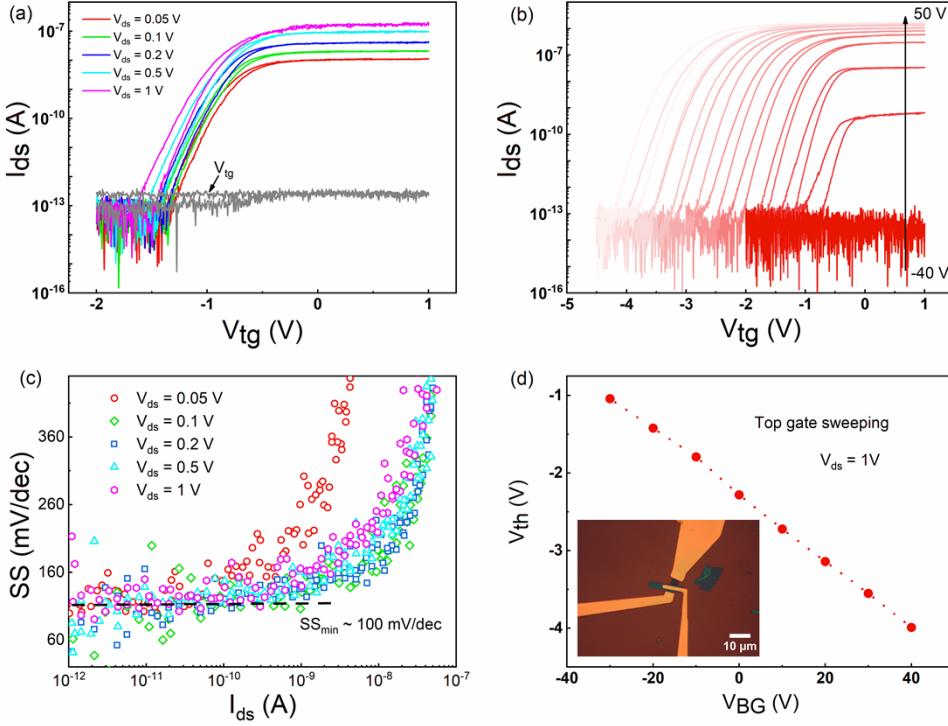


Fig. S6 The performance of FET (a) Typical dual-sweep transfer curves of the MoS₂/GaPS₄ measured under different V_{ds} from 0.05 to 1 V. (b) Dual gated transfer curves of the MoS₂/GaPS₄ FET under different back-gate voltages. (c) Extracted SS value versus I_{ds} characteristics of the device in a, showing a low SS value (< 70 mV/decade) for a wide I_{ds} range. (d) The extracted threshold voltage V_{th} from b is a function of V_{BG} .

Table S1. Extracted parameters for the performance of MoS₂ FETs using GaPS₄ as a top gate dielectric.

Device No.	Thickness of MoS ₂ (nm)	Thickness of GaPS ₄ (nm)	On/Off Ratio	Mobility ($cm^2/V\cdot s$)	Threshold voltage (V)	SS (mv/dec)
1(Figure 3)	7	22	10^7	14.2	-0.78	80
2	10	48	10^4	16.67	-0.46	100
3	4	20	10^5	8.78	-0.59	110
4	9	30	10^6	14.68	-0.27	135

7. Dielectric characteristics of GaPS₄

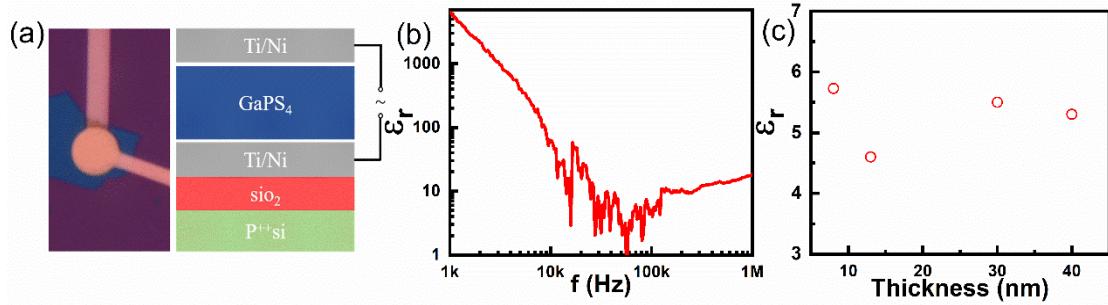


Fig. S7 (a) Optical image and schematic diagram of micro-sized GaPS₄ capacitor used for dielectric measurements, with a metal/GaPS₄/metal sandwich structure. (b) Extracted dielectric constant as a function of frequency from 1 kHz to 1000 kHz for a 40 nm thick flake. (c) Dielectric constant measured at 40 kHz for different thicknesses of flakes.

Table S2. Comparison of the dielectric properties and the corresponding FET performance of GaPS₄ with other 2D dielectric materials

Dielectric	E _g	ϵ_r	FET channel	I_{on}/I_{off}	SS	refs
SnP ₂ S ₆	2.23	23	MoS ₂	10^7	69.4	1
CaF ₂	12.1	8.4	MoS ₂	10^6	91	2
h-BN	6	3.9	MoS ₂	10^6	80	3
SiP ₂	2.14	8.1	MoS ₂	10^5	-	4
GaInS ₃	2.91	12	MoS ₂	10^7	71.2	5
LaOBr	5.3	8	MoS ₂	10^8	85	6
MnAl ₂ S ₄	3.65	6.1	MoS ₂	10^7	80	7
Sr ₂ Nb ₃ O ₁₀	3.8	24.6	MoS ₂	10^6	88	8
In ₂ P ₃ S ₉	3.5	24	MoS ₂	10^5	88	9
GaPS ₄	3.55	5.3	MoS ₂	10^7	80	This work

Reference

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