## **Supporting Information**

## Defect-Level Trap optimization in Cu<sub>2</sub>ZnSn(S,Se)<sub>4</sub> Photovoltaic Materials via Sb<sup>3+</sup>-Doping for over 13% Efficient Solar Cell

Yingfen Li,<sup>a</sup> Xingye Chen,<sup>\*b</sup> Runxi Wang,<sup>a</sup> Nian Zhou,<sup>a</sup> Fang Huang,<sup>a</sup> Jun Zhao,<sup>\*b</sup> Zhenghua Su,<sup>b</sup> Shuo Chen,<sup>b</sup> and Guangxing Liang<sup>\*b</sup>

Layer properties	CZTSSe	CdS	ΙΤΟ
thickness (nm)	1700	30	500
$E_{g}(eV)$	1.14	2.4	3.65
χ (eV)	4.05	4.27	4.8
$N_{\rm C}~({\rm cm}^{-3})$	$2.2 \times 10^{18}$	2.3×10 <sup>18</sup>	5.2×10 <sup>18</sup>
$N_{\rm V}~({\rm cm}^{-3})$	1.8×10 <sup>19</sup>	$1.8 \times 10^{19}$	$1.0 \times 10^{18}$
$v_{\rm e}$ (cm/s)	$1.0 \times 10^{7}$	2.6×10 <sup>7</sup>	2.0×10 <sup>7</sup>
$v_{\rm h}$ (cm/s)	$1.0 \times 10^{7}$	$1.3 \times 10^{7}$	$1.0 \times 10^{7}$
$\mu_{\rm e} ({\rm cm^2/Vs})$	20	340	10
$\mu_{ m h}({ m cm}^2/{ m Vs})$	25	10	10
Doping (cm <sup>-3</sup> )	1×10 <sup>15</sup> (a)	$1 \times 10^{15}$ (d)	1×10 <sup>20</sup> (d)

Table S1 Parameters of CZTSSe, CdS, and ITO layers.



Figure S1 Cross-section SEM images of (a-c) Refence and (d-f) Sb-doped thin film

during different selenization times (0, 100 and 900 seconds).



**Figure S2** The element distribution of the thin films prepared by (a) one-step and (b) two-step methods was scanned by EDS line scans.



**Figure S3** The TRPL decay curves of (a) Reference and (b) Sb-doped thin films during one-step and two-step selenization.



Figure S4 Statistical photovoltaic performance data for the CZTSSe-based devices, including  $V_{\text{OC}}$ ,  $J_{\text{SC}}$ , FF, and PCE.



**Figure S5** XPS spectra of Cu 2p, Zn 2p, S 2p and Se 3d peaks with the reference and Sb-doped films.



**Figure S6** The AS spectra and Arrhenius plot with the (a-b) Reference and (c-d) Sbdoped films.



Figure S7 The magnified UPS spectra region and energy band diagram with (a-b) reference and (c-d) Sb-doped samples.



Figure S8 (a) Schematic view of illumination position in the simulation model device,

(c) Equivalent electrical network of a solar cell under illumination.



Figure S9 Device performance versus (a-c) CBO, interface defect and (d-f) bulk defect

for simulated CZTSSe devices.



**Figure S10** Device equivalent circuit diagram and the EIS plots with the (a) Reference and (b) Sb-doped films.