

Supporting Information:

A Facile Room Temperature Iodination Route to in-situ Fabrication of Patterned Copper-Iodide/Silicon Quasi-Bulk-Heterojunction Thin Films for Photovoltaic Application

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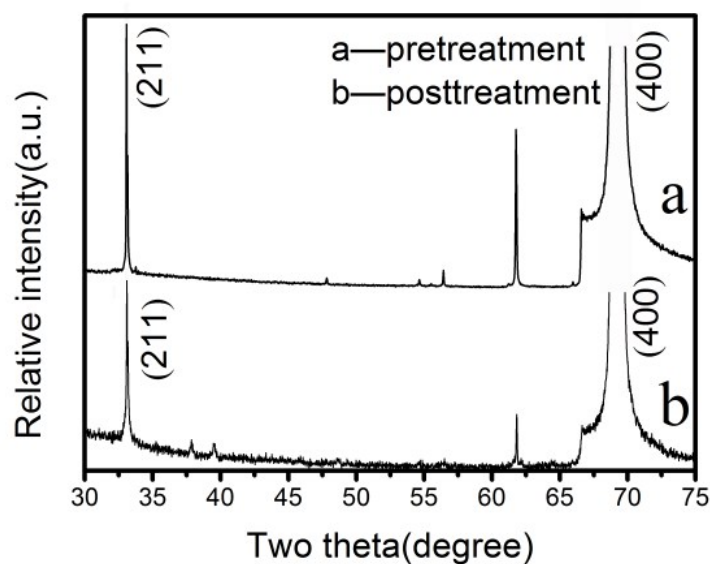


Figure S1. XRD patterns of virgin and pretreated Si substrate.

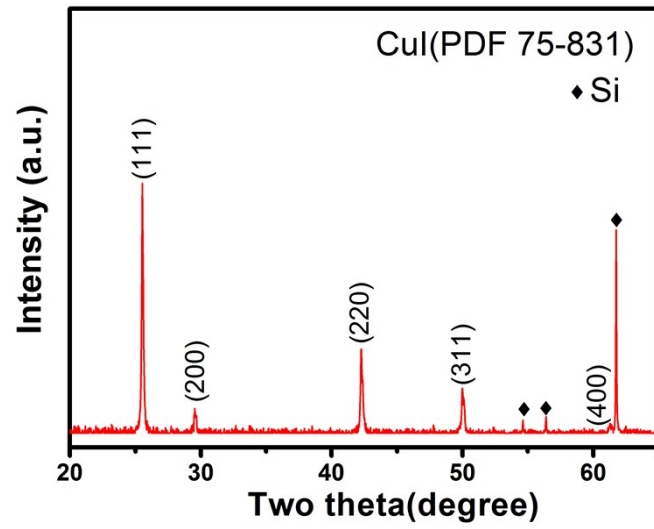


Figure S2. XRD pattern of CuI/Si-100 (CuI transformed from 100nm Cu).

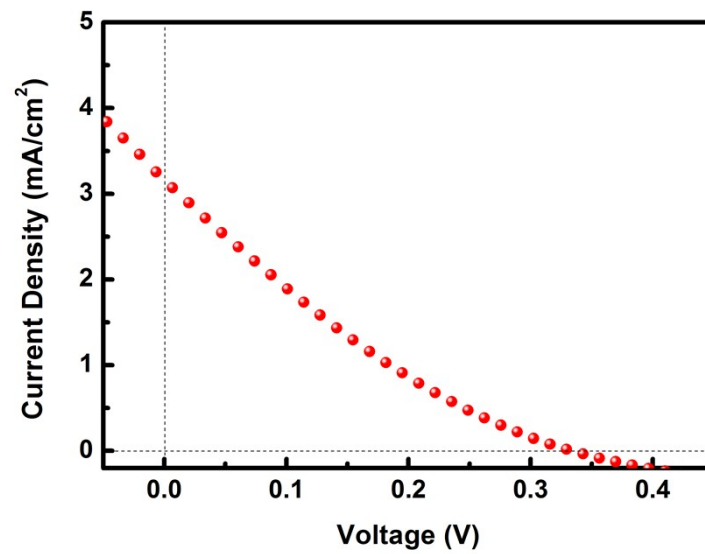


Figure S3. I-V curve of the CuI/Si solar cell device under AM 1.5G simulated sun light (100 mW/cm²)