

Supplementary information :

## Local silicon doping as a promoter of patterned electrografting of diazonium for directed surface functionalization.

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### Cleaning process description for the locally doped silicon wafers.

The implanted pattern is revealed by atomic force microscopy (AFM) imaging where doped areas appear lower than undoped ones, with an average height difference of 7 nm. This topography is due to the manufacturing process. After implantation and before annealing, the wafers underwent various cleaning treatment:

- oxygen plasma
- H<sub>2</sub>SO<sub>4</sub>/H<sub>2</sub>O<sub>2</sub>, 80°C, 10 minutes
- HCl/H<sub>2</sub>O<sub>2</sub>/H<sub>2</sub>O, 80°C, 5 minutes to remove the resin used for the photolithography.

The oxide layer formed by this process was then etched by HF treatment (BE HF 10%). After activation (annealing at 1000°C for 30 minutes under N<sub>2</sub> atmosphere), the wafers were once again cleaned by HF treatment and then stored at laboratory atmosphere. It is known that the growth of the oxide layer is more efficient on the implanted area than on the intrinsic part,<sup>1-5</sup> the successive HF treatments lead to a deeper etching of the doped area than the intrinsic part.

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