page S1

ELECTRONIC SUPPLEMENTARY INFORMATION FOR

Microfabricated Thermal Modulator for Comprehensive Two-Dimensional Micro Gas Chromatography: Design, Thermal Modeling, and Preliminary Testing

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Figure S1. Fabrication process flow for the µTM. (A) The process was started with a silicon wafer of 4 inches in diameter and 500 µm in thickness. We thermally grew a 1.2-µm-thick oxide layer on the both sides to serve as a diffusion barrier of the boron doping. The backside was covered by photoresist and the frontside was photolithographically patterned to define the microchannels and the oxide was removed by dipping in buffered hydrofluoric acid (BHF). The exposed areas were doped with boron to a depth of 5 microns in a boron diffusion furnace. (B) A second thermal oxide layer was grown on both sides of the wafer as a diffusion barrier for the second boron doping. After separate patterning steps on each side of the wafer, the oxide was removed with BHF. (C) Microchannels (140 µm-deep and 250 µm-wide) and inlet-outlet ports (400 µm-deep and 400 µm-wide) were formed by two-step Si deep reactive etching (DRIE). (D) A second boron doping step was performed to protect the microchannels in the subsequent Si wet-etching process (step H) and the frontside oxide was removed (in BHF) in preparation for the anodic bonding process. (E) A glass wafer (Pyrex 7740, Sensors Prep Services, 100 µm thickness) was cleaned in sulfuric-acid solution and anodically bonded to the frontside of the wafer. (F) Patterned microheaters and temperature sensors (electronbeam evaporated Ti/Pt; 20 nm/100 nm) were then fabricated on the glass layer by a lift-off process in an acetone solution. (G) DRIE was used to define the channel areas at the backside of Si wafer. (H) The remaining Si around the microchannels was removed by a combination of DRIE and wet-etching with ethylene-diamine-pyrocatechol solution. Finally, the wafer was diced into individual chips.



Figure S2. Experimental setup and calibration curves. (A) Experimental setup for the temperature calibration and thermal measurements. (B and C) Temperature calibration of microheaters and temperature sensors. Electrical resistances of the microheaters (B) are several times lower than those of the temperature sensors (C). Resistances (*R*) of the heaters and the temperature sensors are linearly proportional to the temperature (*T*). That is, R = a + bT where *a* and *b* are constants that are determined by the linear curve fitting of each resistor.

page S3

Lumped Thermal Model

We developed a series of equations to describe the thermal response of each stage and the thermal crosstalk between the stages. To solve the ordinary differential equations, we used the ode45 solver in MATLAB (The MathWorks, www.mathworks.com). The equations were derived from a balance among the energy rates (unit: W) at three control volumes. Each control volume includes the thermal mass of the first stage, the second stage, and the rim, respectively (See Fig. 1C). The energy rates consist of the change of energy storage, conduction, convection, and radiation per unit time. We define the heat transfer as positive when the heat comes into the control volume. The resulting equations are coupled as follows:

$$\dot{E}_{1_sys} = \dot{E}_{1_TEC} + \dot{E}_{1_3} + \dot{E}_{1_2} + \dot{E}_{1_gen} + \dot{E}_{1_conv} + \dot{E}_{1_rad}$$
(1)

$$\dot{E}_{2_sys} = \dot{E}_{2_TEC} + \dot{E}_{2_3} + \dot{E}_{2_1} + \dot{E}_{2_gen} + \dot{E}_{2_conv} + \dot{E}_{2_rad}$$
(2)

$$\dot{E}_{3_sys} = \dot{E}_{3_TEC} + \dot{E}_{3_1} + \dot{E}_{3_2} + \dot{E}_{3_conv} + \dot{E}_{3_rad}$$
(3)

where the numbers (1, 2, and 3) in the subscript correspond to the first stage, the second stage, and the rim, respectively. \dot{E}_{i_sys} (i = 1, 2, 3) is the rate of energy storage for each thermal mass (the first stage, the second stage, and the rim) and is expressed as follows:

$$\dot{E}_{i_sys} = (\rho C_p V)_i \frac{dT_i}{dt}$$
(4)

$$(\rho C_{\rm p} V)_{\rm i} = (\rho C_{\rm p} V)_{\rm Si_i} + (\rho C_{\rm p} V)_{\rm Pyrex_i}$$
⁽⁵⁾

where ρ , C_p , and V are the density, the specific heat capacity, and the volume of each material (Si and Pyrex), respectively. T_i and t are the temperature of the ith thermal mass and time, respectively. \dot{E}_{i-TEC} is the conductive heattransfer between the ith thermal mass and the thermoelectric cooler (TEC):

$$\dot{E}_{i_\text{TEC}} = \frac{k_{\text{air}} A_{i_\text{TEC}}}{L_{\text{air}}} (T_i - T_{\text{TEC}})$$
(6)

where k_{air} is the thermal conductivity of air (0.0294 W m⁻¹K⁻¹). A_{i-TEC} , L_{air} , and T_{TEC} are the facing area of the ith thermal mass to the TEC, the air-gap, and the TEC temperature, respectively. \dot{E}_{i-j} (i, j = 1, 2, 3, but i \neq j) is the conductive heat transfer between thermal masses:

$$\dot{E}_{1-2} = -\frac{k_{\rm Si}A_{\rm IC}}{L_{\rm IC}}(T_1 - T_2) - \frac{k_{\rm Pyrex}A_{1-2_Memb}}{L_{\rm IC}}(T_1 - T_2)$$
(7)

page S4

$$\dot{E}_{1-3} = -\frac{k_{\rm Si}A_{\rm IC}}{L_{\rm IC}}(T_1 - T_3) - \frac{k_{\rm Pyrex}A_{1-3_MembIC}}{L_{\rm IC}}(T_1 - T_3) - \frac{2k_{\rm Pyrex}A_{1-3_Memb}}{L_{1-3_Memb}}(T_1 - T_3)$$
(8)

$$\dot{E}_{2-1} = -\dot{E}_{1-2} \tag{9}$$

$$\dot{E}_{2-3} = -\frac{k_{\rm Si}A_{\rm IC}}{L_{\rm IC}}(T_2 - T_3) - \frac{k_{\rm Pyrex}A_{2-3_MembIC}}{L_{\rm IC}}(T_2 - T_3) - \frac{2k_{\rm Pyrex}A_{2-3_Memb}}{L_{2-3_Memb}}(T_2 - T_3)$$
(10)

$$\dot{E}_{3-1} = -\dot{E}_{1-3} \tag{11}$$

$$\dot{E}_{3-2} = -\dot{E}_{2-3} \tag{12}$$

where k_{Si} and k_{Pyrex} are the thermal conductivity of Si and Pyrex (130 W m⁻¹K⁻¹ and 1.4 W m⁻¹K⁻¹), respectively. A_{IC} is the cross-sectional area of the Si interconnection channel (IC), and L_{IC} is the length of the IC. L_{1-3}_{Memb} and L_{2-3}_{Memb} are the distances between 1-3 and 2-3, respectively. A_{1-2}_{Memb} , A_{1-3}_{Memb} , and A_{2-3}_{Memb} are the cross-sectional areas of the Pyrex membrane between thermal mass 1and 2, 1and 3, and 2and 3; however they exclude the areas contacting the ICs. A_{1-3}_{MembIC} and A_{2-3}_{MembIC} are the cross-sectional areas of the Pyrex membrane between thermal masses 13 and 2-3, respetively, that are in contact with the ICs. $\dot{E}_{i_{gen}}$ is the heat generation by integrated microheaters:

$$\dot{E}_{i_gen} = V_i^2 / R_i \tag{13}$$

where V_i and R_i are the applied voltage and electrical resistance, respectively, at the ith microheater. \dot{E}_{i_conv} and \dot{E}_{i_rad} are the heat transfer by convection and radiation, respectively. We assume natural convection for the \dot{E}_{i_conv} term and also assume that the µTM is a small convex object in a large-cavity environment for the term \dot{E}_{i_rad} . They are expressed as follows:

$$\dot{E}_{i_conv} = -hA_{Si_i}(T_i - T_{\infty}) \tag{14}$$

$$\dot{E}_{i_rad} = -\sigma_{SB} \varepsilon_r A_{Si_i} (T_i^4 - T_{\infty}^4)$$
⁽¹⁵⁾

where h, σ_{SB} and ε_r are the natural convection coefficient (10 W m⁻²K⁻¹), Stephan-Boltzman constant (5.67×10⁻⁸ W m⁻²K⁻⁴) and the emissivity of the radiating Si surface (0.52), respectively. A_{Si_i} and T_{∞} are the area of ith thermal mass on the Si surface and ambient temperature, respectively.