

Sub-glass transition annealing enhances polymer solar cell performance

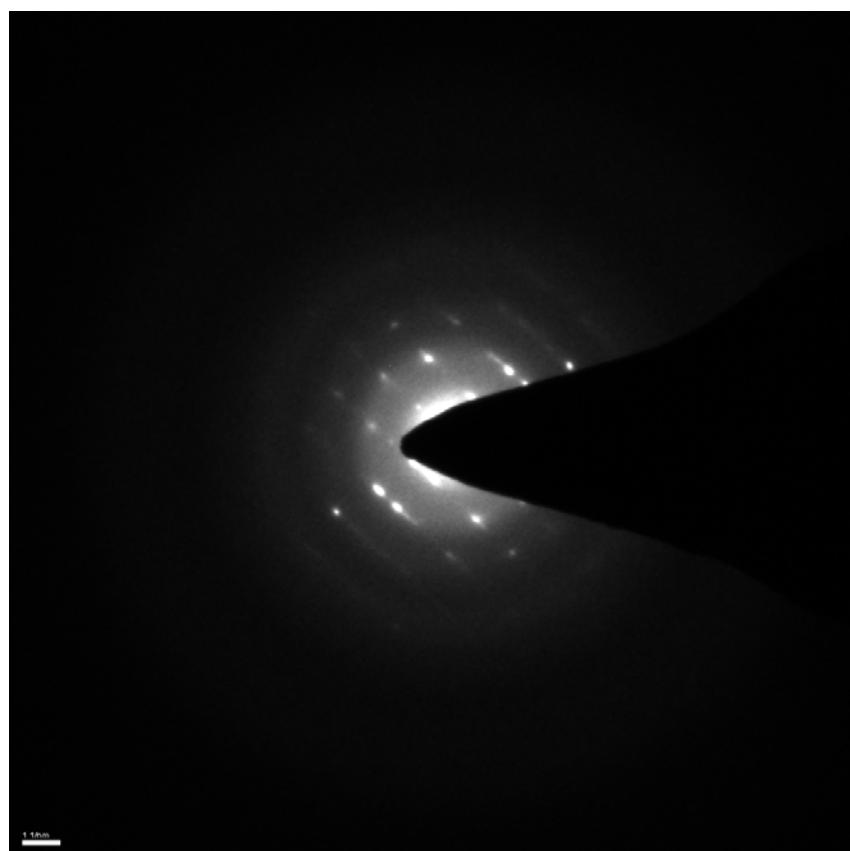
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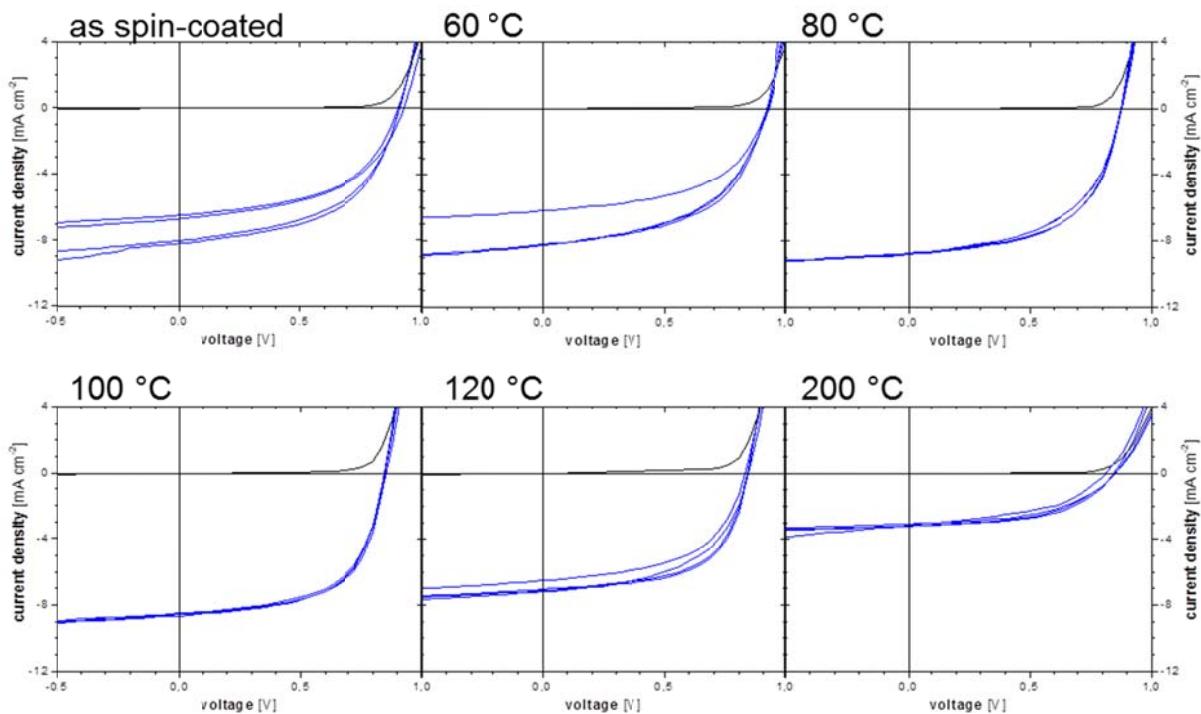
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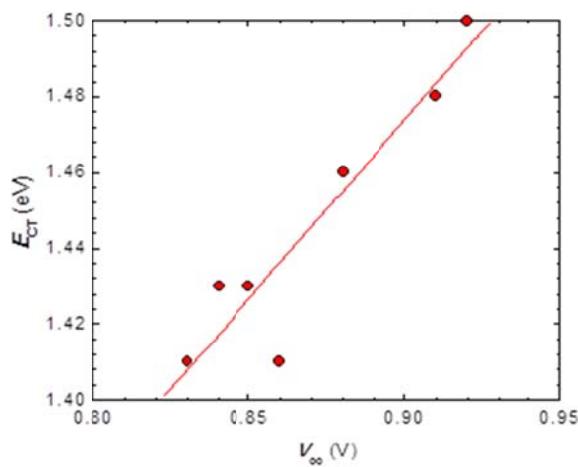
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SI Fig. 1 Electron diffraction pattern of a PCBM crystal grown at 120 °C.



SI Fig. 2 JV-curves of devices prepared by annealing the active layer at the indicated temperatures; device characteristics for four diodes placed on the same substrate are shown together with the dark current curve of one pixel.



SI Fig. 3 CT state energy as a function of V_{oc} measured for TQ1:PCBM devices with active layers annealed at $T_{\text{anneal}} \sim 20\text{-}200$ °C. The red line is a linear fit.