

Supplementary Information

Role of solution-processed high- κ gate dielectrics in electrical performances of oxide thin-film transistors

Wangying Xu, Han Wang, Lei Ye, Jianbin Xu*

Department of Electronic Engineering, The Chinese University of Hong Kong, Shatin, New Territories, Hong Kong

E-mail address: jbxu@ee.cuhk.edu.hk Fax: +852-2603-5558; Tel: +852-3943-8297

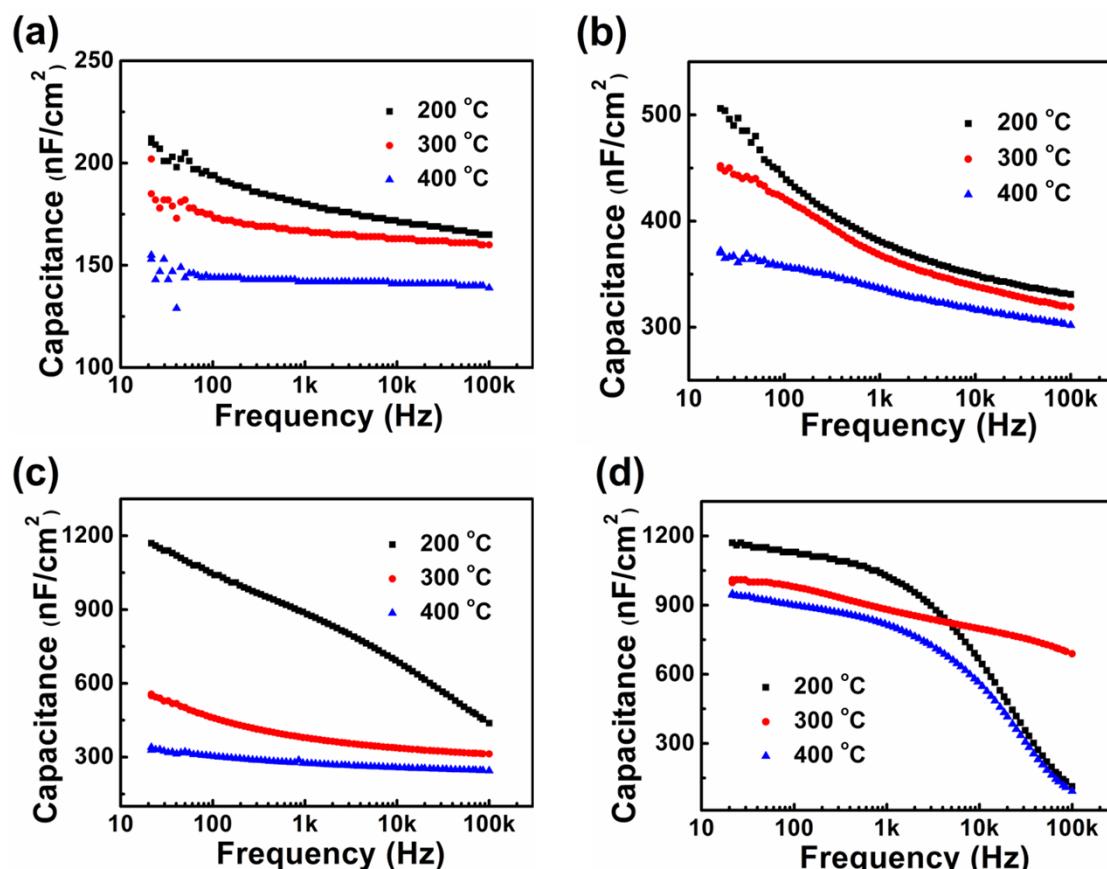


Figure S1. Capacitance vs. frequency plots for (a) AlO_x , (b) ZrO_x , (c) YO_x and (d) TiO_x dielectrics annealed at different temperatures.

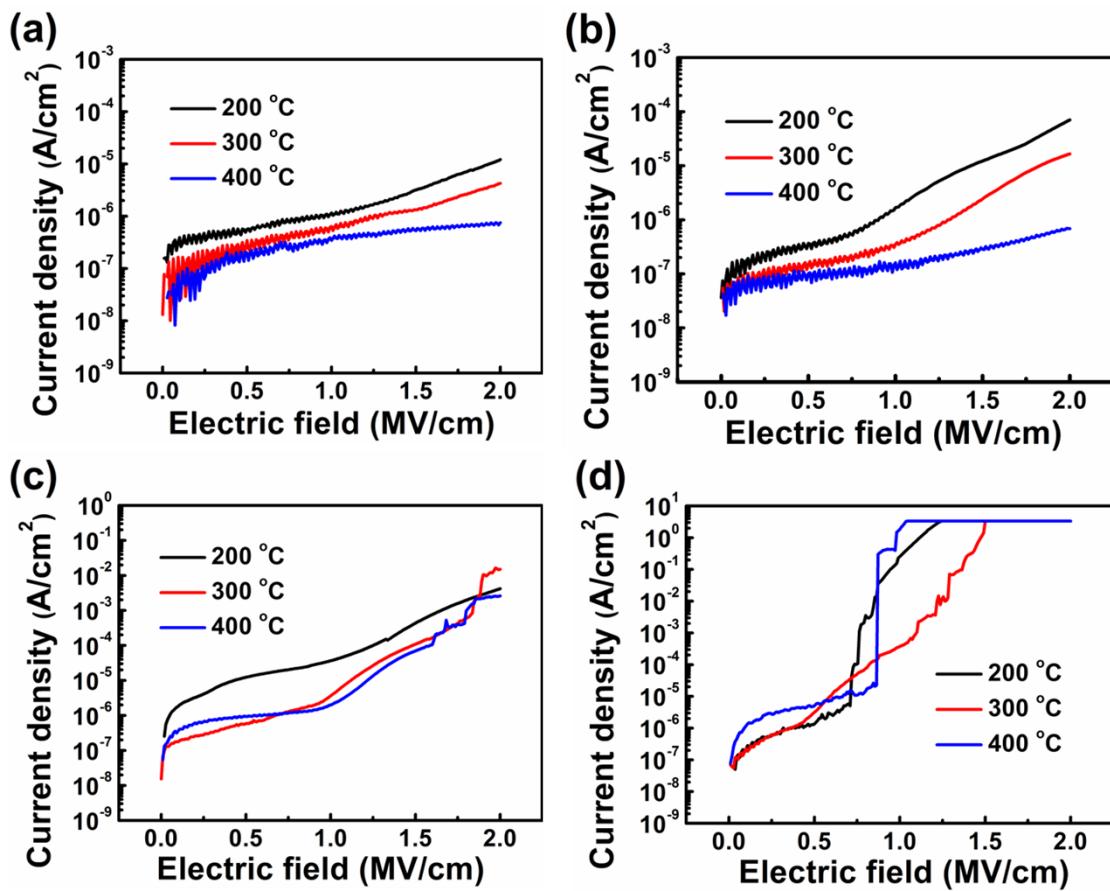


Figure S2. Leakage current density vs. electrical field plots for (a) AlO_x , (b) ZrO_x , (c) YO_x and (d) TiO_x dielectrics annealed at different temperatures.